



SPEC® CINT2006 Result

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Bull SAS

NovaScale T860 E1
(Intel Xeon E5430, 2.66 GHz)

SPECint®2006 = 24.1

SPECint_base2006 = 21.1

CPU2006 license: 20

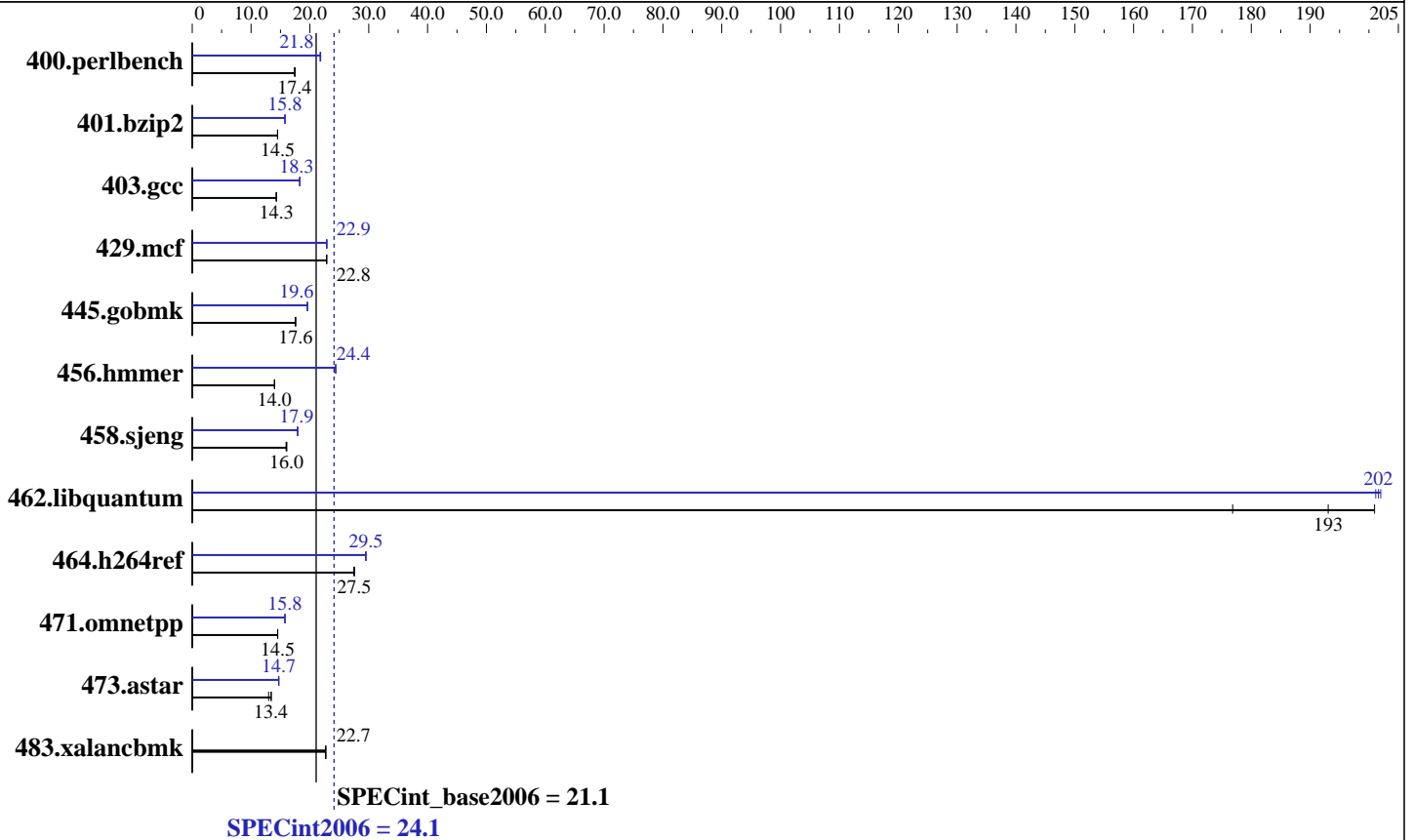
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Jul-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 SP1
 Kernel 2.6.16.46-0.12-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15
 SmartHeap library V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 559 | 17.5 | 562 | 17.4 | <u>561</u> | <u>17.4</u> | 450 | 21.7 | 447 | 21.8 | <u>449</u> | <u>21.8</u> |
| 401.bzip2 | <u>665</u> | <u>14.5</u> | 665 | 14.5 | 666 | 14.5 | <u>612</u> | <u>15.8</u> | 613 | 15.7 | 611 | 15.8 |
| 403.gcc | 563 | 14.3 | 564 | 14.3 | <u>563</u> | <u>14.3</u> | <u>440</u> | <u>18.3</u> | 440 | 18.3 | 441 | 18.2 |
| 429.mcf | 399 | 22.9 | <u>399</u> | <u>22.8</u> | 400 | 22.8 | 399 | 22.8 | <u>399</u> | <u>22.9</u> | 399 | 22.9 |
| 445.gobmk | 597 | 17.6 | 597 | 17.6 | <u>597</u> | <u>17.6</u> | 535 | 19.6 | <u>536</u> | <u>19.6</u> | 536 | 19.6 |
| 456.hmmer | <u>667</u> | <u>14.0</u> | 668 | 14.0 | 667 | 14.0 | 383 | 24.4 | 383 | 24.4 | <u>383</u> | <u>24.4</u> |
| 458.sjeng | 753 | 16.1 | 759 | 15.9 | <u>755</u> | <u>16.0</u> | 676 | 17.9 | 675 | 17.9 | <u>676</u> | <u>17.9</u> |
| 462.libquantum | 117 | 177 | 103 | 201 | <u>107</u> | <u>193</u> | 103 | 202 | <u>103</u> | <u>202</u> | 103 | 201 |
| 464.h264ref | 805 | 27.5 | <u>804</u> | <u>27.5</u> | 803 | 27.6 | 749 | 29.5 | 751 | 29.5 | <u>749</u> | <u>29.5</u> |
| 471.omnetpp | 431 | 14.5 | 430 | 14.5 | <u>430</u> | <u>14.5</u> | 396 | 15.8 | <u>397</u> | <u>15.8</u> | 397 | 15.7 |
| 473.astar | <u>524</u> | <u>13.4</u> | 522 | 13.4 | 541 | 13.0 | <u>477</u> | <u>14.7</u> | 478 | 14.7 | 477 | 14.7 |
| 483.xalancbmk | <u>304</u> | <u>22.7</u> | 304 | 22.7 | 305 | 22.6 | <u>304</u> | <u>22.7</u> | 304 | 22.7 | 305 | 22.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.html



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You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.xml

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For other inquiries, please contact webmaster@spec.org.

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