



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 107

ACTINA SOLAR 442 S3 (Intel Xeon X7350, 2.93 GHz)

SPECint_rate_base2006 = 96.2

CPU2006 license: 3388

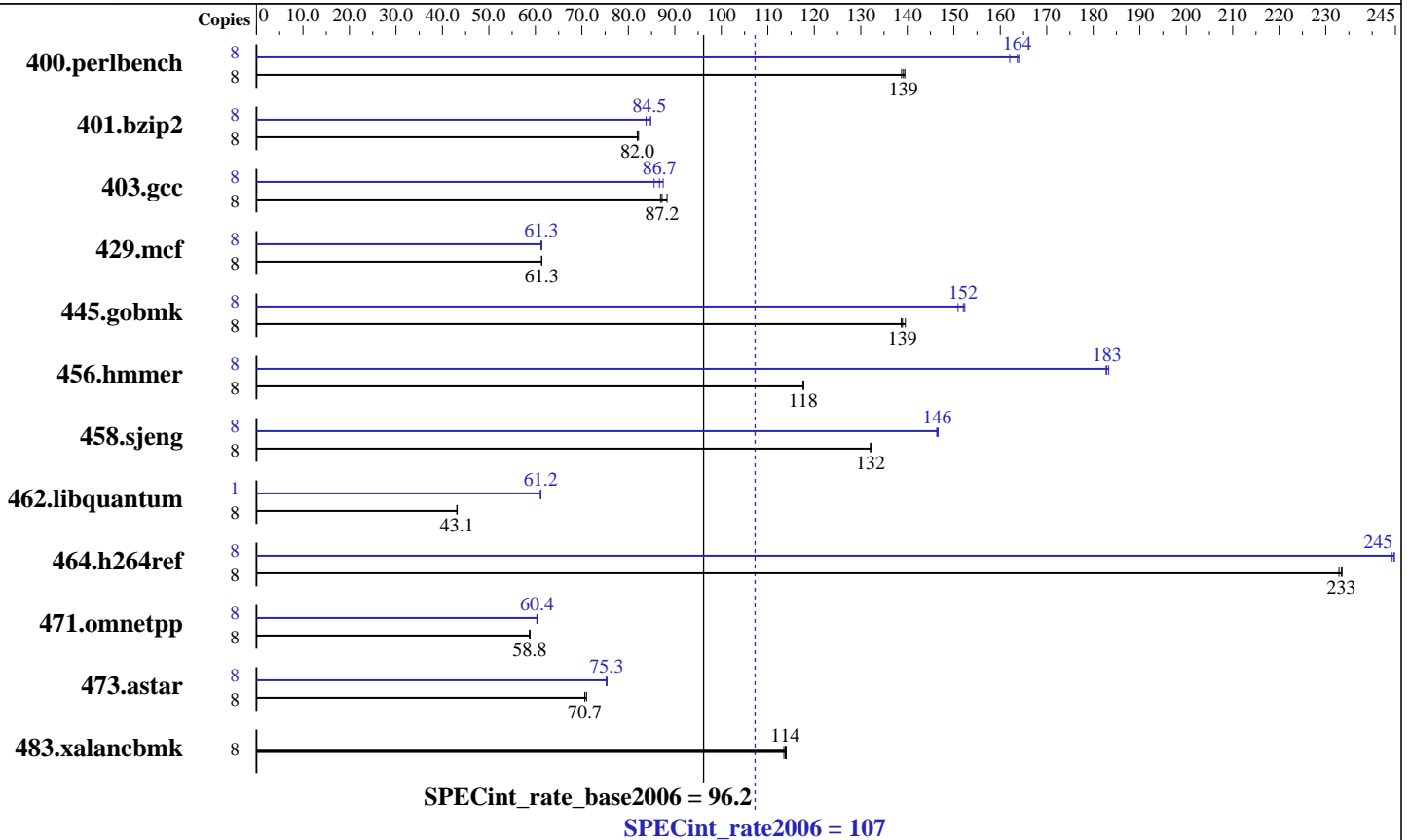
Test date: Aug-2008

Test sponsor: ACTION S.A.

Hardware Availability: Dec-2007

Tested by: ACTION S.A.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X7350
 CPU Characteristics: 1066 MHz bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
 Disk Subsystem: 1x 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	563	139	<u>562</u>	<u>139</u>	560	139	8	477	164	482	162	<u>478</u>	<u>164</u>
401.bzip2	8	<u>942</u>	<u>82.0</u>	942	81.9	939	82.2	8	910	84.8	<u>913</u>	<u>84.5</u>	921	83.8
403.gcc	8	729	88.3	<u>738</u>	<u>87.2</u>	741	86.9	8	736	87.5	<u>743</u>	<u>86.7</u>	753	85.5
429.mcf	8	1190	61.3	<u>1189</u>	<u>61.3</u>	1188	61.4	8	<u>1190</u>	<u>61.3</u>	1189	61.3	1192	61.2
445.gobmk	8	605	139	<u>604</u>	<u>139</u>	601	140	8	556	151	<u>552</u>	<u>152</u>	551	152
456.hmmmer	8	<u>635</u>	<u>118</u>	635	118	634	118	8	408	183	<u>408</u>	<u>183</u>	407	183
458.sjeng	8	733	132	<u>733</u>	<u>132</u>	732	132	8	<u>661</u>	<u>146</u>	660	147	661	146
462.libquantum	8	<u>3843</u>	<u>43.1</u>	3844	43.1	3837	43.2	1	<u>339</u>	<u>61.2</u>	339	61.2	339	61.1
464.h264ref	8	758	234	<u>759</u>	<u>233</u>	760	233	8	723	245	<u>723</u>	<u>245</u>	725	244
471.omnetpp	8	<u>850</u>	<u>58.8</u>	850	58.8	851	58.8	8	829	60.3	828	60.4	<u>828</u>	<u>60.4</u>
473.astar	8	<u>795</u>	<u>70.7</u>	791	71.0	795	70.6	8	<u>746</u>	<u>75.3</u>	746	75.3	745	75.4
483.xalancbmk	8	484	114	<u>485</u>	<u>114</u>	486	113	8	484	114	<u>485</u>	<u>114</u>	486	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml>



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