SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

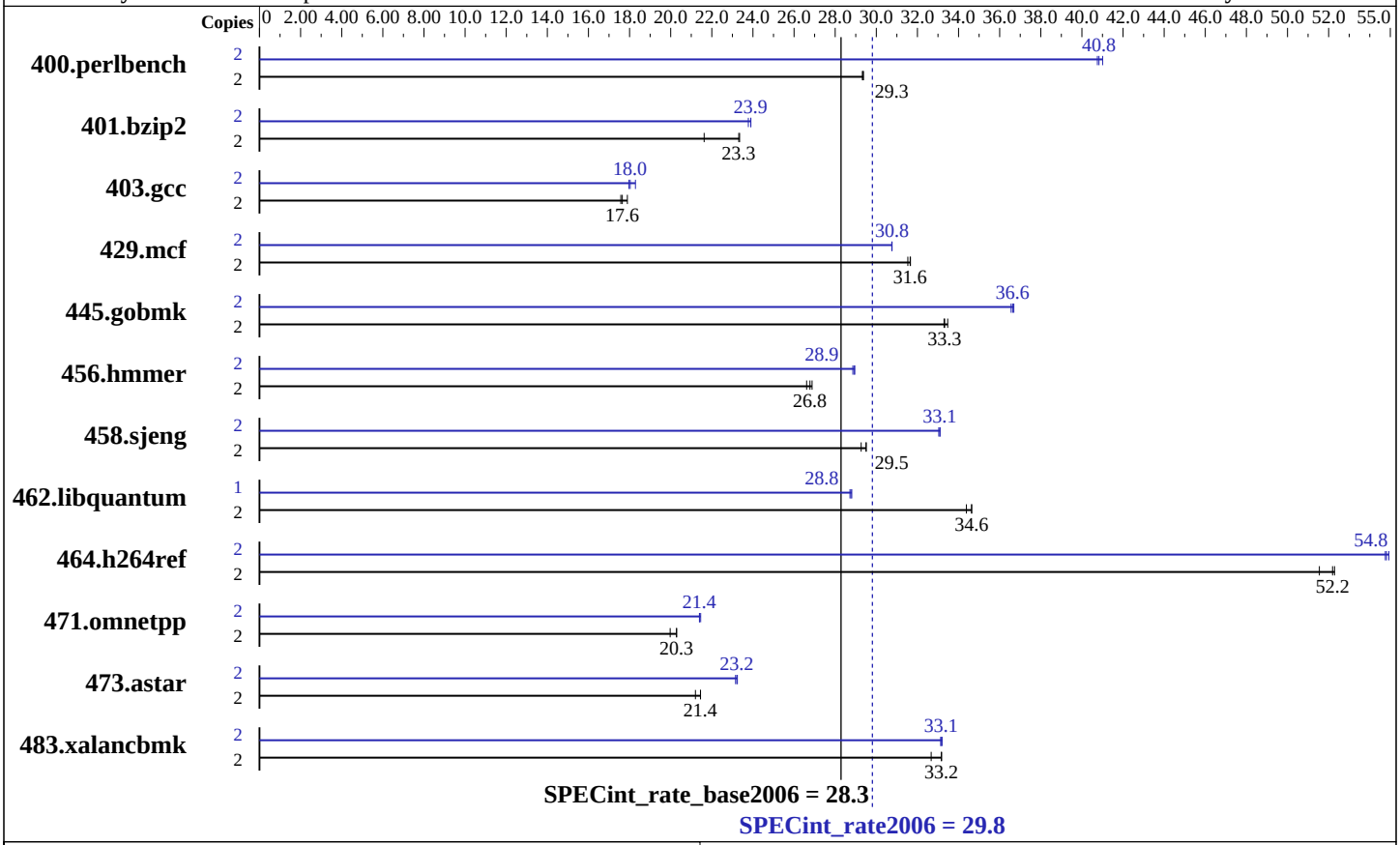
Intel Corporation
Lenovo Thinkpad T61

SPECint®_rate2006 = 29.8

SPECint_rate_base2006 = 28.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2008
Hardware Availability: Sep-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo T7800
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron PC2-6400 DDR2-800 CL5)
Disk Subsystem:	Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional SP2
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation
Lenovo Thinkpad T61

SPECint_rate2006 = 29.8

SPECint_rate_base2006 = 28.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2008
Hardware Availability: Sep-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**666**	**29.3**	665	29.4	666	29.3	2	479	40.8	**479**	**40.8**	476	41.0
401.bzip2	2	892	21.6	**828**	**23.3**	827	23.4	2	**808**	**23.9**	808	23.9	812	23.8
403.gcc	2	900	17.9	916	17.6	**912**	**17.6**	2	880	18.3	896	18.0	**893**	**18.0**
429.mcf	2	576	31.7	**576**	**31.6**	578	31.5	2	593	30.8	593	30.8	**593**	**30.8**
445.gobmk	2	**630**	**33.3**	627	33.5	630	33.3	2	572	36.7	**573**	**36.6**	574	36.6
456.hmmer	2	**697**	**26.8**	694	26.9	701	26.6	2	644	29.0	**645**	**28.9**	647	28.9
458.sjeng	2	**820**	**29.5**	820	29.5	827	29.3	2	**731**	**33.1**	733	33.0	731	33.1
462.libquantum	2	1196	34.6	**1197**	**34.6**	1205	34.4	1	**720**	**28.8**	721	28.7	719	28.8
464.h264ref	2	846	52.3	**848**	**52.2**	859	51.5	2	806	54.9	**807**	**54.8**	808	54.8
471.omnetpp	2	616	20.3	**616**	**20.3**	626	20.0	2	**584**	**21.4**	584	21.4	583	21.5
473.astar	2	655	21.5	**655**	**21.4**	662	21.2	2	**605**	**23.2**	604	23.2	606	23.2
483.xalancbmk	2	416	33.2	**416**	**33.2**	422	32.7	2	**416**	**33.1**	416	33.1	416	33.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qvec-guard-write /F512000000
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:42:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 30 September 2008.