SPEC® CINT2006 Result

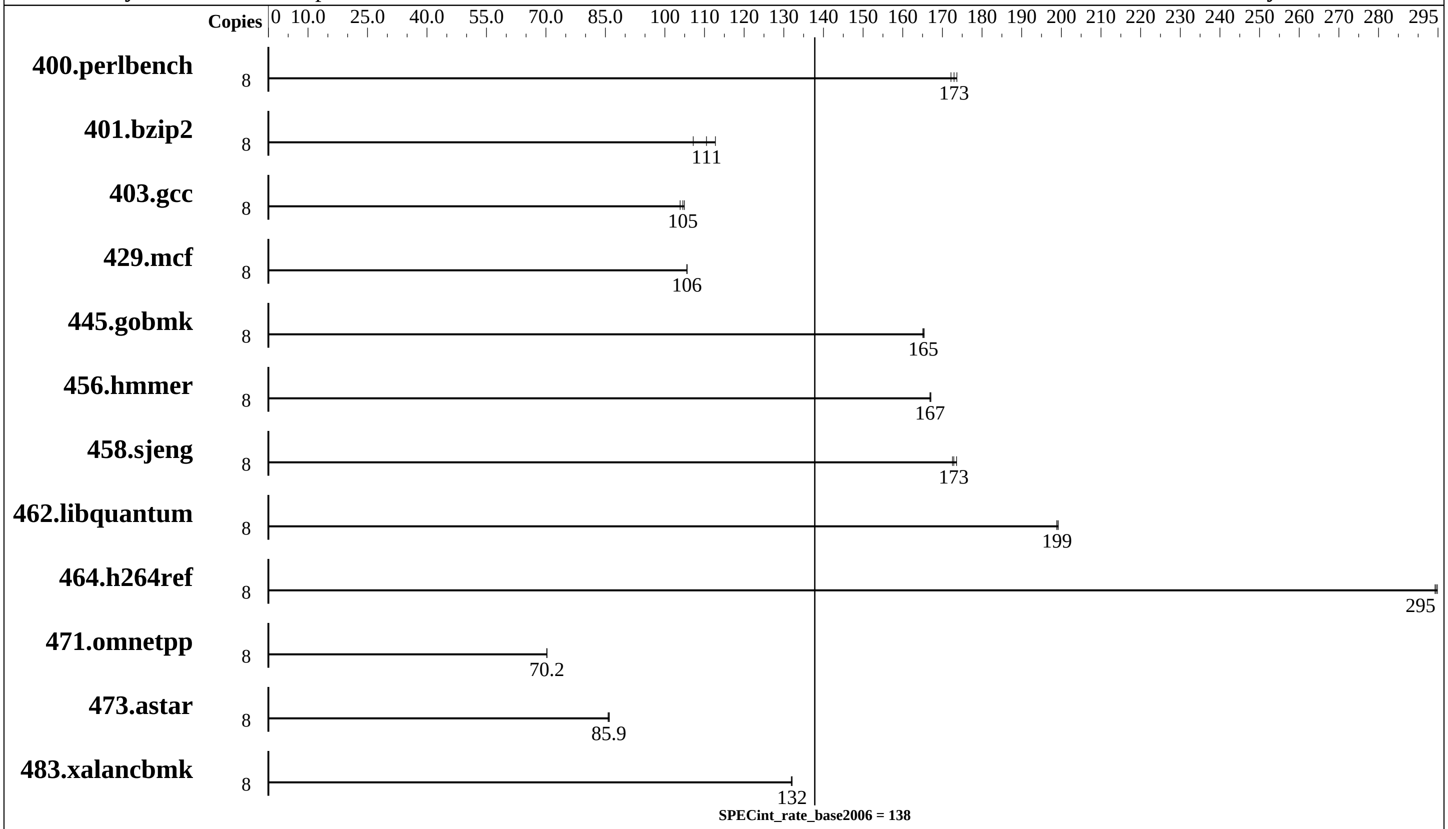
IBM Corporation

IBM BladeCenter HS21 (Intel Xeon X5470)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 138

CPU2006 license:	11	**Test date:**	Sep-2008
Test sponsor:	IBM Corporation	**Hardware Availability:**	Oct-2008
Tested by:	IBM Corporation	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon X5470
CPU Characteristics:	1333MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	8	454	172	450	174	**452**	**173**							
401.bzip2	8	685	113	**699**	**111**	720	107							
403.gcc	8	614	105	**616**	**105**	620	104							
429.mcf	8	691	106	**691**	**106**	691	106							
445.gobmk	8	**508**	**165**	508	165	507	165							
456.hmmer	8	447	167	447	167	**447**	**167**							
458.sjeng	8	561	173	**560**	**173**	558	174							
462.libquantum	8	832	199	834	199	**833**	**199**							
464.h264ref	8	602	294	**601**	**295**	600	295							
471.omnetpp	8	**712**	**70.2**	712	70.2	712	70.3							
473.astar	8	655	85.7	**654**	**85.9**	653	86.0							
483.xalancbmk	8	**418**	**132**	419	132	418	132							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores
```

General Notes

```
 Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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