



SPEC® CINT2006 Result

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Bull SAS

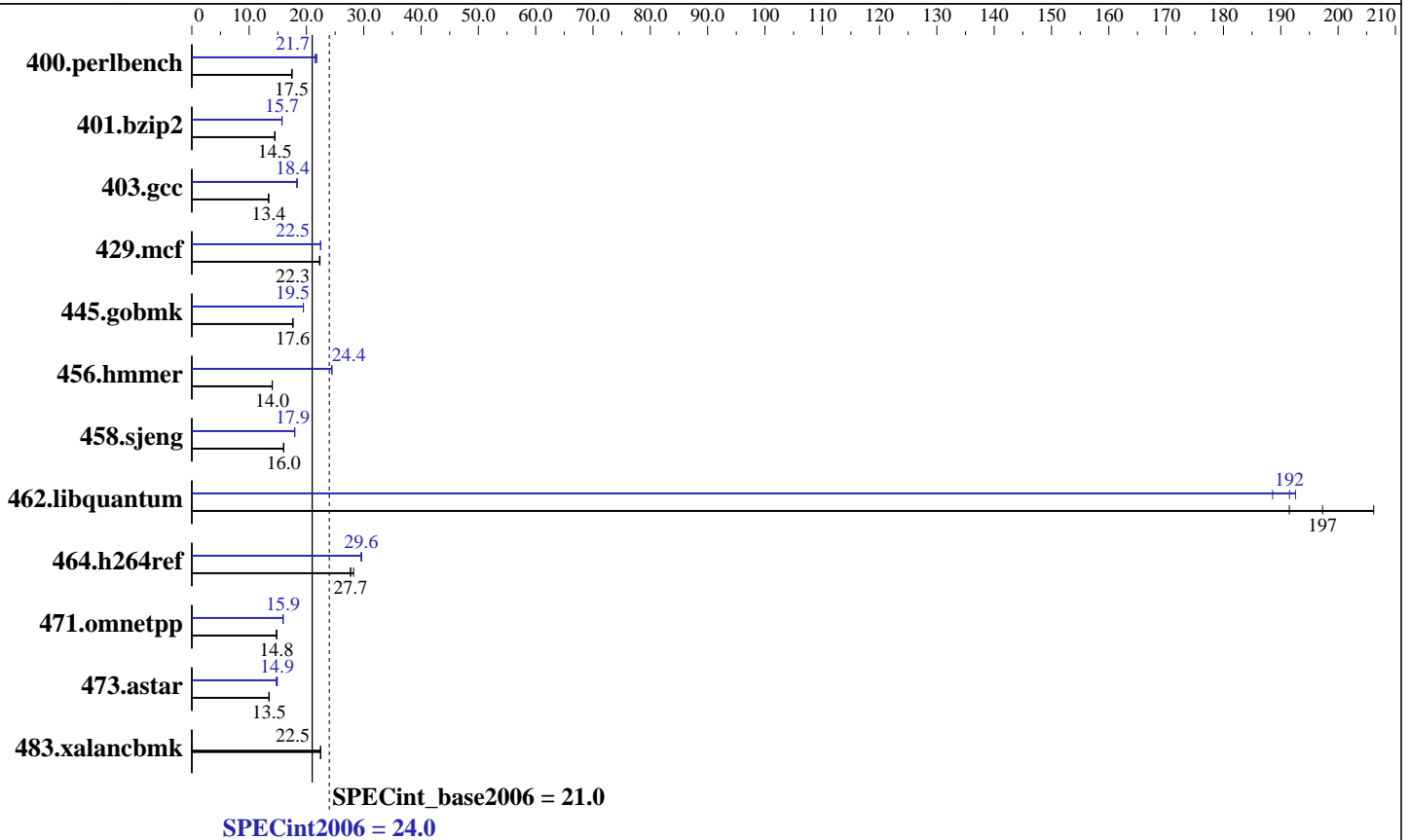
NovaScale B260
(Intel Xeon E5430, 2.66 GHz)

SPECint®2006 = 24.0

SPECint_base2006 = 21.0

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Sep-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15
 SmartHeap library V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | <u>559</u> | <u>17.5</u> | 561 | 17.4 | 559 | 17.5 | <u>450</u> | <u>21.7</u> | 448 | 21.8 | 455 | 21.5 |
| 401.bzip2 | 669 | 14.4 | 664 | 14.5 | <u>664</u> | <u>14.5</u> | 613 | 15.7 | <u>613</u> | <u>15.7</u> | 614 | 15.7 |
| 403.gcc | 603 | 13.4 | <u>600</u> | <u>13.4</u> | 600 | 13.4 | 438 | 18.4 | 439 | 18.3 | <u>439</u> | <u>18.4</u> |
| 429.mcf | 408 | 22.4 | <u>409</u> | <u>22.3</u> | 409 | 22.3 | <u>406</u> | <u>22.5</u> | 406 | 22.5 | 405 | 22.5 |
| 445.gobmk | 595 | 17.6 | 595 | 17.6 | <u>595</u> | <u>17.6</u> | 539 | 19.5 | <u>539</u> | <u>19.5</u> | 539 | 19.5 |
| 456.hmmmer | <u>665</u> | <u>14.0</u> | 666 | 14.0 | 664 | 14.0 | 382 | 24.4 | <u>382</u> | <u>24.4</u> | 382 | 24.4 |
| 458.sjeng | 753 | 16.1 | <u>755</u> | <u>16.0</u> | 756 | 16.0 | 674 | 17.9 | <u>674</u> | <u>17.9</u> | 674 | 18.0 |
| 462.libquantum | 108 | 191 | <u>105</u> | <u>197</u> | 100 | 206 | 110 | 189 | 108 | 193 | <u>108</u> | <u>192</u> |
| 464.h264ref | 800 | 27.7 | <u>798</u> | <u>27.7</u> | 784 | 28.2 | 746 | 29.7 | 749 | 29.5 | <u>748</u> | <u>29.6</u> |
| 471.omnetpp | 423 | 14.8 | <u>423</u> | <u>14.8</u> | 423 | 14.8 | 392 | 15.9 | 393 | 15.9 | <u>392</u> | <u>15.9</u> |
| 473.astar | <u>520</u> | <u>13.5</u> | 520 | 13.5 | 522 | 13.5 | 471 | 14.9 | <u>471</u> | <u>14.9</u> | 477 | 14.7 |
| 483.xalancbmk | 307 | 22.5 | <u>307</u> | <u>22.5</u> | 307 | 22.5 | 307 | 22.5 | <u>307</u> | <u>22.5</u> | 307 | 22.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

Platform Notes

BIOS configuration:
Hardware Prefetcher Enabled
Adjacent Cache-Line Prefetch Enabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.xml

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