



SPEC[®] CFP2006 Result

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Acer Incorporated

SPECfp[®]_rate2006 = 55.1

Acer Altos R920 (Intel Xeon E7420)

SPECfp_rate_base2006 = 53.0

CPU2006 license: 97

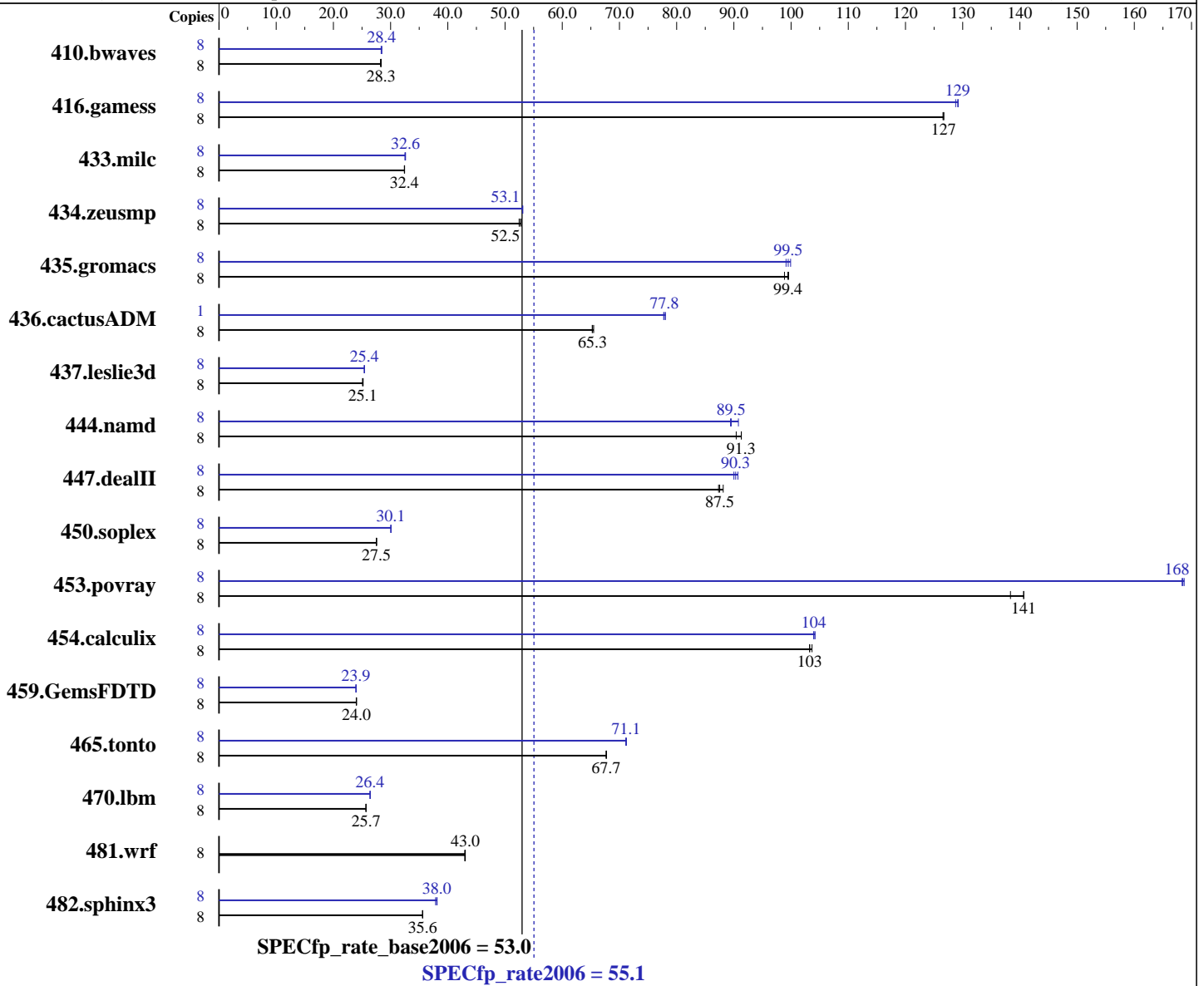
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Oct-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E7420
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2, 4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16 x 2048MB ECC FB-DIMM DDR2-667 CL5)
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 3837 | 28.3 | 3835 | 28.3 | 3851 | 28.2 | 8 | 3836 | 28.3 | 3823 | 28.4 | 3822 | 28.4 |
| 416.gamess | 8 | 1237 | 127 | 1238 | 127 | 1236 | 127 | 8 | 1212 | 129 | 1213 | 129 | 1216 | 129 |
| 433.milc | 8 | 2265 | 32.4 | 2263 | 32.4 | 2266 | 32.4 | 8 | 2260 | 32.5 | 2252 | 32.6 | 2254 | 32.6 |
| 434.zeusmp | 8 | 1388 | 52.5 | 1380 | 52.8 | 1386 | 52.5 | 8 | 1372 | 53.1 | 1371 | 53.1 | 1371 | 53.1 |
| 435.gromacs | 8 | 574 | 99.6 | 578 | 98.8 | 574 | 99.4 | 8 | 572 | 99.9 | 576 | 99.1 | 574 | 99.5 |
| 436.cactusADM | 8 | 1464 | 65.3 | 1460 | 65.5 | 1465 | 65.2 | 1 | 154 | 77.8 | 153 | 78.0 | 154 | 77.7 |
| 437.leslie3d | 8 | 2984 | 25.2 | 2993 | 25.1 | 2996 | 25.1 | 8 | 2958 | 25.4 | 2959 | 25.4 | 2960 | 25.4 |
| 444.namd | 8 | 710 | 90.4 | 703 | 91.3 | 703 | 91.3 | 8 | 707 | 90.8 | 718 | 89.4 | 717 | 89.5 |
| 447.dealII | 8 | 1046 | 87.5 | 1038 | 88.1 | 1048 | 87.3 | 8 | 1013 | 90.3 | 1017 | 90.0 | 1009 | 90.7 |
| 450.soplex | 8 | 2423 | 27.5 | 2427 | 27.5 | 2421 | 27.6 | 8 | 2221 | 30.0 | 2220 | 30.1 | 2220 | 30.1 |
| 453.povray | 8 | 303 | 141 | 308 | 138 | 303 | 141 | 8 | 252 | 169 | 253 | 168 | 253 | 168 |
| 454.calculix | 8 | 637 | 104 | 639 | 103 | 639 | 103 | 8 | 635 | 104 | 633 | 104 | 635 | 104 |
| 459.GemsFDTD | 8 | 3533 | 24.0 | 3538 | 24.0 | 3530 | 24.0 | 8 | 3546 | 23.9 | 3544 | 23.9 | 3542 | 24.0 |
| 465.tonto | 8 | 1162 | 67.7 | 1163 | 67.7 | 1163 | 67.7 | 8 | 1107 | 71.1 | 1106 | 71.1 | 1105 | 71.2 |
| 470.lbm | 8 | 4278 | 25.7 | 4277 | 25.7 | 4277 | 25.7 | 8 | 4161 | 26.4 | 4161 | 26.4 | 4161 | 26.4 |
| 481.wrf | 8 | 2075 | 43.1 | 2076 | 43.0 | 2080 | 43.0 | 8 | 2075 | 43.1 | 2076 | 43.0 | 2080 | 43.0 |
| 482.sphinx3 | 8 | 4380 | 35.6 | 4378 | 35.6 | 4383 | 35.6 | 8 | 4098 | 38.0 | 4089 | 38.1 | 4119 | 37.9 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Taskset was used to bind processes to cores except for 436.cactusADM in peak.

Platform Notes

BIOS Settings
Adjacent Cache Line Prefetch set to OFF
Hardware Prefetch set to OFF
High Bandwidth Option set to ON



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General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
'ulimit -s unlimited' was set for stacksize unlimited

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

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Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.15.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.15.xml>

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For other inquiries, please contact webmaster@spec.org.

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