



SPEC[®] CFP2006 Result

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ACTION S.A.

SPECfp[®]_rate2006 = Not Run

ACTINA SOLAR E 110 X3 (Intel Xeon X3330, 2.66 GHz)

SPECfp_rate_base2006 = 45.4

CPU2006 license: 9008

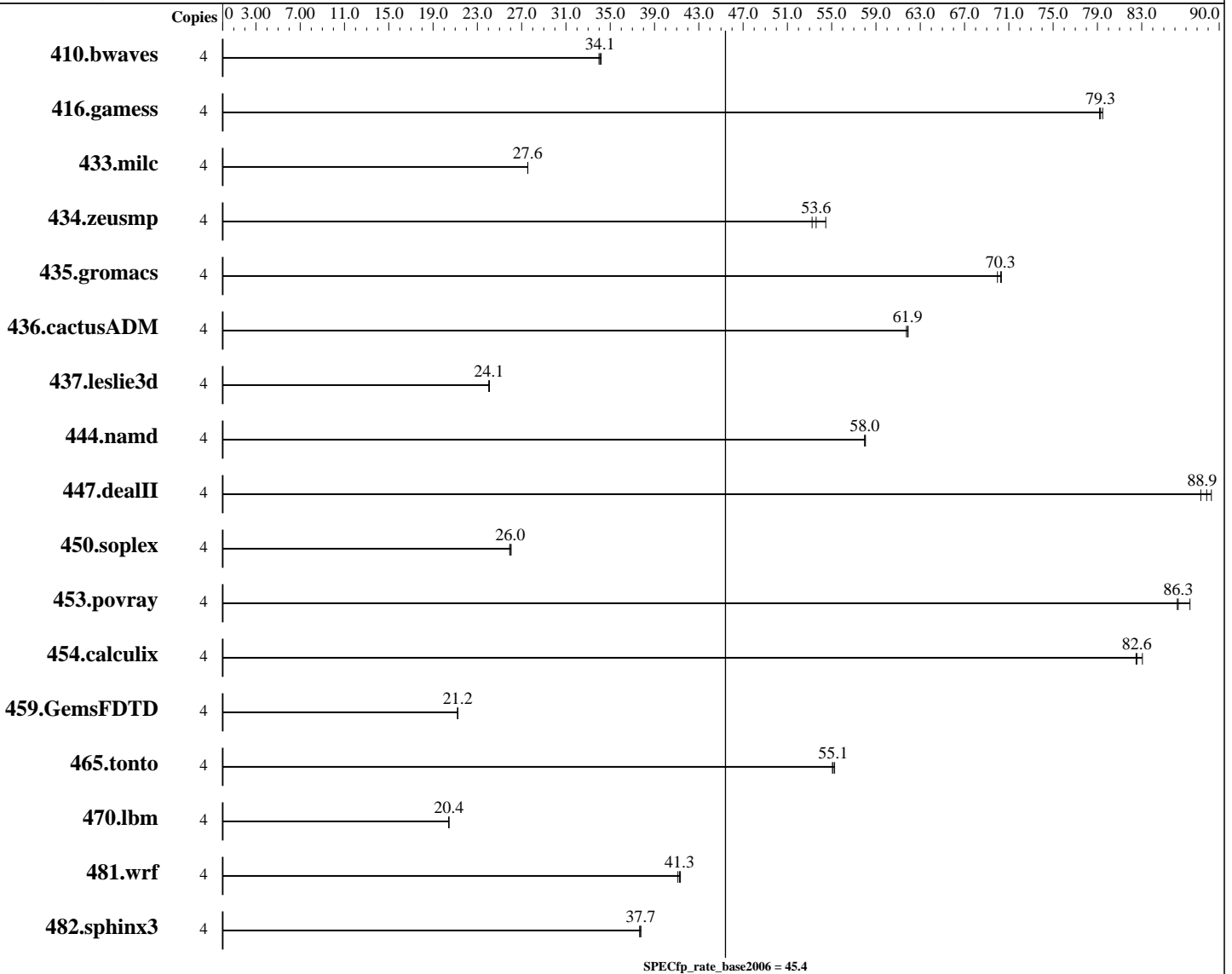
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Nov-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X3330
 CPU Characteristics: 2.66 GHz, 1333 MHz System Bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System: SuSe Linux Enterprise Server (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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Hardware (Continued)

L3 Cache: None
Other Cache: None
Memory: 8 GB (4x 2 GB DDR2 ECC, PC-6400)
Disk Subsystem: 1x 500 GB SATA, 7200 RPM
Other Hardware: None

Software (Continued)

Peak Pointers: Not Applicable
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--------|---------|-------|---------|-------|---------|-------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 4 | 1590 | 34.2 | <u>1595</u> | <u>34.1</u> | 1599 | 34.0 | | | | | | | | | |
| 416.gamess | 4 | 989 | 79.2 | <u>988</u> | <u>79.3</u> | 985 | 79.5 | | | | | | | | | |
| 433.milc | 4 | 1332 | 27.6 | <u>1333</u> | <u>27.6</u> | 1333 | 27.6 | | | | | | | | | |
| 434.zeusmp | 4 | 684 | 53.2 | <u>679</u> | <u>53.6</u> | 668 | 54.5 | | | | | | | | | |
| 435.gromacs | 4 | <u>407</u> | <u>70.3</u> | 408 | 70.0 | 406 | 70.3 | | | | | | | | | |
| 436.cactusADM | 4 | 772 | 61.9 | <u>773</u> | <u>61.9</u> | 774 | 61.7 | | | | | | | | | |
| 437.leslie3d | 4 | 1561 | 24.1 | 1565 | 24.0 | <u>1562</u> | <u>24.1</u> | | | | | | | | | |
| 444.namd | 4 | 553 | 58.0 | <u>553</u> | <u>58.0</u> | 553 | 58.1 | | | | | | | | | |
| 447.dealII | 4 | 512 | 89.3 | 518 | 88.3 | <u>515</u> | <u>88.9</u> | | | | | | | | | |
| 450.soplex | 4 | 1281 | 26.1 | 1287 | 25.9 | <u>1284</u> | <u>26.0</u> | | | | | | | | | |
| 453.povray | 4 | 247 | 86.2 | <u>247</u> | <u>86.3</u> | 244 | 87.4 | | | | | | | | | |
| 454.calculix | 4 | <u>400</u> | <u>82.6</u> | 397 | 83.1 | 400 | 82.5 | | | | | | | | | |
| 459.GemsFDTD | 4 | 1999 | 21.2 | 2000 | 21.2 | <u>2000</u> | <u>21.2</u> | | | | | | | | | |
| 465.tonto | 4 | 712 | 55.3 | <u>714</u> | <u>55.1</u> | 715 | 55.1 | | | | | | | | | |
| 470.lbm | 4 | 2691 | 20.4 | 2691 | 20.4 | <u>2691</u> | <u>20.4</u> | | | | | | | | | |
| 481.wrf | 4 | 1081 | 41.3 | 1087 | 41.1 | <u>1083</u> | <u>41.3</u> | | | | | | | | | |
| 482.sphinx3 | 4 | 2065 | 37.8 | 2070 | 37.7 | <u>2066</u> | <u>37.7</u> | | | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

'taskset' command was used to bind processes to CPUs.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

```

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Mon Jul 13 18:41:37 2009 by SPEC CPU2006 PS/PDF formatter v6323.