SPEC® CINT2006 Result

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Unisys Corporation

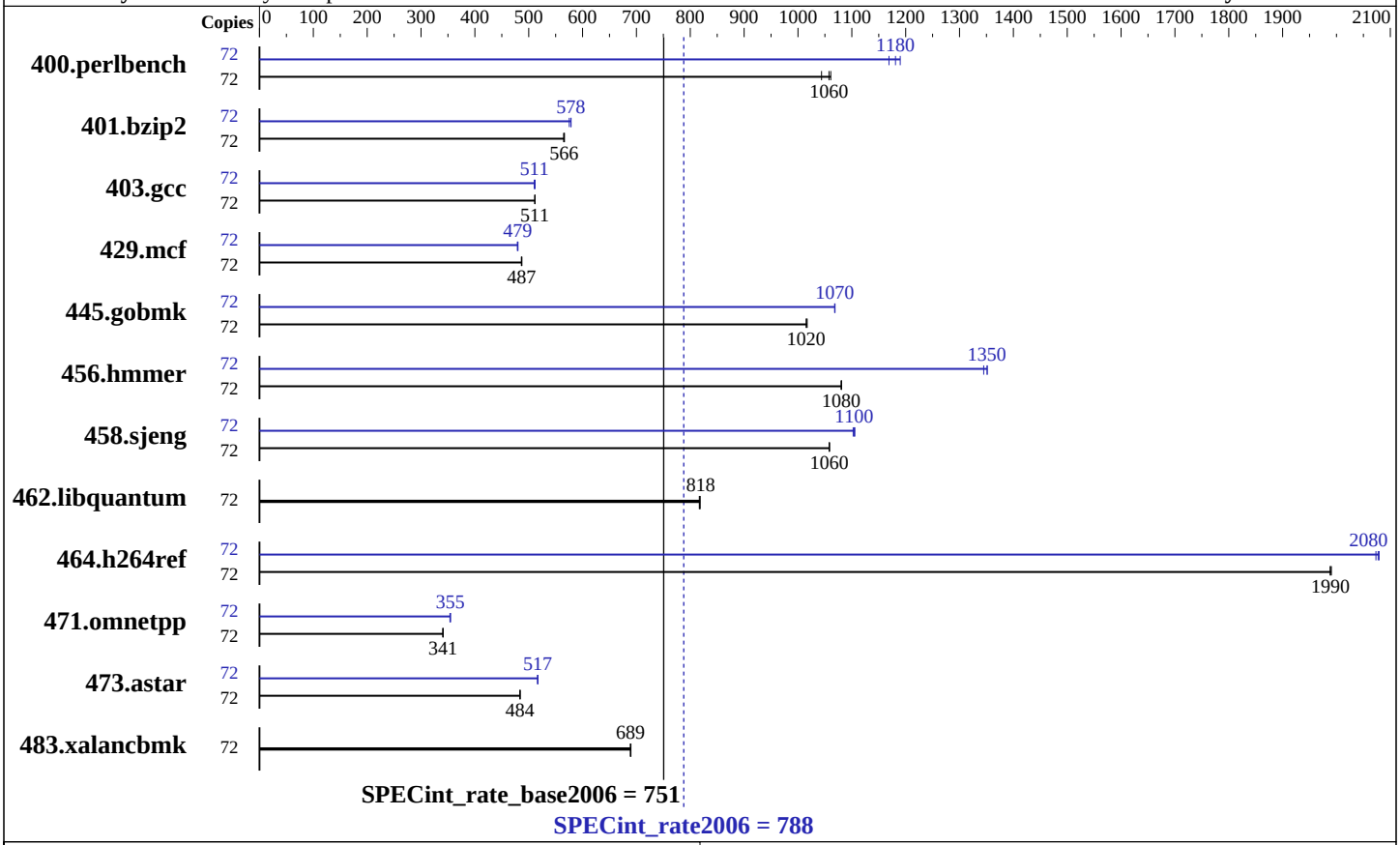
Unisys ES7000 Model 7600R, Intel Xeon X7460, 2.66GHz

SPECint®_rate2006 = 788

SPECint_rate_base2006 = 751

CPU2006 license: 15
Test sponsor: Unisys Corporation
Tested by: Unisys Corporation

Test date: Dec-2008
Hardware Availability: Nov-2008
Software Availability: Jan-2009



Hardware

CPU Name:	Intel Xeon X7460
CPU Characteristics:	
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	72 cores, 12 chips, 6 cores/chip
CPU(s) orderable:	4,8,12,16 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	9 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (24x4GB, 2 rank, PC2-5300F-555-11-E0)
Disk Subsystem:	2 x 146 GB SAS,15000 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) SP2(Kernel 2.6.16-60.0.21-smp)
Compiler:	Intel C++ Compiler 11.0 for Linux (Build 20080730) Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	72	674	1040	663	1060	**665**	**1060**	72	602	1170	**596**	**1180**	591	1190
401.bzip2	72	1227	566	1230	565	**1227**	**566**	72	**1202**	**578**	1201	579	1208	575
403.gcc	72	1134	511	**1133**	**511**	1133	512	72	1135	510	1132	512	**1135**	**511**
429.mcf	72	**1349**	**487**	1350	486	1348	487	72	1368	480	1371	479	**1370**	**479**
445.gobmk	72	744	1020	743	1020	**744**	**1020**	72	707	1070	707	1070	**707**	**1070**
456.hmmer	72	622	1080	621	1080	**622**	**1080**	72	**497**	**1350**	499	1340	497	1350
458.sjeng	72	824	1060	823	1060	**823**	**1060**	72	790	1100	788	1110	**789**	**1100**
462.libquantum	72	**1824**	**818**	1823	818	1826	817	72	**1824**	**818**	1823	818	1826	817
464.h264ref	72	801	1990	**801**	**1990**	800	1990	72	769	2070	**767**	**2080**	766	2080
471.omnetpp	72	1319	341	1322	341	**1321**	**341**	72	1272	354	1269	355	**1269**	**355**
473.astar	72	1045	484	**1045**	**484**	1044	484	72	**978**	**517**	977	517	979	516
483.xalancbmk	72	720	690	721	689	**721**	**689**	72	720	690	721	689	**721**	**689**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores.
```

Operating System Notes

```
'ulimit -s unlimited' allows stack to grow to system limit.

/proc/sys/vm/zone_reclaim_mode=1 to enable zone_reclaim.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.xml

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