



SPEC® CINT2006 Result

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YOYotech

(Test Sponsor: Future Publishing Ltd.)

SPECint®_rate2006 = 130

Fi7EPOWER MLK1610 (Intel Core i7-965)

SPECint_rate_base2006 = 122

CPU2006 license: 3772

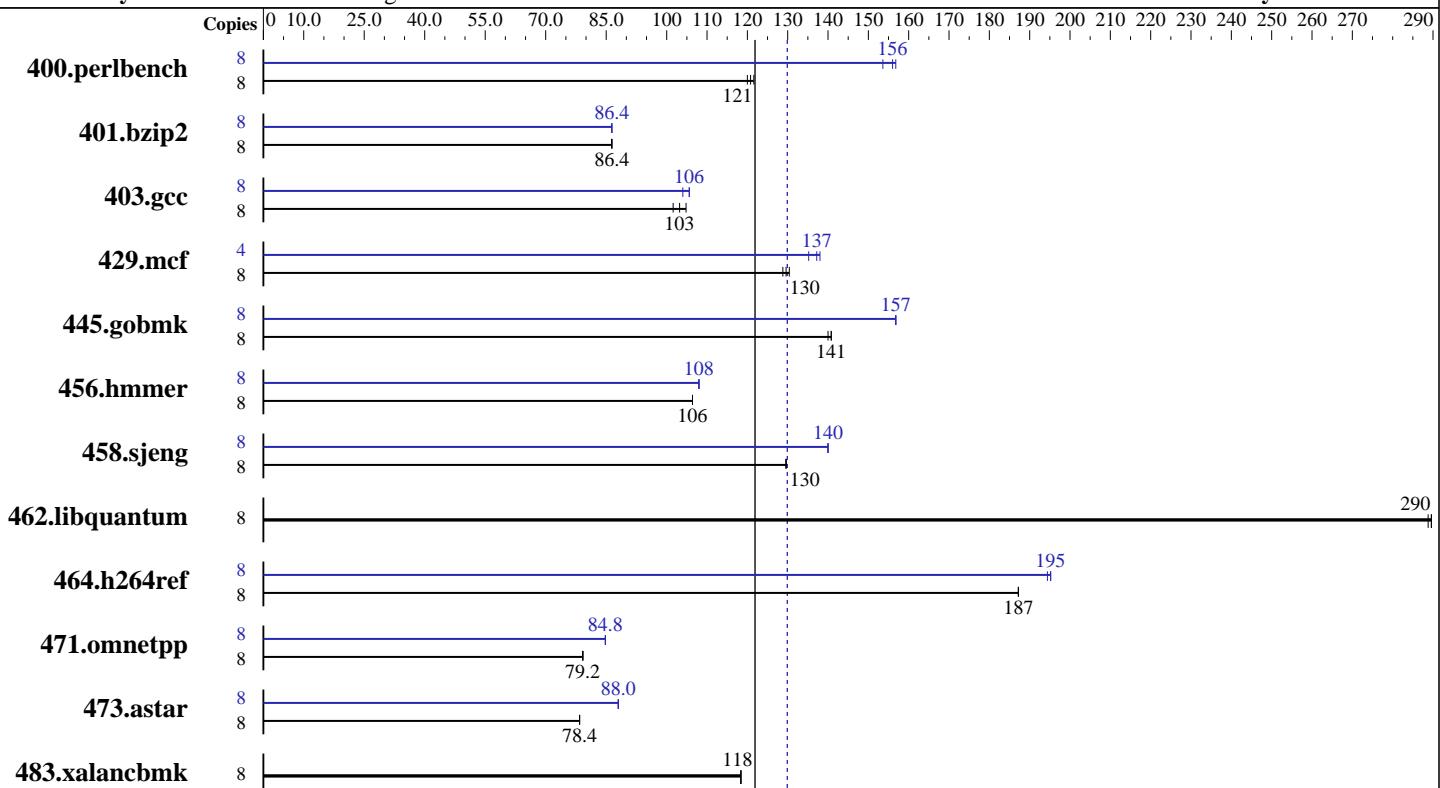
Test date: Oct-2008

Test sponsor: Future Publishing Ltd.

Hardware Availability: Nov-2008

Tested by: Future Publishing Ltd.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core i7-965 Extreme Edition
CPU Characteristics: Intel Turbo Boost Technology disabled, clocked at 3.73 GHz
CPU MHz: 3733
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 9 GB (3x 2GB and 3x 1GB Corsair DDR3-1066, 9-9-9-24)
Disk Subsystem: 80 GB SATA, SSD
Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
Compiler: Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	650	120	641	122	648	121	8	508	154	500	157	501	156
401.bzip2	8	896	86.4	890	86.4	896	86.4	8	897	86.4	897	86.4	897	86.4
403.gcc	8	615	105	622	103	636	102	8	621	104	611	106	610	106
429.mcf	8	559	130	565	129	563	130	4	266	137	270	135	265	138
445.gobmk	8	595	141	599	140	597	141	8	535	157	534	157	534	157
456.hammer	8	699	106	699	106	699	106	8	689	108	690	108	690	108
458.sjeng	8	747	130	748	130	747	130	8	690	140	691	140	691	140
462.libquantum	8	572	290	573	289	573	290	8	572	290	573	289	573	290
464.h264ref	8	948	187	947	187	948	187	8	907	195	911	194	908	195
471.omnetpp	8	630	79.2	629	79.2	630	79.2	8	589	84.8	589	84.8	589	84.8
473.astar	8	715	78.4	714	78.4	714	78.4	8	639	88.0	640	88.0	640	88.0
483.xalancbmk	8	465	118	465	118	465	118	8	465	118	465	118	465	118

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

System was configured with HIS Radeon HD 4870 X2 discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F5120000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch  
/F5120000000 shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias  
/F512000000
```

```
403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gnu: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html>
<http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml>
<http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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