SPEC® CINT2006 Result

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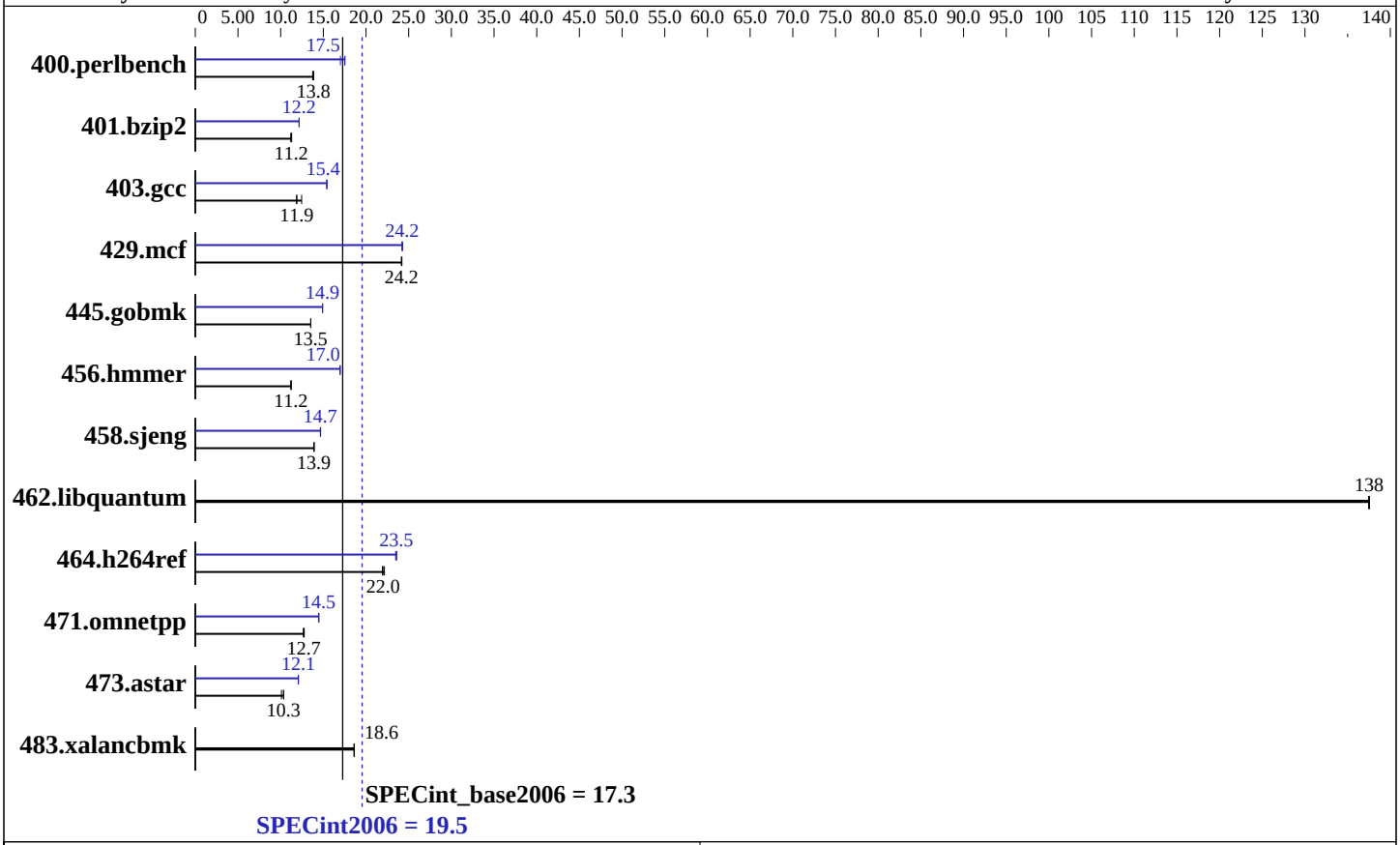
NTT System S. A.
NTT Tytan S8 Series

SPECint®2006 = 19.5

SPECint_base2006 = 17.3

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Dec-2008
Hardware Availability: Dec-2008
Software Availability: Dec-2008



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	2 GHz, 2x6 MB L2 shared, 1333 MHz System Bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4GB)
Disk Subsystem:	300 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP1, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	705	13.9	711	13.7	**706**	**13.8**	**558**	**17.5**	557	17.5	575	17.0
401.bzip2	**858**	**11.2**	855	11.3	862	11.2	794	12.2	**793**	**12.2**	793	12.2
403.gcc	646	12.5	**677**	**11.9**	678	11.9	**522**	**15.4**	522	15.4	522	15.4
429.mcf	**377**	**24.2**	378	24.2	377	24.2	375	24.3	377	24.2	**376**	**24.2**
445.gobmk	775	13.5	776	13.5	**776**	**13.5**	704	14.9	704	14.9	**704**	**14.9**
456.hmmer	831	11.2	832	11.2	**832**	**11.2**	**550**	**17.0**	551	16.9	550	17.0
458.sjeng	**870**	**13.9**	871	13.9	869	13.9	824	14.7	**825**	**14.7**	826	14.6
462.libquantum	151	137	151	138	**151**	**138**	151	137	151	138	**151**	**138**
464.h264ref	**1006**	**22.0**	1010	21.9	999	22.2	943	23.5	**940**	**23.5**	938	23.6
471.omnetpp	490	12.8	**491**	**12.7**	494	12.7	433	14.4	**432**	**14.5**	432	14.5
473.astar	696	10.1	679	10.3	**679**	**10.3**	**582**	**12.1**	582	12.1	580	12.1
483.xalancbmk	370	18.6	371	18.6	**370**	**18.6**	370	18.6	371	18.6	**370**	**18.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -O2 -ipo -no-prec-div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml

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