



SPEC® CFP2006 Result

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ACTION S.A.

SPECfp®_rate2006 = Not Run

ACTINA SOLAR 212 X2 (Intel Xeon E5410, 2.33 GHz)

SPECfp_rate_base2006 = 36.9

CPU2006 license: 9008

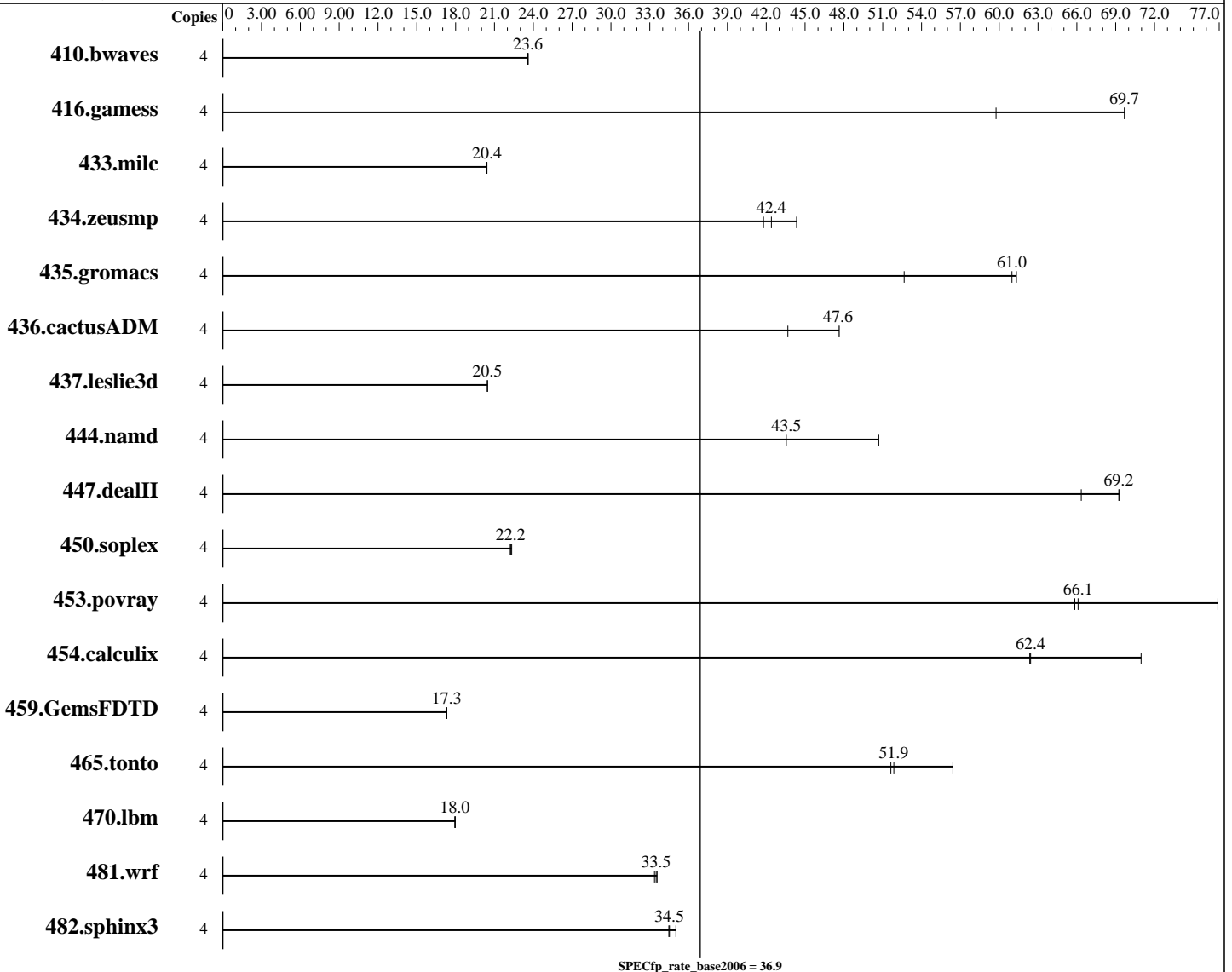
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics: 1333 MHz System Bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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Hardware (Continued)

L3 Cache: None
Other Cache: None
Memory: 16 GB (8x 2 GB, PC2-5300, CL 5-5-5, FB ECC)
Disk Subsystem: 500 GB SATA, 7200 RPM
Other Hardware: None

Software (Continued)

Peak Pointers: Not Applicable
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|--------------------|--------------------|--------------------|--------------------|--------------------|--------------------|--------|---------|-------|---------|-------|---------|-------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 4 | 2304 | 23.6 | 2305 | 23.6 | <u>2304</u> | <u>23.6</u> | | | | | | | | | |
| 416.gamess | 4 | 1124 | 69.7 | 1311 | 59.8 | <u>1124</u> | <u>69.7</u> | | | | | | | | | |
| 433.milc | 4 | 1798 | 20.4 | 1799 | 20.4 | <u>1798</u> | <u>20.4</u> | | | | | | | | | |
| 434.zeusmp | 4 | <u>859</u> | <u>42.4</u> | 871 | 41.8 | 821 | 44.3 | | | | | | | | | |
| 435.gromacs | 4 | 466 | 61.3 | 542 | 52.7 | <u>468</u> | <u>61.0</u> | | | | | | | | | |
| 436.cactusADM | 4 | <u>1005</u> | <u>47.6</u> | 1095 | 43.7 | 1003 | 47.6 | | | | | | | | | |
| 437.leslie3d | 4 | <u>1838</u> | <u>20.5</u> | 1846 | 20.4 | 1837 | 20.5 | | | | | | | | | |
| 444.namd | 4 | 737 | 43.5 | <u>737</u> | <u>43.5</u> | 633 | 50.7 | | | | | | | | | |
| 447.dealII | 4 | <u>661</u> | <u>69.2</u> | 660 | 69.3 | 690 | 66.3 | | | | | | | | | |
| 450.soplex | 4 | 1501 | 22.2 | <u>1499</u> | <u>22.2</u> | 1494 | 22.3 | | | | | | | | | |
| 453.povray | 4 | 323 | 65.8 | <u>322</u> | <u>66.1</u> | 277 | 76.9 | | | | | | | | | |
| 454.calculix | 4 | 529 | 62.4 | <u>529</u> | <u>62.4</u> | 465 | 71.0 | | | | | | | | | |
| 459.GemsFDTD | 4 | 2457 | 17.3 | <u>2451</u> | <u>17.3</u> | 2451 | 17.3 | | | | | | | | | |
| 465.tonto | 4 | 762 | 51.6 | <u>759</u> | <u>51.9</u> | 698 | 56.4 | | | | | | | | | |
| 470.lbm | 4 | <u>3062</u> | <u>18.0</u> | 3062 | 17.9 | 3059 | 18.0 | | | | | | | | | |
| 481.wrf | 4 | <u>1334</u> | <u>33.5</u> | 1339 | 33.4 | 1331 | 33.6 | | | | | | | | | |
| 482.sphinx3 | 4 | <u>2258</u> | <u>34.5</u> | 2262 | 34.5 | 2226 | 35.0 | | | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

This result is measured on ACTINA SOLAR 222 X2.
Note that the ACTINA SOLAR 222 X2 and ACTINA SOLAR 212 X2 are electrically equivalent.



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General Notes

taskset was used to bind processes to cores
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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