



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

ASUS P6T WS PRO workstation motherboard (Intel Core i7-965 Extreme Edition)

SPECint®2006 = 35.2

SPECint_base2006 = 31.5

CPU2006 license: 009016

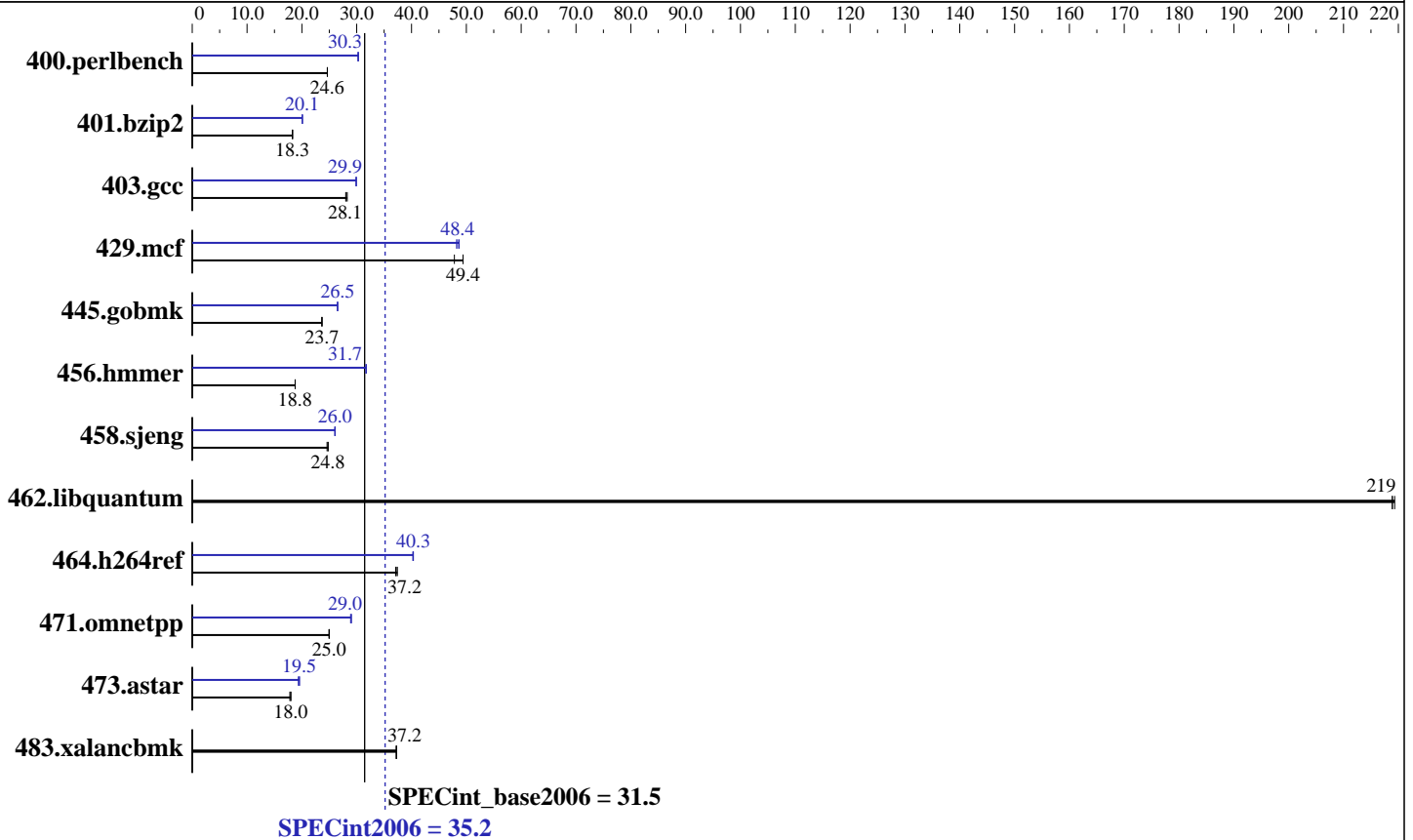
Test sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test date: Dec-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core i7-965 Extreme Edition
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (6 x 2GB DDR3-1333 ECC, CL=9)
 Disk Subsystem: Hitachi HDT725050VLA360 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	396	24.6	396	24.6	<u>396</u>	<u>24.6</u>	323	30.2	322	30.3	<u>323</u>	<u>30.3</u>
401.bzip2	527	18.3	<u>528</u>	<u>18.3</u>	528	18.3	481	20.1	480	20.1	<u>481</u>	<u>20.1</u>
403.gcc	285	28.3	287	28.0	<u>286</u>	<u>28.1</u>	269	29.9	<u>269</u>	<u>29.9</u>	269	29.9
429.mcf	191	47.8	<u>185</u>	<u>49.4</u>	185	49.4	187	48.7	<u>188</u>	<u>48.4</u>	189	48.2
445.gobmk	<u>443</u>	<u>23.7</u>	443	23.7	444	23.6	396	26.5	396	26.5	<u>396</u>	<u>26.5</u>
456.hmmer	<u>497</u>	<u>18.8</u>	497	18.8	497	18.8	<u>294</u>	<u>31.7</u>	294	31.7	294	31.7
458.sjeng	492	24.6	488	24.8	<u>489</u>	<u>24.8</u>	465	26.0	<u>465</u>	<u>26.0</u>	465	26.0
462.libquantum	94.7	219	<u>94.7</u>	<u>219</u>	94.5	219	94.7	219	<u>94.7</u>	<u>219</u>	94.5	219
464.h264ref	591	37.4	<u>595</u>	<u>37.2</u>	596	37.1	550	40.2	<u>549</u>	<u>40.3</u>	549	40.3
471.omnetpp	250	25.0	<u>250</u>	<u>25.0</u>	250	25.0	215	29.0	<u>215</u>	<u>29.0</u>	216	28.9
473.astar	394	17.8	390	18.0	<u>391</u>	<u>18.0</u>	363	19.3	359	19.6	<u>359</u>	<u>19.5</u>
483.xalancbmk	185	37.2	<u>185</u>	<u>37.2</u>	185	37.3	185	37.2	<u>185</u>	<u>37.2</u>	185	37.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Platform Notes

Tested systems can be used with formfactors.org ATX 2.2 spec
PC Power and Cooling 600W power supply
System was configured with ATi RV530LE discrete graphics card

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc

456.hmmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.03.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Mon Jul 13 10:29:56 2009 by SPEC CPU2006 PS/PDF formatter v6323.