SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

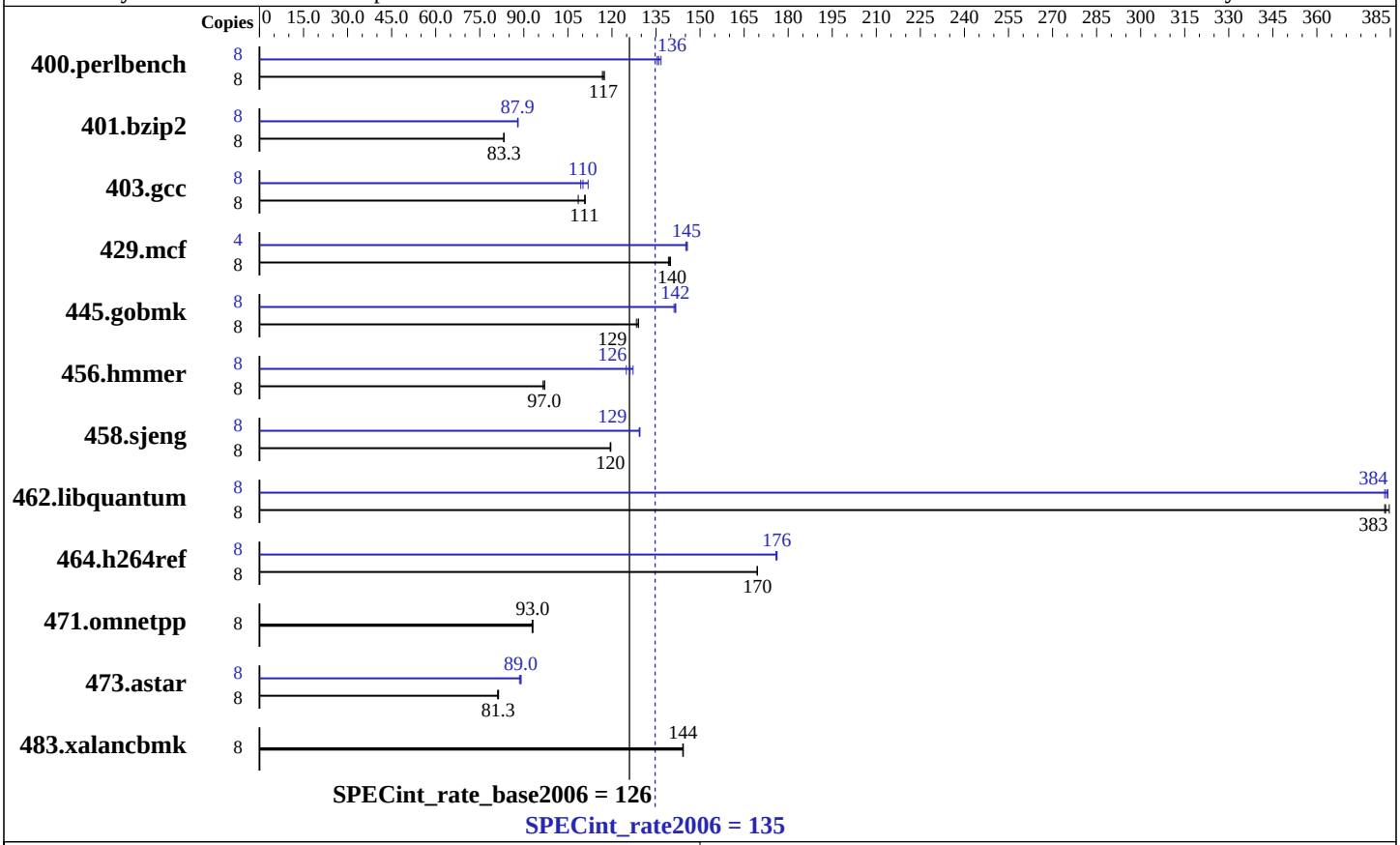
ASUS P6T WS PRO workstation motherboard (Intel Core i7-965 Extreme Edition)

SPECint®_rate2006 = 135

SPECint_rate_base2006 = 126

CPU2006 license: 009016
Test sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

Test date: Dec-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core i7-965 Extreme Edition
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (6 x 2GB DDR3-1333 ECC, CL=9)
Disk Subsystem:	Hitachi HDT725050VLA360 500GB SATAII, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**668**	**117**	666	117	669	117	8	**575**	**136**	577	135	572	137
401.bzip2	8	929	83.1	927	83.3	**927**	**83.3**	8	878	87.9	**878**	**87.9**	877	88.1
403.gcc	8	581	111	593	109	**582**	**111**	8	**585**	**110**	589	109	576	112
429.mcf	8	**523**	**140**	521	140	524	139	4	250	146	**251**	**145**	251	145
445.gobmk	8	**651**	**129**	654	128	650	129	8	**593**	**142**	592	142	595	141
456.hmmer	8	769	97.1	773	96.5	**769**	**97.0**	8	**592**	**126**	598	125	587	127
458.sjeng	8	811	119	809	120	**810**	**120**	8	747	130	749	129	**748**	**129**
462.libquantum	8	**432**	**383**	431	385	433	383	8	**432**	**384**	433	383	431	384
464.h264ref	8	**1044**	**170**	1044	170	1045	169	8	**1005**	**176**	1007	176	1005	176
471.omnetpp	8	539	92.8	**538**	**93.0**	537	93.1	8	539	92.8	**538**	**93.0**	537	93.1
473.astar	8	690	81.4	**691**	**81.3**	693	81.1	8	**631**	**89.0**	634	88.5	631	89.0
483.xalancbmk	8	383	144	**383**	**144**	383	144	8	383	144	**383**	**144**	383	144

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'numactl' used to bind processes to CPUs
```

Platform Notes

```
Tested systems can be used with formfactors.org ATX 2.2 spec
PC Power and Cooling 600W power supply
System was configured with ATi RV530LE discrete graphics card
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.03.xml`

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For other inquiries, please contact webmaster@spec.org.

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