SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

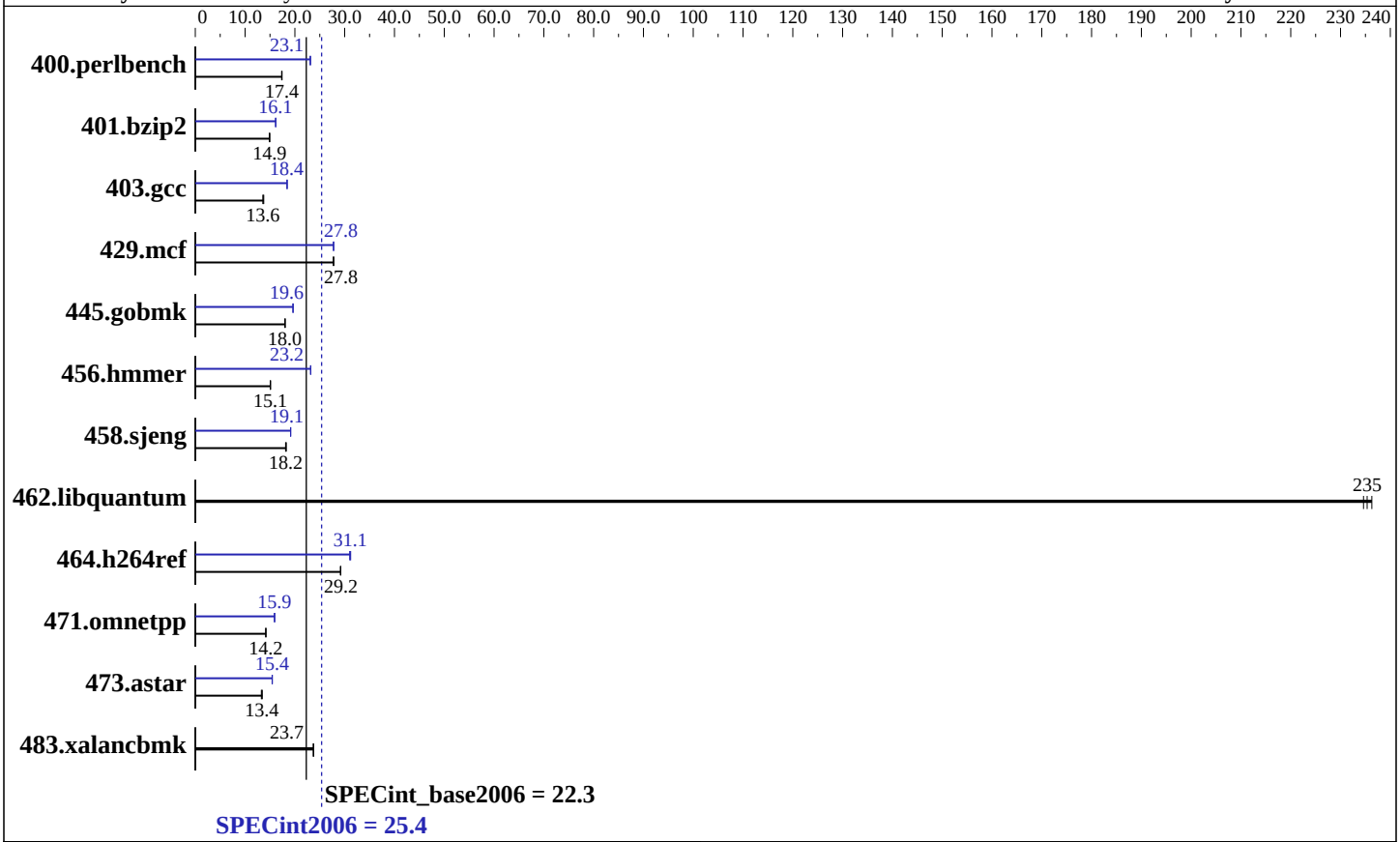
NTT System S. A.
NTT Tytan S8 Series

SPECint®2006 = 25.4

SPECint_base2006 = 22.3

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Jan-2009
Hardware Availability: Dec-2008
Software Availability: Dec-2008



Hardware

CPU Name:	Intel Xeon E5430
CPU Characteristics:	2.66 GHz, 2x6 MB P2 shared, 1333 MHz System Bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4 x 4GB DDR2-667 FBDIMM)
Disk Subsystem:	300 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP1, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NTT System S. A.
NTT Tytan S8 Series

SPECint2006 = 25.4

SPECint_base2006 = 22.3

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Jan-2009
Hardware Availability: Dec-2008
Software Availability: Dec-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	564	17.3	**562**	**17.4**	561	17.4	424	23.1	422	23.1	**423**	**23.1**
401.bzip2	649	14.9	646	14.9	**646**	**14.9**	597	16.2	**598**	**16.1**	598	16.1
403.gcc	**591**	**13.6**	592	13.6	586	13.7	**437**	**18.4**	437	18.4	437	18.4
429.mcf	329	27.7	**328**	**27.8**	328	27.8	328	27.8	**328**	**27.8**	329	27.8
445.gobmk	582	18.0	**582**	**18.0**	582	18.0	**534**	**19.6**	535	19.6	534	19.6
456.hmmer	619	15.1	618	15.1	**618**	**15.1**	**403**	**23.2**	402	23.2	403	23.2
458.sjeng	662	18.3	667	18.1	**665**	**18.2**	**632**	**19.1**	631	19.2	633	19.1
462.libquantum	88.3	235	**88.0**	**235**	87.7	236	88.3	235	**88.0**	**235**	87.7	236
464.h264ref	758	29.2	**758**	**29.2**	759	29.1	**711**	**31.1**	709	31.2	714	31.0
471.omnetpp	441	14.2	**441**	**14.2**	442	14.1	393	15.9	**392**	**15.9**	392	15.9
473.astar	522	13.4	529	13.3	**524**	**13.4**	453	15.5	455	15.4	**455**	**15.4**
483.xalancbmk	291	23.7	290	23.8	**291**	**23.7**	291	23.7	290	23.8	**291**	**23.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:36:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 February 2009.