SPEC® CINT2006 Result

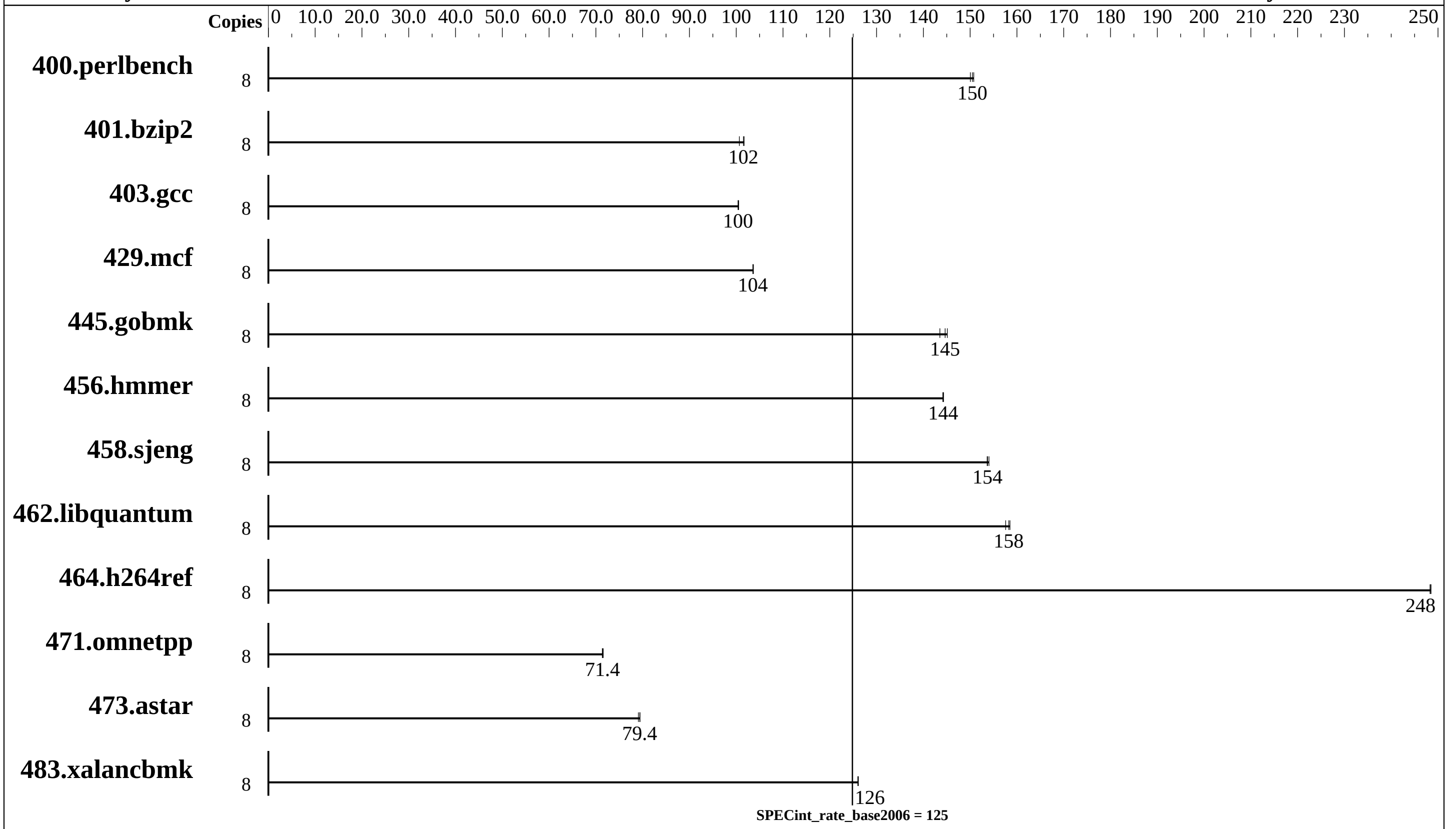
Itautec

Servidor Itautec MX221 (Intel Xeon E5440)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 125

CPU2006 license:	9001	**Test date:**	Jan-2009
Test sponsor:	Itautec	**Hardware Availability:**	Dec-2007
Tested by:	Itautec	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	
CPU MHz:	2830
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4 * 4 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SAS, 36GB, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_b_11.0.074
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	521	150	**519**	**150**	518	151							
401.bzip2	8	**760**	**102**	767	101	759	102							
403.gcc	8	641	100	641	101	**641**	**100**							
429.mcf	8	704	104	**704**	**104**	704	104							
445.gobmk	8	585	144	**580**	**145**	578	145							
456.hmmer	8	518	144	**517**	**144**	517	144							
458.sjeng	8	**630**	**154**	630	154	628	154							
462.libquantum	8	1052	158	**1047**	**158**	1046	159							
464.h264ref	8	713	248	**713**	**248**	712	248							
471.omnetpp	8	**700**	**71.4**	699	71.6	701	71.4							
473.astar	8	707	79.4	**707**	**79.4**	710	79.1							
483.xalancbmk	8	**438**	**126**	438	126	438	126							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.
```

Platform Notes

```
BIOS Settings:
Hardware Prefetch Disabled
```

General Notes

```
This result was measured on the Servidor Itautec MX221.
The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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