# SPEC® CINT2006 Result

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## Intel Corporation

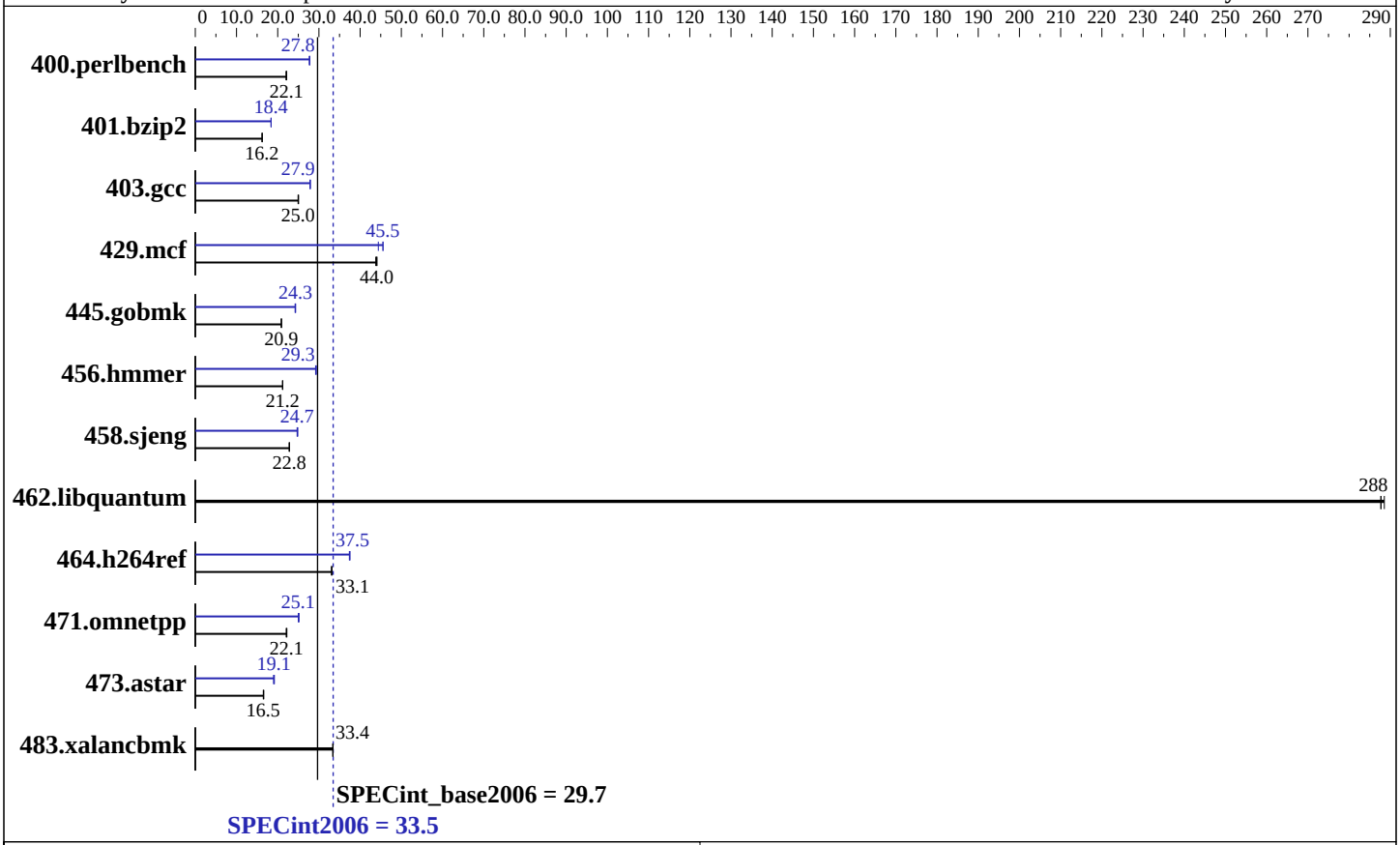
Supermicro SuperServer 6026T-NTR+ (Intel Xeon X5550, 2.66 GHz)

SPECint®2006 = 33.5

SPECint_base2006 = 29.7

**CPU2006 license:** 13
**Test sponsor:** Intel Corporation
**Tested by:** Intel Corporation

**Test date:** Mar-2009
**Hardware Availability:** Mar-2009
**Software Availability:** Feb-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5550 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.06 GHz |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1, 2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4GB DDR3-1333, CL9) |
| Disk Subsystem: | 1 x 150 GB SATA, 10000RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64 |
| Compiler: | Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1<br>Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 444 | 22.0 | 440 | 22.2 | **442** | **22.1** | 353 | 27.6 | 352 | 27.8 | **352** | **27.8** |
| 401.bzip2 | **595** | **16.2** | 594 | 16.2 | 595 | 16.2 | 525 | 18.4 | **525** | **18.4** | 524 | 18.4 |
| 403.gcc | 321 | 25.0 | **322** | **25.0** | 323 | 24.9 | **289** | **27.9** | 288 | 27.9 | 289 | 27.9 |
| 429.mcf | 208 | 43.8 | 207 | 44.1 | **207** | **44.0** | 205 | 44.4 | **200** | **45.5** | 200 | 45.6 |
| 445.gobmk | 502 | 20.9 | **502** | **20.9** | 502 | 20.9 | 431 | 24.3 | 433 | 24.2 | **432** | **24.3** |
| 456.hmmer | 440 | 21.2 | **440** | **21.2** | 440 | 21.2 | **319** | **29.3** | 318 | 29.4 | 320 | 29.2 |
| 458.sjeng | **530** | **22.8** | 530 | 22.8 | 531 | 22.8 | 486 | 24.9 | **489** | **24.7** | 490 | 24.7 |
| 462.libquantum | 71.8 | 289 | 72.0 | 288 | **72.0** | **288** | 71.8 | 289 | 72.0 | 288 | **72.0** | **288** |
| 464.h264ref | **668** | **33.1** | 668 | 33.1 | 670 | 33.1 | **590** | **37.5** | 590 | 37.5 | 592 | 37.4 |
| 471.omnetpp | 281 | 22.2 | 283 | 22.1 | **283** | **22.1** | 250 | 25.0 | 248 | 25.2 | **249** | **25.1** |
| 473.astar | 425 | 16.5 | **424** | **16.5** | 422 | 16.6 | 366 | 19.2 | 370 | 19.0 | **367** | **19.1** |
| 483.xalancbmk | 207 | 33.4 | 206 | 33.4 | **207** | **33.4** | 207 | 33.4 | 206 | 33.4 | **207** | **33.4** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -par-runtime-control -opt-prefetch`

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-prefetch`

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# Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml


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