



SPEC® CINT2006 Result

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Sun Microsystems

SPECint®2006 = 35.0

Sun Fire X2270 (Intel Xeon X5570 2.93GHz)

SPECint_base2006 = 30.5

CPU2006 license: 6

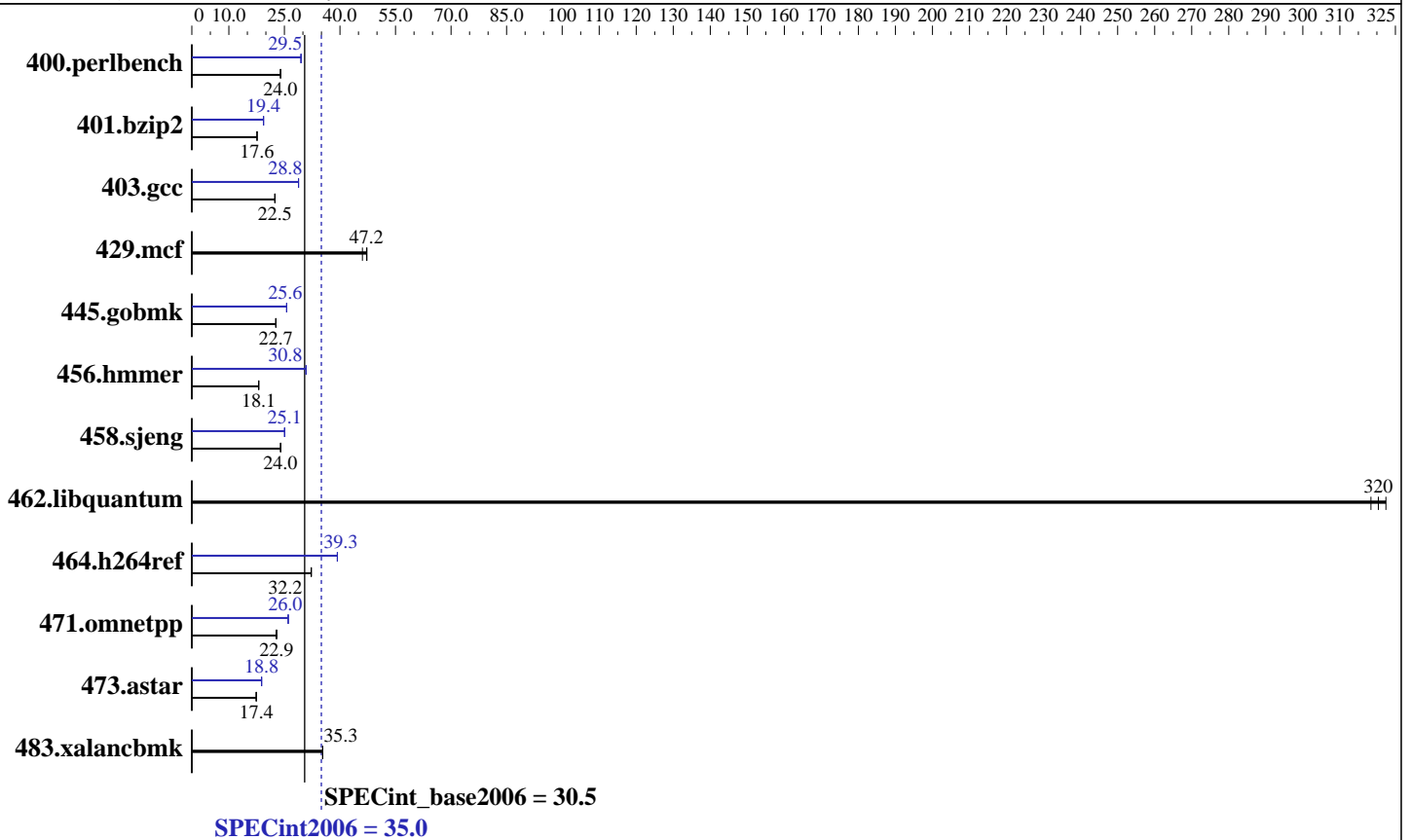
Test date: Mar-2009

Test sponsor: Sun Microsystems

Hardware Availability: Apr-2009

Tested by: Sun Microsystems

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1333)
 Disk Subsystem: 1 x 1 TB, SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: I_cprof_p_11.0.066, I_cprof_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	407	24.0	408	24.0	408	23.9	331	29.5	332	29.5	331	29.5
401.bzip2	548	17.6	548	17.6	548	17.6	498	19.4	498	19.4	498	19.4
403.gcc	358	22.5	358	22.5	360	22.4	280	28.8	279	28.8	279	28.8
429.mcf	198	46.1	193	47.3	193	47.2	198	46.1	193	47.3	193	47.2
445.gobmk	462	22.7	462	22.7	462	22.7	410	25.6	410	25.6	410	25.6
456.hammer	517	18.1	517	18.1	517	18.1	303	30.8	302	30.8	302	30.9
458.sjeng	505	24.0	504	24.0	505	24.0	483	25.1	482	25.1	483	25.0
462.libquantum	64.3	322	65.1	318	64.7	320	64.3	322	65.1	318	64.7	320
464.h264ref	686	32.2	685	32.3	687	32.2	563	39.3	563	39.3	563	39.3
471.omnetpp	275	22.7	272	23.0	273	22.9	240	26.0	240	26.0	240	26.0
473.astar	405	17.3	403	17.4	402	17.5	373	18.8	373	18.8	372	18.9
483.xalancbmk	195	35.3	195	35.3	195	35.3	195	35.3	195	35.3	195	35.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.07.xml>

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For other inquiries, please contact webmaster@spec.org.

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