SPEC® CINT2006 Result

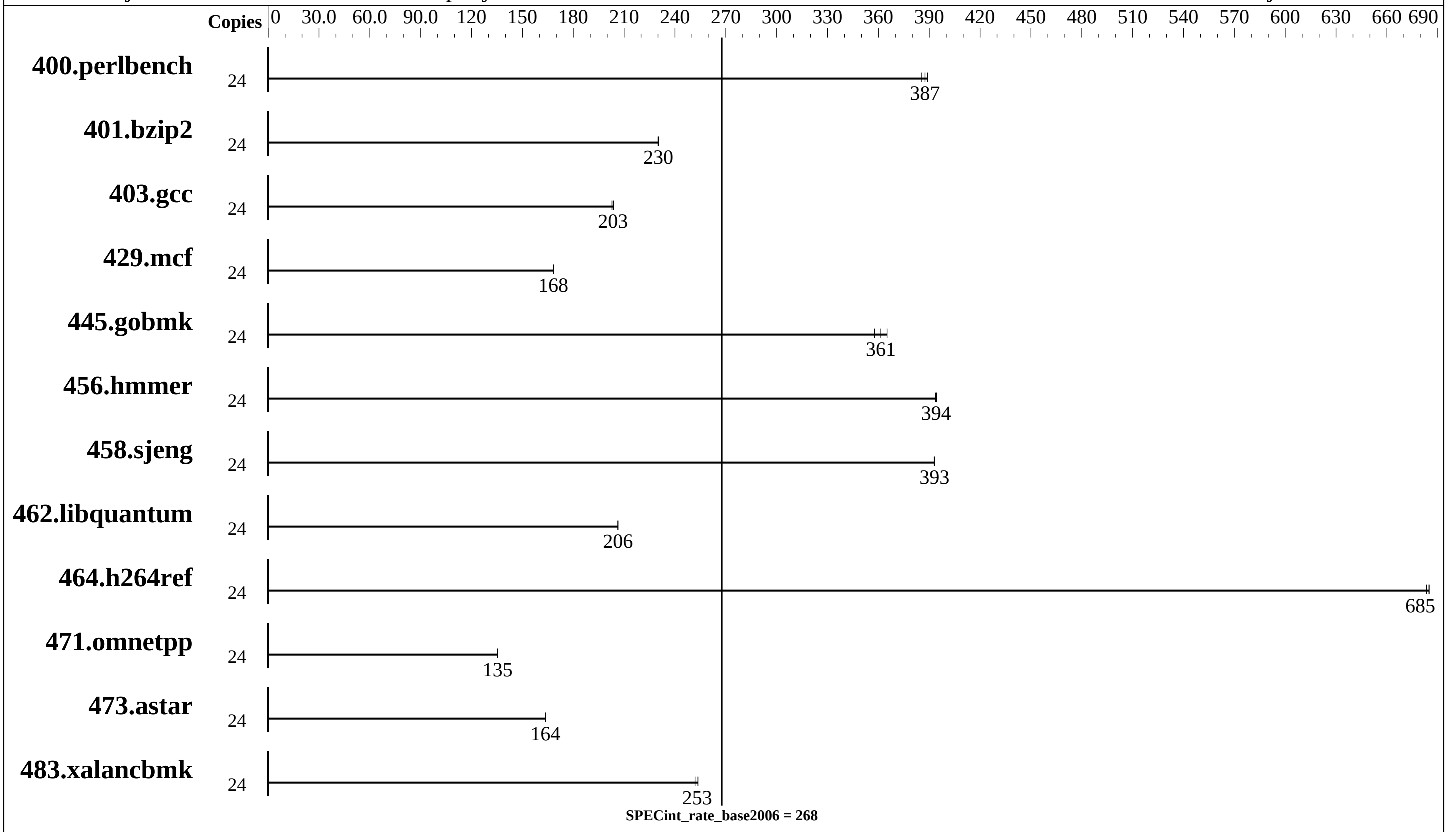
Hewlett-Packard Company

ProLiant DL580 G5
(2.66 GHz, Intel Xeon X7460)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 268

CPU2006 license: 3		**Test date:**	Apr-2009
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Sep-2008
Tested by:	Hewlett-Packard Company	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon X7460
CPU Characteristics:	2.66 GHz, 16 MB L3 shared, 1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	9 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (16x4 GB PC2-5300F CL5)
Disk Subsystem:	1x146GB 10 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**605**	**387**	603	389	608	386							
401.bzip2	24	1005	230	**1006**	**230**	1007	230							
403.gcc	24	953	203	**950**	**203**	948	204							
429.mcf	24	1300	168	**1301**	**168**	1302	168							
445.gobmk	24	**697**	**361**	704	358	690	365							
456.hmmer	24	**568**	**394**	569	394	568	394							
458.sjeng	24	**739**	**393**	739	393	738	393							
462.libquantum	24	2414	206	2409	206	**2409**	**206**							
464.h264ref	24	**776**	**685**	775	685	777	683							
471.omnetpp	24	1111	135	**1108**	**135**	1107	135							
473.astar	24	**1030**	**164**	1030	164	1031	163							
483.xalancbmk	24	**654**	**253**	653	254	657	252							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 '/usr/bin/tasket' used to bind processes to CPUs
 taskset was used to bind processes to cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
 BIOS configuration:
   Power Regulator set to Static High Performance Mode
   Adjacent Sector Prefetch Disabled
   Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 23:47:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 May 2009.