# SPEC® CINT2006 Result

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## Cisco Systems

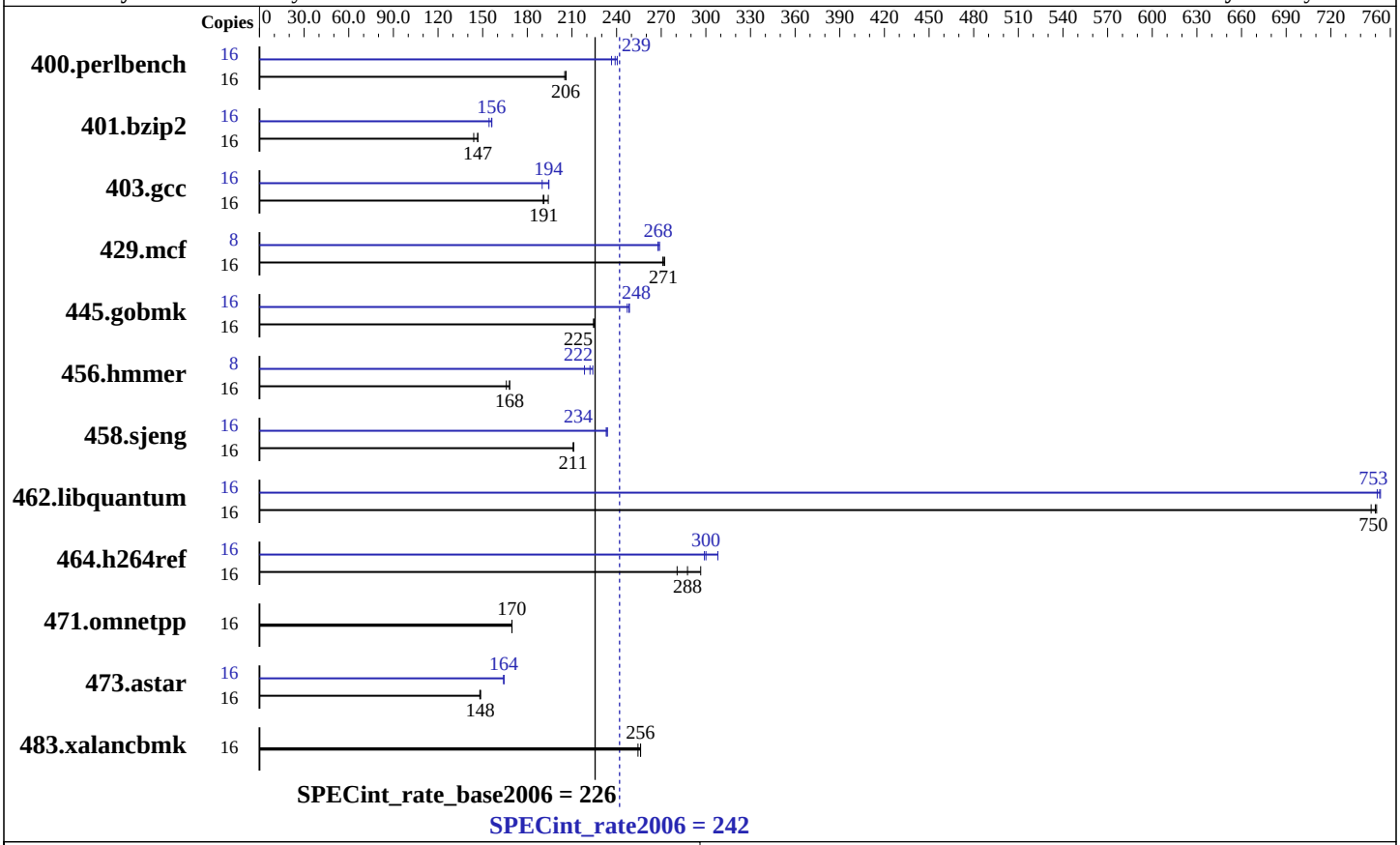
## Cisco B200-M1 (Intel Xeon X5550, 2.66 GHz)

SPECint®_rate2006 = 242

SPECint_rate_base2006 = 226

**CPU2006 license:** 9019
**Test sponsor:** Cisco Systems
**Tested by:** Cisco Systems

**Test date:** May-2009
**Hardware Availability:** May-2009
**Software Availability:** May-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5550 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.06 GHz |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1, 2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4GB DDR3 1333 MHz) |
| Disk Subsystem: | 73 GB SAS ST973451SS, 15000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | **759** | **206** | 758 | 206 | 762 | 205 | 16 | 650 | 240 | 661 | 237 | **654** | **239** |
| 401.bzip2 | 16 | 1052 | 147 | **1053** | **147** | 1072 | 144 | 16 | 1001 | 154 | **990** | **156** | 989 | 156 |
| 403.gcc | 16 | 675 | 191 | 664 | 194 | **674** | **191** | 16 | **663** | **194** | 678 | 190 | 662 | 195 |
| 429.mcf | 16 | **537** | **271** | 538 | 271 | 536 | 272 | 8 | 271 | 269 | 272 | 268 | **272** | **268** |
| 445.gobmk | 16 | **747** | **225** | 744 | 225 | 748 | 225 | 16 | 679 | 247 | **676** | **248** | 675 | 249 |
| 456.hmmer | 16 | 888 | 168 | **888** | **168** | 900 | 166 | 8 | 342 | 218 | **336** | **222** | 333 | 224 |
| 458.sjeng | 16 | **918** | **211** | 917 | 211 | 918 | 211 | 16 | 831 | 233 | 828 | 234 | **829** | **234** |
| 462.libquantum | 16 | 444 | 747 | **442** | **750** | 442 | 751 | 16 | 441 | 751 | **440** | **753** | 440 | 753 |
| 464.h264ref | 16 | 1194 | 297 | 1262 | 281 | **1231** | **288** | 16 | 1149 | 308 | **1180** | **300** | 1184 | 299 |
| 471.omnetpp | 16 | 589 | 170 | 589 | 170 | **589** | **170** | 16 | 589 | 170 | 589 | 170 | **589** | **170** |
| 473.astar | 16 | 758 | 148 | **757** | **148** | 756 | 149 | 16 | **684** | **164** | 684 | 164 | 684 | 164 |
| 483.xalancbmk | 16 | **431** | **256** | 434 | 254 | 431 | 256 | 16 | **431** | **256** | 434 | 254 | 431 | 256 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl --localalloc --physcpubind=$BIND was used to bind copies to the cores
 using following bind list:
 bind =  0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15
```

## Operating System Notes

```
 ulimit -s unlimited was used to set the stack size
```

## General Notes

```
Submitted_by: "Ven Immani (immaniv)" <immaniv@cisco.com>
Submitted: Wed Jun 10 17:31:06 EDT 2009
Submission: cpu2006-20090601-07562.sub

Submitted_by: "Ven Immani (immaniv)" <immaniv@cisco.com>
Submitted: Wed Jun 10 17:35:52 EDT 2009
Submission: cpu2006-20090601-07562.sub
```

## Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

Continued on next page

## Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml`


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For other inquiries, please contact webmaster@spec.org.

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