SPEC® CINT2006 Result

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Intel Corporation

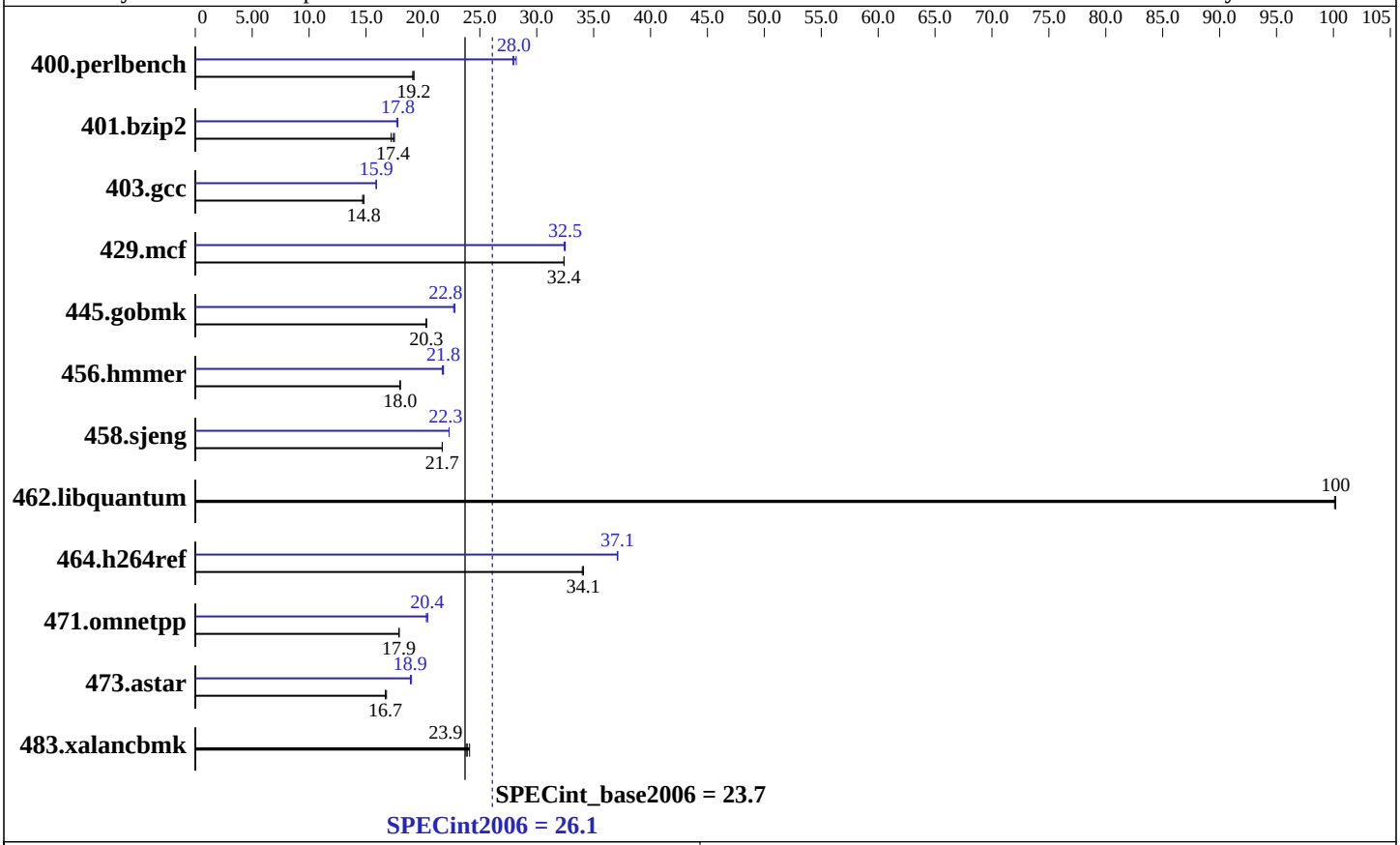
Lenovo T400 (Intel Core 2 Duo T9900)

SPECint®2006 = 26.1

SPECint_base2006 = 23.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo T9900
CPU Characteristics:	
CPU MHz:	3066
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron DDR3-1066 CL7)
Disk Subsystem:	Hitachi HTS722020K9SA00 200GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional w/ SP2 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	512	19.1	510	19.2	**510**	**19.2**	346	28.2	**349**	**28.0**	351	27.9
401.bzip2	560	17.2	**555**	**17.4**	552	17.5	**543**	**17.8**	544	17.7	543	17.8
403.gcc	546	14.7	545	14.8	**546**	**14.8**	505	15.9	505	15.9	**505**	**15.9**
429.mcf	282	32.4	281	32.4	**281**	**32.4**	282	32.4	281	32.5	**281**	**32.5**
445.gobmk	**516**	**20.3**	516	20.3	516	20.3	**461**	**22.8**	461	22.8	461	22.7
456.hmmer	**517**	**18.0**	518	18.0	517	18.0	428	21.8	**428**	**21.8**	430	21.7
458.sjeng	558	21.7	**558**	**21.7**	558	21.7	542	22.3	**542**	**22.3**	542	22.3
462.libquantum	207	100	207	100	**207**	**100**	207	100	207	100	**207**	**100**
464.h264ref	650	34.0	**649**	**34.1**	649	34.1	597	37.1	**597**	**37.1**	597	37.1
471.omnetpp	350	17.9	**349**	**17.9**	349	17.9	**307**	**20.4**	307	20.4	307	20.3
473.astar	421	16.7	**419**	**16.7**	419	16.8	369	19.0	**371**	**18.9**	372	18.9
483.xalancbmk	287	24.1	289	23.8	**289**	**23.9**	287	24.1	289	23.8	**289**	**23.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 1066 MHz
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE`

401.bzip2: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`/F512000000`

403.gcc: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml

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