SPEC® CINT2006 Result

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Intel Corporation

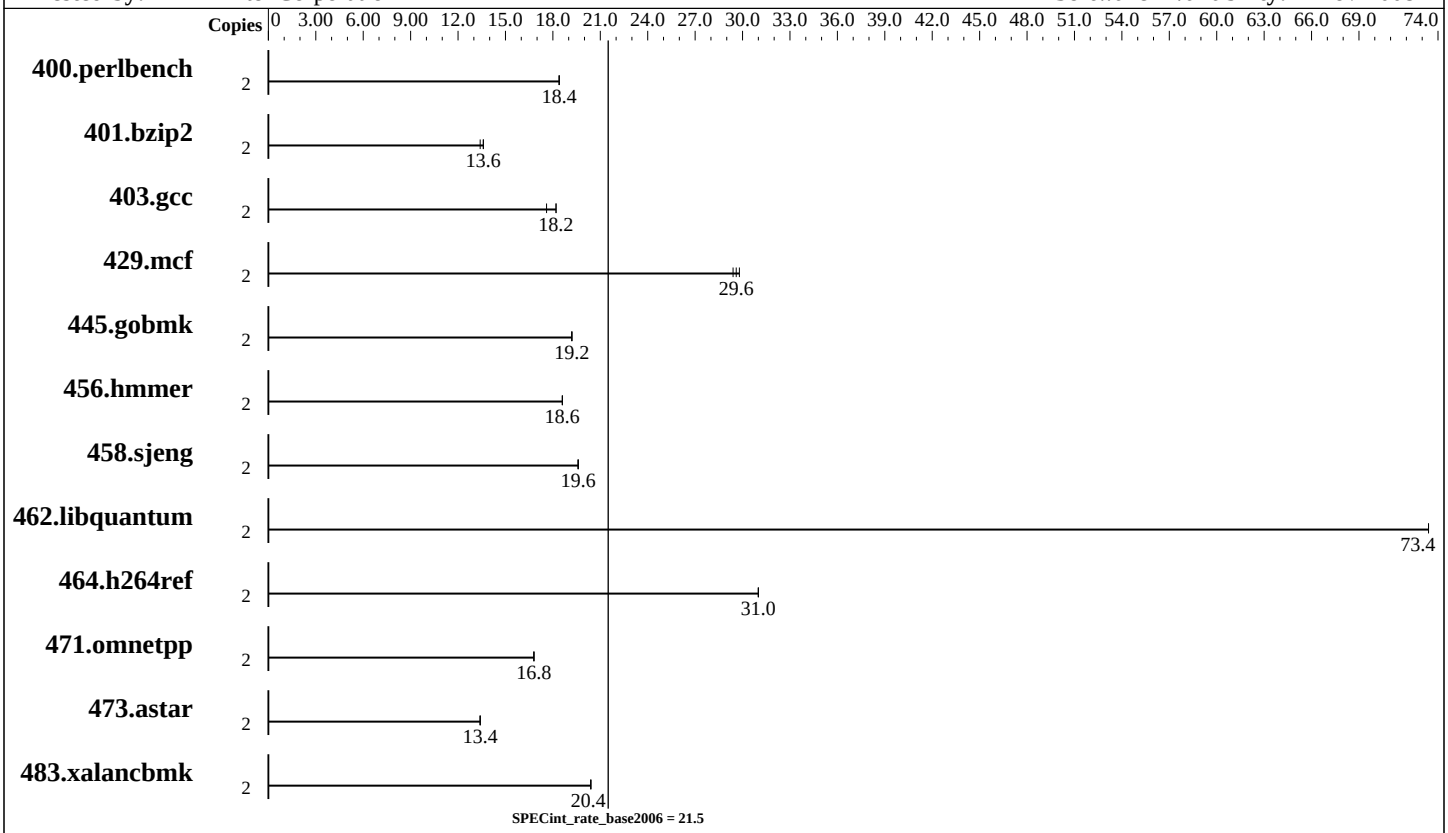
Acer Aspire 3810T (Intel Core 2 Duo SU9400)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 21.5

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo SU9400
CPU Characteristics:	
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	3 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB DDR3-1066 CL7; bios sets it to DDR2-800 CL5)
Disk Subsystem:	ST9250315AS 250 GB SATA, 5400RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Home Premium w/ SP1 (32-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	1058	18.4	1060	18.4	**1059**	**18.4**							
401.bzip2	2	1412	13.6	1430	13.4	**1423**	**13.6**							
403.gcc	2	885	18.2	912	17.6	**885**	**18.2**							
429.mcf	2	611	29.8	622	29.4	**618**	**29.6**							
445.gobmk	2	1098	19.2	1095	19.2	**1097**	**19.2**							
456.hmmer	2	**1003**	**18.6**	1001	18.6	1004	18.6							
458.sjeng	2	1234	19.6	**1233**	**19.6**	1232	19.6							
462.libquantum	2	**565**	**73.4**	565	73.4	564	73.4							
464.h264ref	2	1431	31.0	1430	31.0	**1430**	**31.0**							
471.omnetpp	2	747	16.8	748	16.8	**747**	**16.8**							
473.astar	2	1055	13.4	**1055**	**13.4**	1054	13.4							
483.xalancbmk	2	**679**	**20.4**	679	20.4	680	20.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
The system bus runs at 800 MHz
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 01:25:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 June 2009.