SPEC® CINT2006 Result

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Dell Inc.

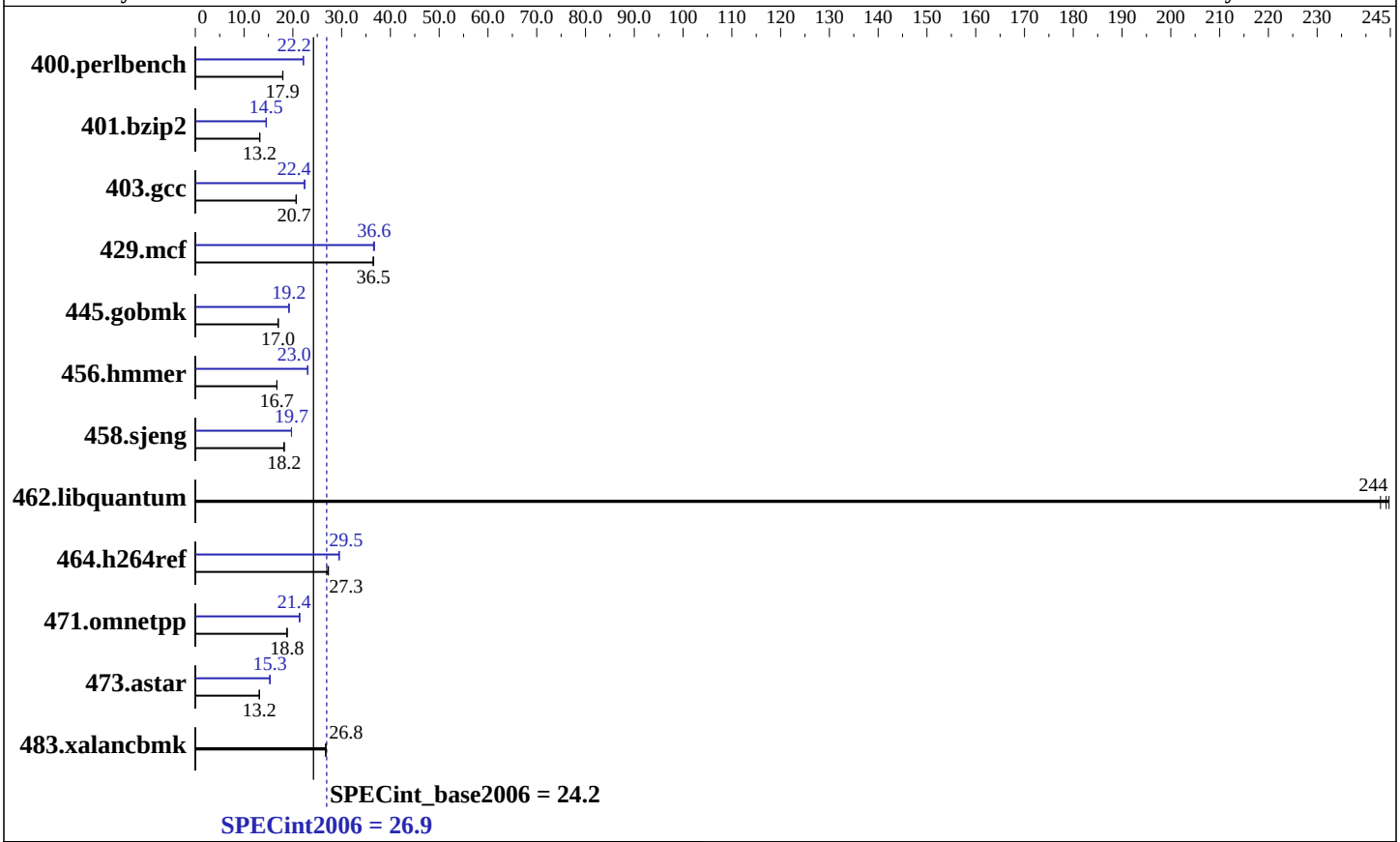
PowerEdge R410 (Intel Xeon E5520, 2.26 GHz)

SPECint®2006 = 26.9

SPECint_base2006 = 24.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3-1066 DR RDIMM)
Disk Subsystem:	1 x 250 GB 7200 RPM SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	544	18.0	546	17.9	**545**	**17.9**	441	22.1	**441**	**22.2**	441	22.2
401.bzip2	731	13.2	730	13.2	**730**	**13.2**	664	14.5	664	14.5	**664**	**14.5**
403.gcc	389	20.7	**389**	**20.7**	390	20.7	359	22.4	**359**	**22.4**	359	22.4
429.mcf	**250**	**36.5**	249	36.6	250	36.4	**249**	**36.6**	250	36.5	248	36.7
445.gobmk	617	17.0	**617**	**17.0**	617	17.0	546	19.2	546	19.2	**546**	**19.2**
456.hmmer	558	16.7	**558**	**16.7**	558	16.7	405	23.1	405	23.0	**405**	**23.0**
458.sjeng	**664**	**18.2**	665	18.2	664	18.2	**613**	**19.7**	614	19.7	613	19.7
462.libquantum	85.3	243	**84.9**	**244**	84.7	245	85.3	243	**84.9**	**244**	84.7	245
464.h264ref	821	27.0	811	27.3	**811**	**27.3**	**750**	**29.5**	750	29.5	752	29.4
471.omnetpp	332	18.8	**332**	**18.8**	332	18.8	293	21.3	292	21.4	**292**	**21.4**
473.astar	**534**	**13.2**	534	13.2	536	13.1	457	15.3	**459**	**15.3**	460	15.3
483.xalancbmk	**258**	**26.8**	258	26.7	258	26.8	**258**	**26.8**	258	26.7	258	26.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml`

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For other inquiries, please contact webmaster@spec.org.

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