



SPEC® CINT2006 Result

Copyright 2006-2009 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 21.0

PowerEdge T710 (Intel Xeon E5504, 2.00 GHz)

SPECint_base2006 = 19.0

CPU2006 license: 55

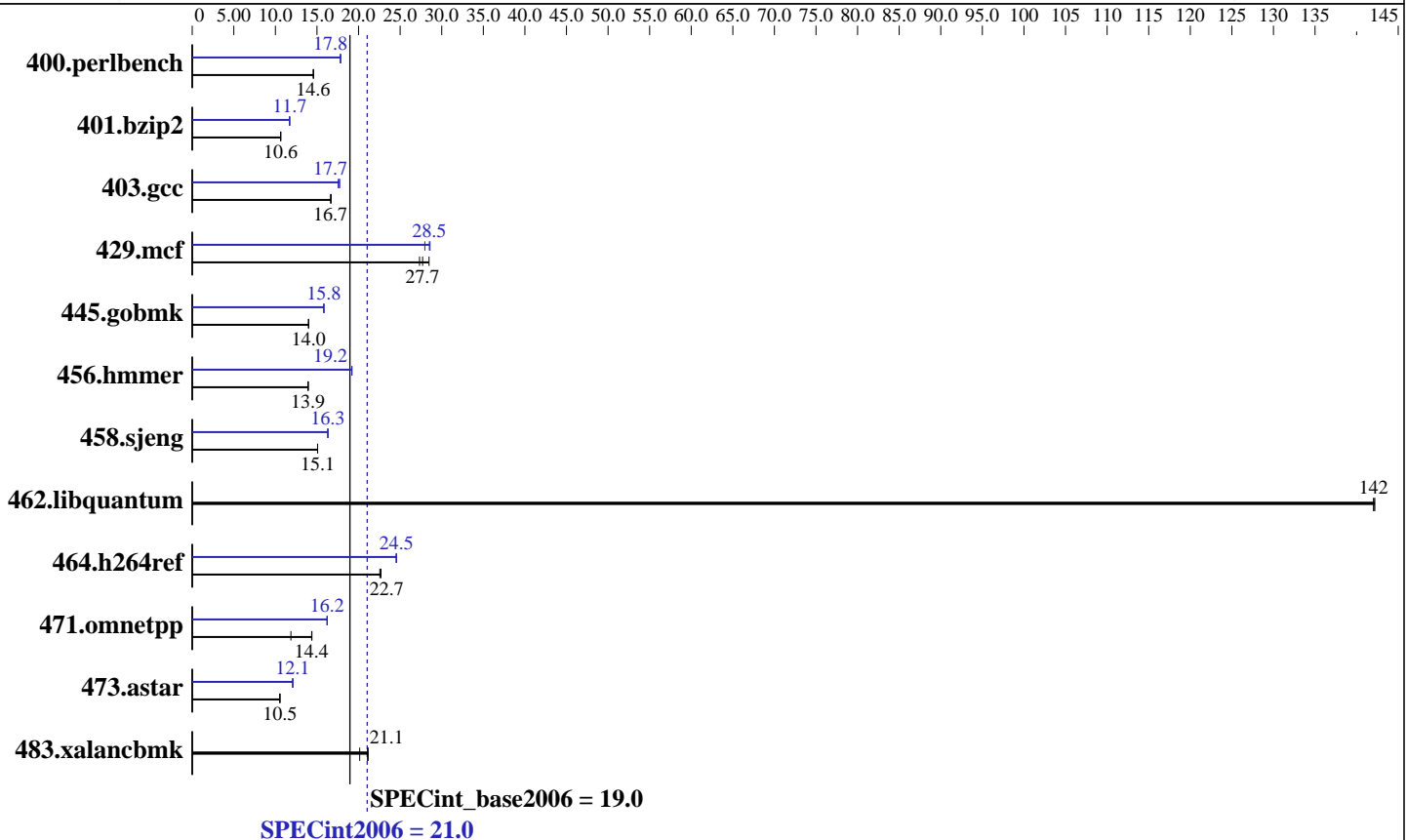
Test date: May-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5504
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2009 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 21.0

PowerEdge T710 (Intel Xeon E5504, 2.00 GHz)

SPECint_base2006 = 19.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>671</u>	<u>14.6</u>	671	14.6	669	14.6	548	17.8	<u>548</u>	<u>17.8</u>	548	17.8
401.bzip2	908	10.6	<u>907</u>	<u>10.6</u>	906	10.6	824	11.7	824	11.7	<u>824</u>	<u>11.7</u>
403.gcc	483	16.7	<u>483</u>	<u>16.7</u>	483	16.7	459	17.6	454	17.7	<u>455</u>	<u>17.7</u>
429.mcf	334	27.3	<u>329</u>	<u>27.7</u>	320	28.5	326	28.0	319	28.6	<u>320</u>	<u>28.5</u>
445.gobmk	<u>750</u>	<u>14.0</u>	750	14.0	750	14.0	<u>663</u>	<u>15.8</u>	663	15.8	662	15.8
456.hammer	669	13.9	670	13.9	<u>669</u>	<u>13.9</u>	486	19.2	486	19.2	<u>486</u>	<u>19.2</u>
458.sjeng	803	15.1	803	15.1	<u>803</u>	<u>15.1</u>	741	16.3	<u>742</u>	<u>16.3</u>	742	16.3
462.libquantum	<u>146</u>	<u>142</u>	146	142	146	142	<u>146</u>	<u>142</u>	146	142	146	142
464.h264ref	975	22.7	981	22.6	<u>976</u>	<u>22.7</u>	<u>902</u>	<u>24.5</u>	901	24.6	903	24.5
471.omnetpp	<u>435</u>	<u>14.4</u>	527	11.9	434	14.4	<u>385</u>	<u>16.2</u>	386	16.2	385	16.2
473.astar	<u>666</u>	<u>10.5</u>	667	10.5	665	10.6	580	12.1	581	12.1	<u>581</u>	<u>12.1</u>
483.xalancbmk	326	21.2	343	20.1	<u>327</u>	<u>21.1</u>	326	21.2	343	20.1	<u>327</u>	<u>21.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2009 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 21.0

PowerEdge T710 (Intel Xeon E5504, 2.00 GHz)

SPECint_base2006 = 19.0

CPU2006 license: 55

Test date: May-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2009 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 21.0

PowerEdge T710 (Intel Xeon E5504, 2.00 GHz)

SPECint_base2006 = 19.0

CPU2006 license: 55

Test date: May-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2009 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 21.0

PowerEdge T710 (Intel Xeon E5504, 2.00 GHz)

SPECint_base2006 = 19.0

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.01.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Sep 15 18:10:07 2009 by SPEC CPU2006 PS/PDF formatter v6323.