# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

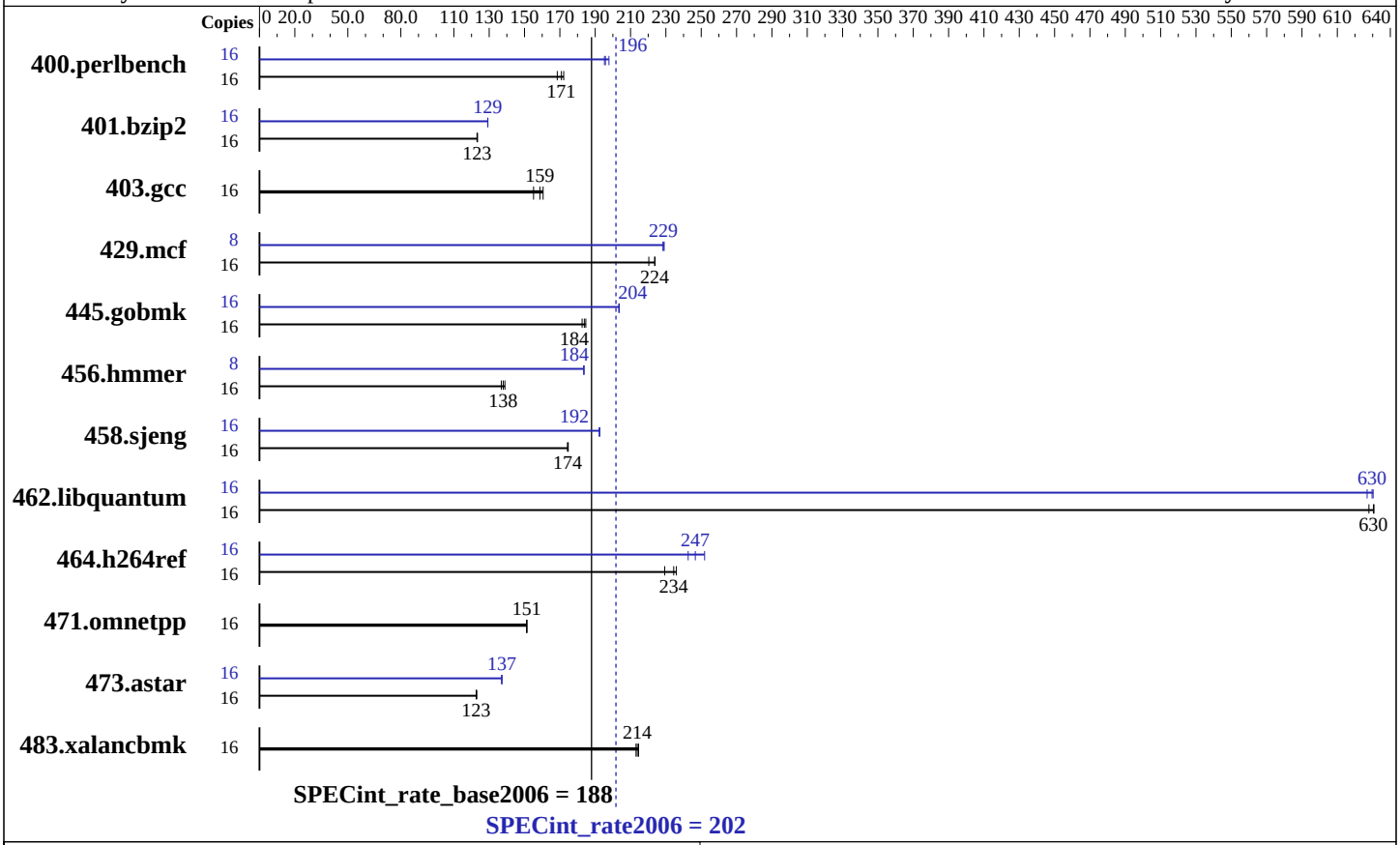
NovaScale R440 E2
(Intel Xeon L5520, 2.26 GHz)

SPECint®_rate2006 = 202

SPECint_rate_base2006 = 188

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** NEC Corporation

**Test date:** Aug-2009
**Hardware Availability:** Jul-2009
**Software Availability:** Feb-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon L5520 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.53 GHz |
| CPU MHz: | 2267 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC) |
| Disk Subsystem: | 1x146.5 GB SAS, 15000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp |
| Compiler: | Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502 |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440 E2
(Intel Xeon L5520, 2.26 GHz)

SPECint_rate2006 = 202

SPECint_rate_base2006 = 188

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** NEC Corporation

**Test date:** Aug-2009
**Hardware Availability:** Jul-2009
**Software Availability:** Feb-2009

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | **915** | **171** | 928 | 169 | 908 | 172 | 16 | **798** | **196** | 801 | 195 | 790 | 198 |
| 401.bzip2 | 16 | 1252 | 123 | 1254 | 123 | **1253** | **123** | 16 | 1195 | 129 | 1195 | 129 | **1195** | **129** |
| 403.gcc | 16 | 802 | 161 | **812** | **159** | 830 | 155 | 16 | 802 | 161 | **812** | **159** | 830 | 155 |
| 429.mcf | 16 | **653** | **224** | 662 | 220 | 652 | 224 | 8 | **319** | **229** | 320 | 228 | 319 | 229 |
| 445.gobmk | 16 | 919 | 183 | 909 | 185 | **913** | **184** | 16 | **825** | **204** | 824 | 204 | 825 | 203 |
| 456.hmmer | 16 | 1075 | 139 | 1091 | 137 | **1082** | **138** | 8 | 407 | 184 | 407 | 184 | **407** | **184** |
| 458.sjeng | 16 | 1108 | 175 | 1110 | 174 | **1110** | **174** | 16 | 1005 | 193 | **1006** | **192** | 1007 | 192 |
| 462.libquantum | 16 | **526** | **630** | 528 | 628 | 526 | 631 | 16 | 526 | 630 | **527** | **630** | 529 | 627 |
| 464.h264ref | 16 | **1510** | **234** | 1501 | 236 | 1544 | 229 | 16 | 1459 | 243 | **1436** | **247** | 1406 | 252 |
| 471.omnetpp | 16 | 661 | 151 | 661 | 151 | **661** | **151** | 16 | 661 | 151 | 661 | 151 | **661** | **151** |
| 473.astar | 16 | **915** | **123** | 913 | 123 | 916 | 123 | 16 | 819 | 137 | 819 | 137 | **819** | **137** |
| 483.xalancbmk | 16 | **516** | **214** | 518 | 213 | 515 | 215 | 16 | **516** | **214** | 518 | 213 | 515 | 215 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS setting:
NUMA configuration: Enabled
```

## General Notes

```
The NEC Express5800/R120a-1(Intel Xeon L5520),
the NEC Express5800/R120a-2(Intel Xeon L5520),
the Bull NovaScale R440 E2 (Intel Xeon L5520, 2.26 GHz) and
the Bull NovaScale R460 E2 (Intel Xeon L5520, 2.26 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R120a-1(Intel Xeon L5520) model.
```

# Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

Continued on next page

# Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64`

483.xalancbmk: `basepeak = yes`

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml`


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 02:58:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 September 2009.