SPEC® CINT2006 Result

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Dell Inc.

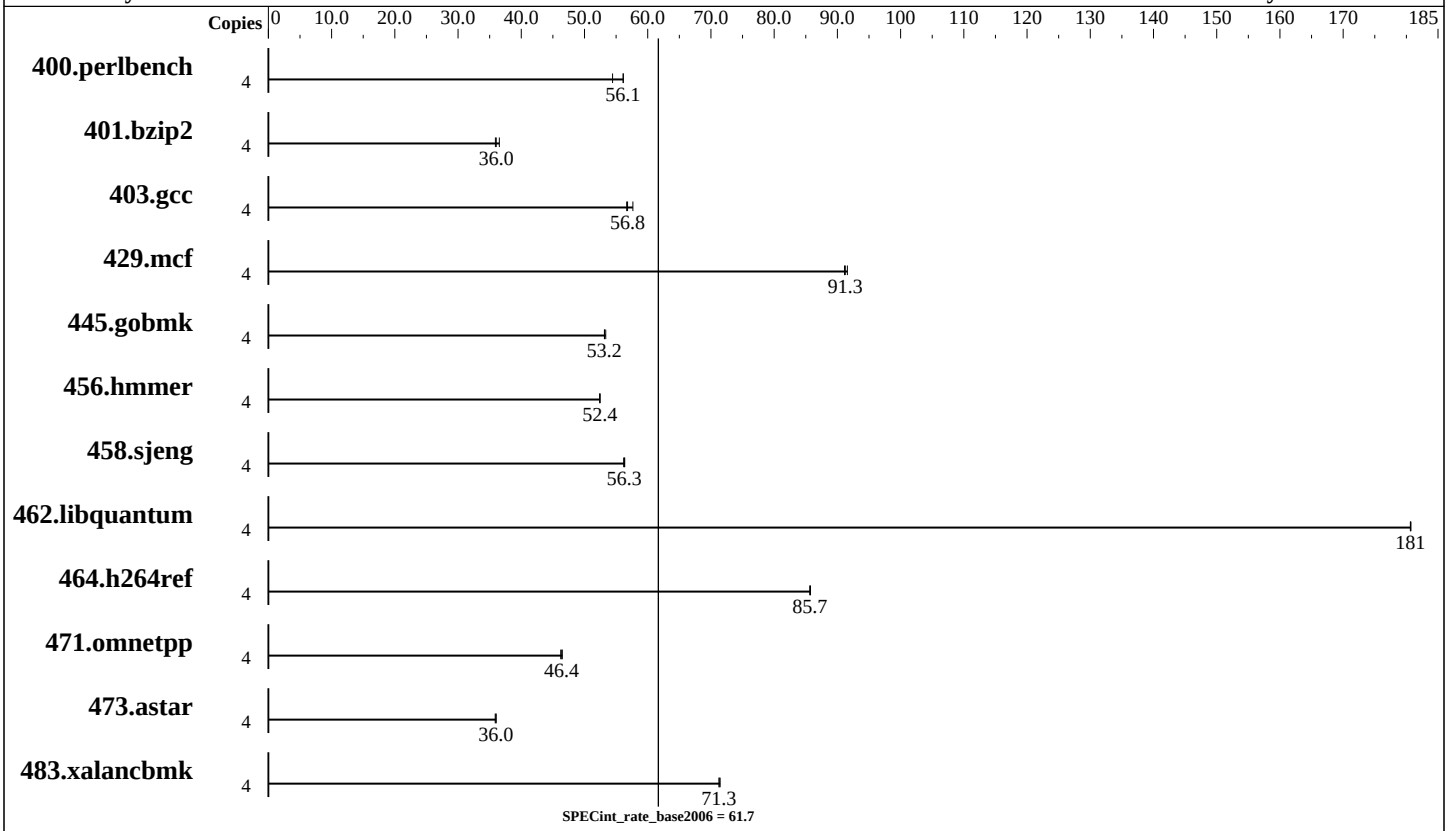
PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 61.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	718	54.4	696	56.1	**697**	**56.1**							
401.bzip2	4	1075	35.9	1057	36.5	**1071**	**36.0**							
403.gcc	4	568	56.7	**567**	**56.8**	558	57.7							
429.mcf	4	398	91.6	**400**	**91.3**	400	91.1							
445.gobmk	4	786	53.4	789	53.2	**789**	**53.2**							
456.hmmer	4	712	52.4	712	52.4	**712**	**52.4**							
458.sjeng	4	861	56.2	**860**	**56.3**	859	56.4							
462.libquantum	4	459	181	**459**	**181**	459	181							
464.h264ref	4	1034	85.6	**1033**	**85.7**	1032	85.7							
471.omnetpp	4	**539**	**46.4**	541	46.2	538	46.5							
473.astar	4	783	35.9	780	36.0	**780**	**36.0**							
483.xalancbmk	4	**387**	**71.3**	386	71.5	387	71.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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