SPEC® CFP2006 Result

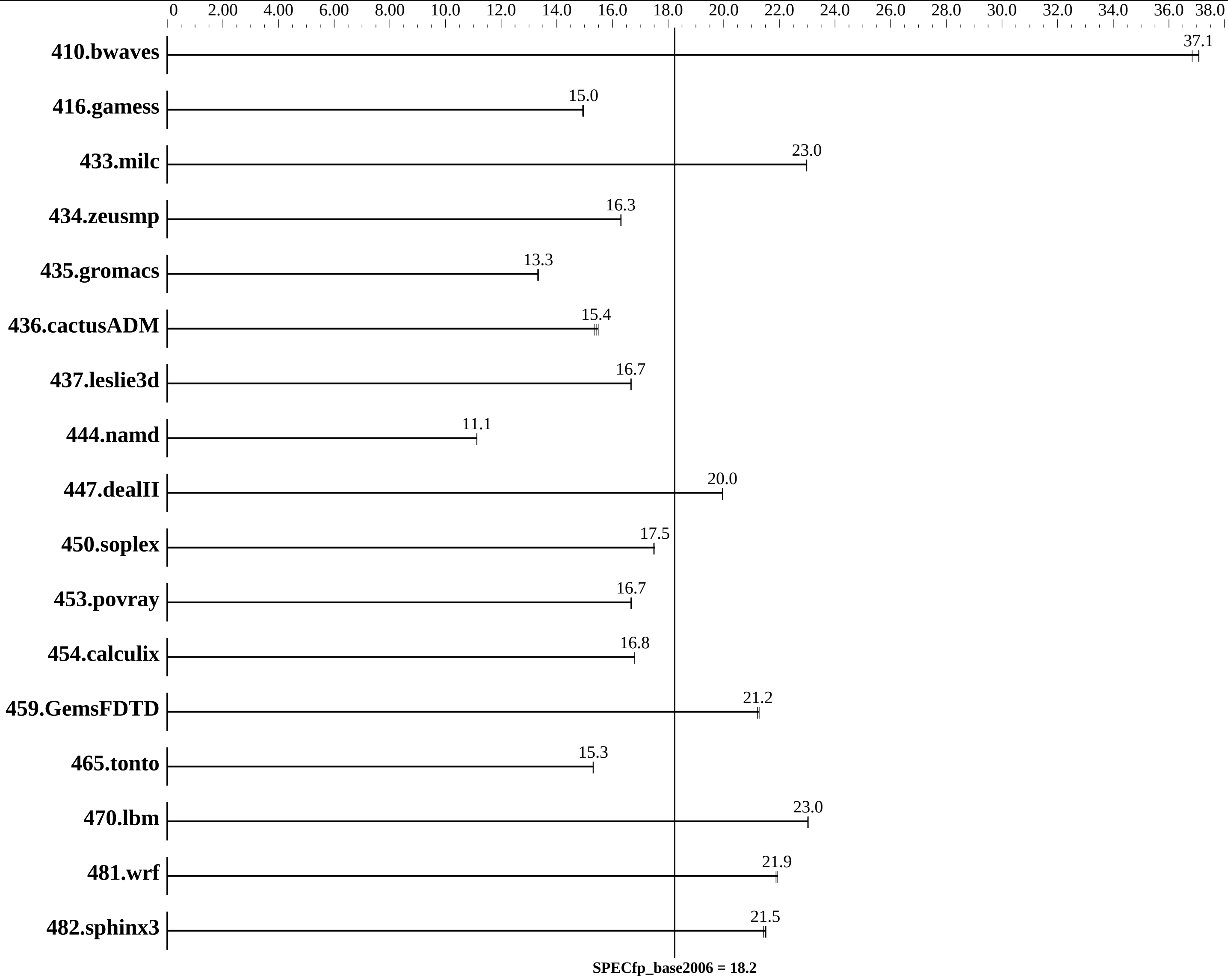
Dell Inc.

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECfp®2006 = Not Run

SPECfp_base2006 = 18.2

CPU2006 license:	55	**Test date:**	Sep-2009
Test sponsor:	Dell Inc.	**Hardware Availability:**	Mar-2009
Tested by:	Dell Inc.	**Software Availability:**	Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)

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L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	2 GB (2 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	Binutils 2.18.50.0.7.20080502

Results Table

	Base						Peak					
Benchmark	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
410.bwaves	369	36.8	**367**	**37.1**	366	37.1						
416.gamess	1313	14.9	1309	15.0	**1309**	**15.0**						
433.milc	399	23.0	400	23.0	**399**	**23.0**						
434.zeusmp	558	16.3	559	16.3	**558**	**16.3**						
435.gromacs	536	13.3	535	13.3	**535**	**13.3**						
436.cactusADM	779	15.3	771	15.5	**775**	**15.4**						
437.leslie3d	564	16.7	564	16.7	**564**	**16.7**						
444.namd	721	11.1	**721**	**11.1**	721	11.1						
447.dealII	573	20.0	**573**	**20.0**	573	20.0						
450.soplex	**476**	**17.5**	477	17.5	476	17.5						
453.povray	320	16.6	**319**	**16.7**	319	16.7						
454.calculix	491	16.8	**491**	**16.8**	491	16.8						
459.GemsFDTD	500	21.2	499	21.3	**500**	**21.2**						
465.tonto	**643**	**15.3**	643	15.3	643	15.3						
470.lbm	597	23.0	596	23.0	**596**	**23.0**						
481.wrf	**510**	**21.9**	509	21.9	511	21.9						
482.sphinx3	906	21.5	909	21.4	**907**	**21.5**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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