SPEC® CINT2006 Result

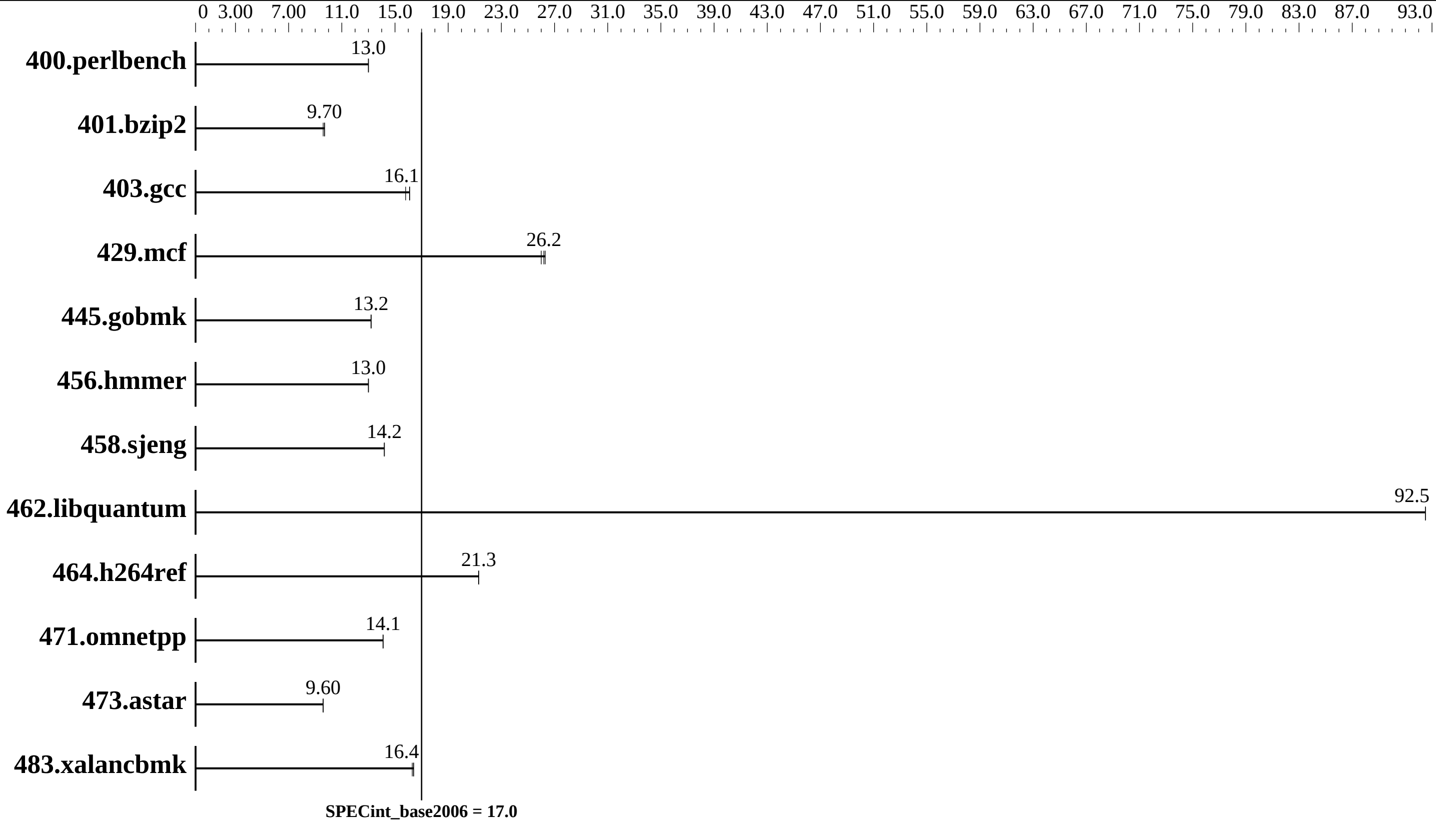
Dell Inc.

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint®2006 = Not Run

SPECint_base2006 = 17.0

CPU2006 license:	55	**Test date:**	Sep-2009
Test sponsor:	Dell Inc.	**Hardware Availability:**	Mar-2009
Tested by:	Dell Inc.	**Software Availability:**	May-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	2 GB (2 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2008 Enterprise x64 Edition SP2
Compiler:	Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

	Base						Peak					
Benchmark	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	752	13.0	749	13.0	**750**	**13.0**						
401.bzip2	992	9.70	1006	9.60	**995**	**9.70**						
403.gcc	510	15.8	501	16.1	**501**	**16.1**						
429.mcf	347	26.3	350	26.0	**348**	**26.2**						
445.gobmk	**795**	**13.2**	796	13.2	795	13.2						
456.hmmer	718	13.0	**718**	**13.0**	718	13.0						
458.sjeng	**854**	**14.2**	854	14.2	854	14.2						
462.libquantum	224	92.5	224	92.5	**224**	**92.5**						
464.h264ref	1040	21.3	**1041**	**21.3**	1041	21.3						
471.omnetpp	444	14.1	**444**	**14.1**	444	14.1						
473.astar	729	9.60	729	9.60	**729**	**9.60**						
483.xalancbmk	**422**	**16.4**	422	16.4	422	16.3						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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