# SPEC® CINT2006 Result

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## Fujitsu

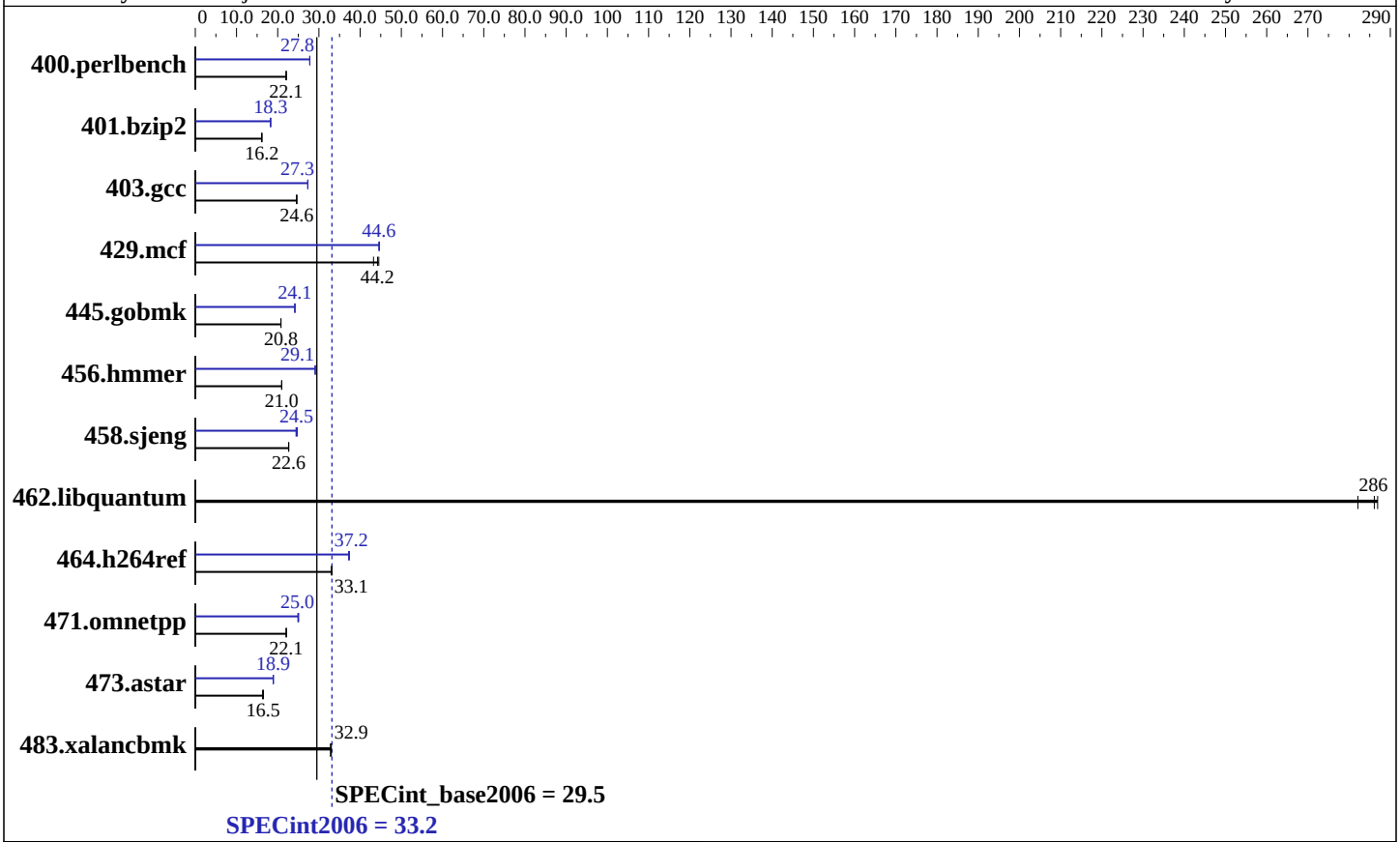
PRIMERGY BX620 S5, Intel Xeon X5550, 2.67 GHz

SPECint®2006 = 33.2

SPECint_base2006 = 29.5

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Oct-2009
**Hardware Availability:** Apr-2009
**Software Availability:** Feb-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5550 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.07 GHz |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SATA, 250 GB, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **443** | **22.1** | 442 | 22.1 | 444 | 22.0 | 352 | 27.7 | **351** | **27.8** | 351 | 27.8 |
| 401.bzip2 | **597** | **16.2** | 597 | 16.2 | 599 | 16.1 | **528** | **18.3** | 527 | 18.3 | 529 | 18.2 |
| 403.gcc | 328 | 24.6 | 326 | 24.7 | **327** | **24.6** | 295 | 27.3 | 295 | 27.3 | **295** | **27.3** |
| 429.mcf | 211 | 43.2 | **206** | **44.2** | 205 | 44.5 | 205 | 44.5 | **205** | **44.6** | 204 | 44.7 |
| 445.gobmk | 505 | 20.8 | **505** | **20.8** | 504 | 20.8 | 433 | 24.2 | 435 | 24.1 | **434** | **24.1** |
| 456.hmmer | 445 | 21.0 | **445** | **21.0** | 445 | 21.0 | 321 | 29.1 | **321** | **29.1** | 321 | 29.1 |
| 458.sjeng | 533 | 22.7 | 535 | 22.6 | **534** | **22.6** | 489 | 24.8 | **493** | **24.5** | 495 | 24.4 |
| 462.libquantum | 73.4 | 282 | **72.4** | **286** | 72.2 | 287 | 73.4 | 282 | **72.4** | **286** | 72.2 | 287 |
| 464.h264ref | **668** | **33.1** | 670 | 33.0 | 668 | 33.1 | 592 | 37.4 | **594** | **37.2** | 594 | 37.2 |
| 471.omnetpp | 284 | 22.0 | 283 | 22.1 | **283** | **22.1** | 251 | 24.9 | 249 | 25.1 | **250** | **25.0** |
| 473.astar | **426** | **16.5** | 425 | 16.5 | 430 | 16.3 | 370 | 19.0 | 372 | 18.9 | **371** | **18.9** |
| 483.xalancbmk | 210 | 32.8 | 209 | 33.0 | **210** | **32.9** | 210 | 32.8 | 209 | 33.0 | **210** | **32.9** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -par-runtime-control -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml`


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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:54:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 November 2009.