SPEC® CFP2006 Result

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Dell Inc.

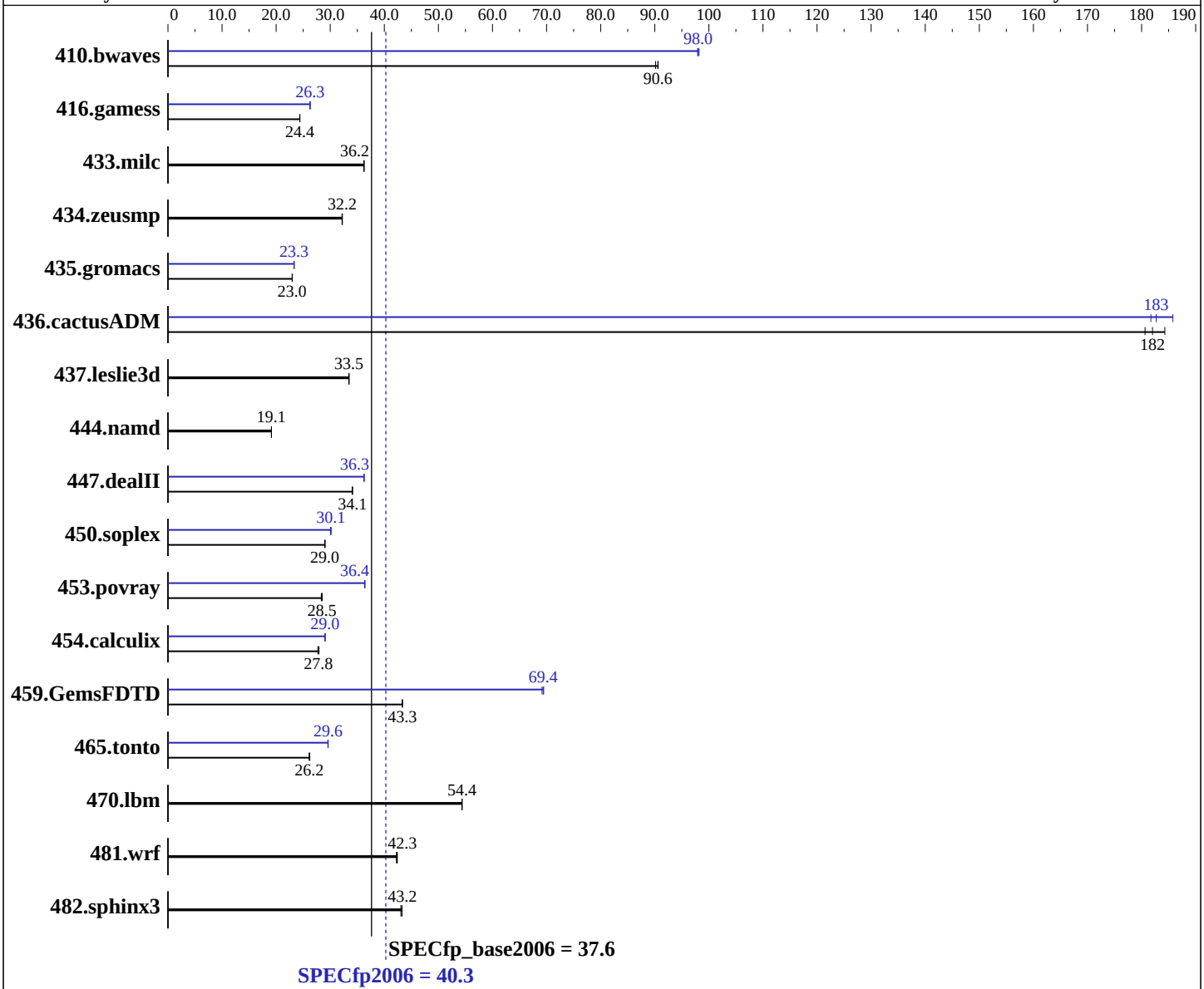
PowerEdge R510 (Intel Xeon X5570, 2.93 GHz)

SPECfp®2006 = 40.3

SPECfp_base2006 = 37.6

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2009
Hardware Availability: Oct-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)

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L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3-1333 DR RDIMM)
Disk Subsystem:	1 x 500 GB 7200 RPM SATA
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	151	90.2	**150**	**90.6**	150	90.6	138	98.2	139	97.9	**139**	**98.0**
416.gamess	**803**	**24.4**	803	24.4	804	24.4	746	26.3	745	26.3	**745**	**26.3**
433.milc	**253**	**36.2**	253	36.2	253	36.3	**253**	**36.2**	253	36.2	253	36.3
434.zeusmp	**283**	**32.2**	283	32.2	282	32.3	**283**	**32.2**	283	32.2	282	32.3
435.gromacs	**311**	**23.0**	311	23.0	311	23.0	**306**	**23.3**	307	23.3	306	23.3
436.cactusADM	64.8	184	66.2	181	**65.7**	**182**	**65.4**	**183**	64.3	186	65.8	182
437.leslie3d	**281**	**33.5**	281	33.5	281	33.5	**281**	**33.5**	281	33.5	281	33.5
444.namd	419	19.1	**419**	**19.1**	419	19.1	419	19.1	**419**	**19.1**	419	19.1
447.dealII	336	34.1	335	34.1	**335**	**34.1**	316	36.2	**316**	**36.3**	315	36.3
450.soplex	287	29.0	**287**	**29.0**	287	29.0	**277**	**30.1**	277	30.1	277	30.1
453.povray	188	28.3	186	28.5	**187**	**28.5**	146	36.4	**146**	**36.4**	146	36.4
454.calculix	295	27.9	298	27.7	**297**	**27.8**	284	29.0	**284**	**29.0**	284	29.1
459.GemsFDTD	245	43.3	245	43.4	**245**	**43.3**	153	69.2	153	69.5	**153**	**69.4**
465.tonto	375	26.2	377	26.1	**376**	**26.2**	**332**	**29.6**	332	29.6	333	29.6
470.lbm	253	54.4	253	54.3	**253**	**54.4**	253	54.4	253	54.3	**253**	**54.4**
481.wrf	263	42.4	**264**	**42.3**	264	42.2	263	42.4	**264**	**42.3**	264	42.2
482.sphinx3	**451**	**43.2**	450	43.3	453	43.1	**451**	**43.2**	450	43.3	453	43.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks (except as noted below):
icpc

450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.xml

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For other inquiries, please contact webmaster@spec.org.

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