SPEC® CFP2006 Result

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Bull SAS

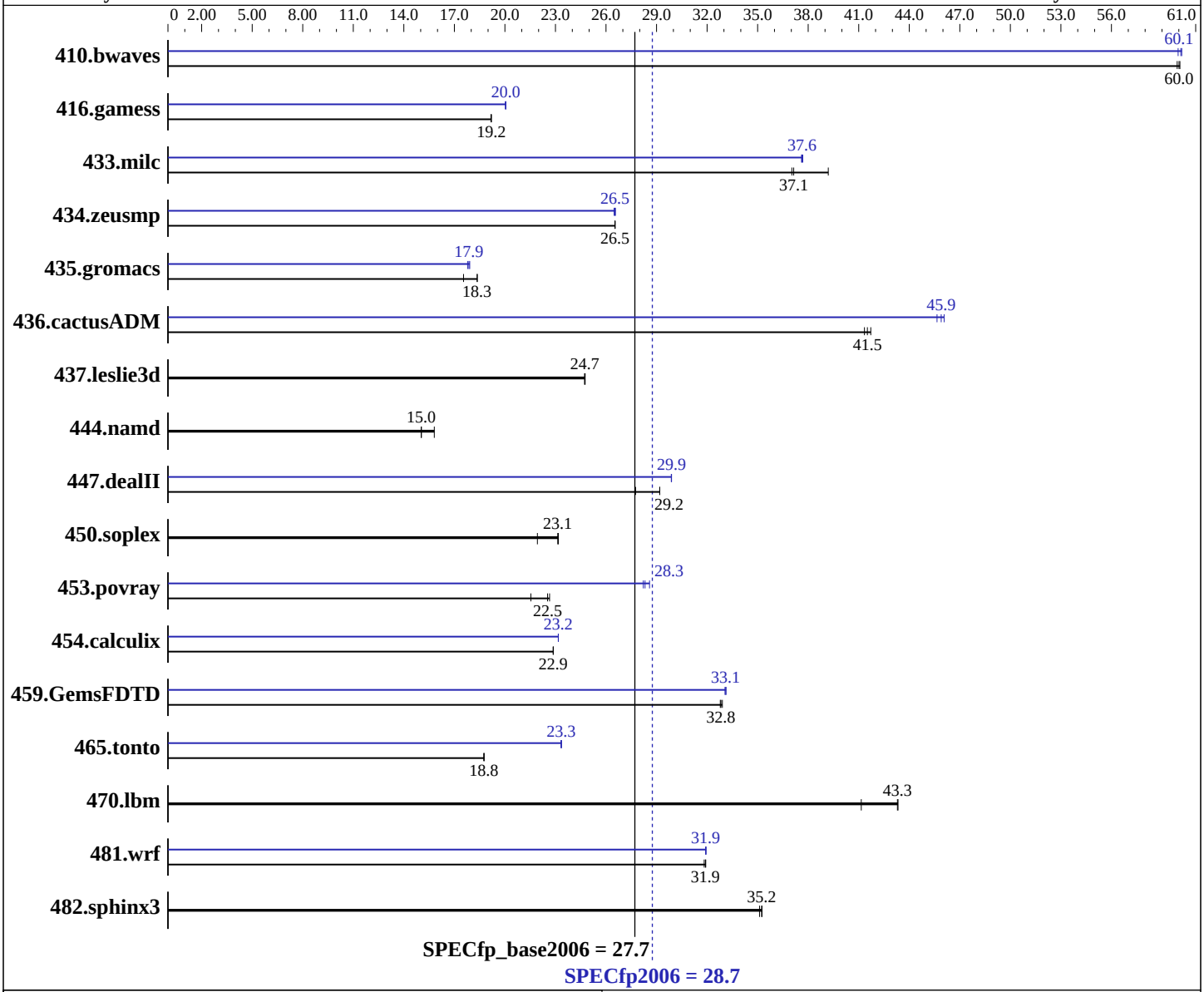
NovaScale R410 F2 (Intel Xeon X3430, 2.40 GHz)

SPECfp®2006 = 28.7

SPECfp_base2006 = 27.7

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Dell Inc.

Test date: Oct-2009
Hardware Availability: Dec-2009
Software Availability: Jul-2009



Hardware

CPU Name:	Intel Xeon X3430
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
Compiler:	Intel Fortran Compiler and Intel C++ Compiler Professional Edition 11.1 For Linux Build 20090511 Package ID: l_cproc_p_11.1.040, l_cprof_p_11.1.040
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)

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L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
Disk Subsystem:	1 x 160 GB 7200 RPM SATA
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	227	59.9	**226**	**60.0**	226	60.1	227	60.0	226	60.2	**226**	**60.1**
416.gamess	1021	19.2	**1020**	**19.2**	1020	19.2	976	20.1	978	20.0	**977**	**20.0**
433.milc	234	39.2	**247**	**37.1**	248	37.0	**244**	**37.6**	244	37.7	244	37.6
434.zeusmp	**343**	**26.5**	343	26.5	343	26.5	344	26.5	343	26.6	**343**	**26.5**
435.gromacs	407	17.5	**389**	**18.3**	389	18.4	401	17.8	399	17.9	**400**	**17.9**
436.cactusADM	**288**	**41.5**	286	41.7	289	41.3	259	46.1	262	45.6	**260**	**45.9**
437.leslie3d	**380**	**24.7**	380	24.8	380	24.7	**380**	**24.7**	380	24.8	380	24.7
444.namd	**533**	**15.0**	507	15.8	533	15.0	**533**	**15.0**	507	15.8	533	15.0
447.dealII	**392**	**29.2**	412	27.8	392	29.2	383	29.9	**383**	**29.9**	383	29.9
450.soplex	**360**	**23.1**	380	21.9	360	23.2	**360**	**23.1**	380	21.9	360	23.2
453.povray	247	21.5	235	22.7	**236**	**22.5**	186	28.6	**188**	**28.3**	189	28.2
454.calculix	361	22.9	361	22.9	**361**	**22.9**	356	23.2	356	23.2	**356**	**23.2**
459.GemsFDTD	**323**	**32.8**	322	32.9	324	32.8	320	33.1	321	33.1	**321**	**33.1**
465.tonto	525	18.8	524	18.8	**525**	**18.8**	**422**	**23.3**	422	23.3	421	23.4
470.lbm	334	41.2	317	43.3	**317**	**43.3**	334	41.2	317	43.3	**317**	**43.3**
481.wrf	**350**	**31.9**	350	31.9	351	31.8	350	31.9	**350**	**31.9**	350	31.9
482.sphinx3	555	35.1	553	35.3	**553**	**35.2**	555	35.1	553	35.3	**553**	**35.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
The Dell PowerEdge R210 (Intel Xeon X3430, 2.40 GHz) and
the Bull NovaScale R410 F2 (Intel Xeon X3430, 2.40 GHz) models are electronically equivalent.
The results have been measured on a Dell PowerEdge R210 (Intel Xeon X3430, 2.40 GHz) model.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: Same as 410.bwaves

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.xml`

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:02:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 December 2009.