SPEC® CFP2006 Result

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Acer Incorporated

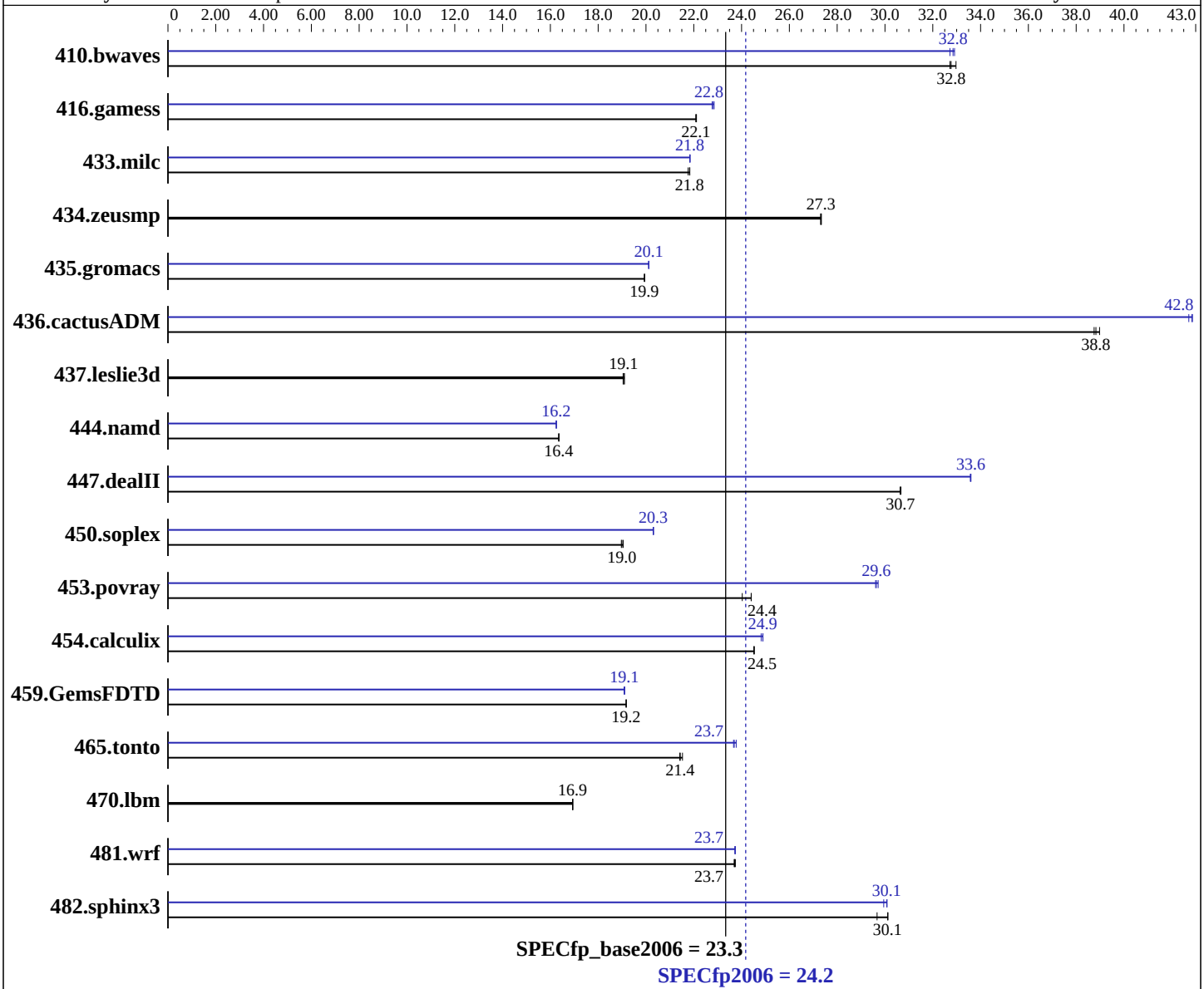
Gateway GT110 F1 (Intel Xeon E3110)

SPECfp®2006 = 24.2

SPECfp_base2006 = 23.3

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Nov-2009
Hardware Availability: Dec-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E3110
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4 x 2048 MB ECC Ubuffered DDR2-800 CL5)
Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	412	33.0	**415**	**32.8**	415	32.7	415	32.7	413	32.9	**414**	**32.8**
416.gamess	885	22.1	**886**	**22.1**	886	22.1	857	22.9	860	22.8	**858**	**22.8**
433.milc	422	21.8	420	21.8	**421**	**21.8**	420	21.8	**420**	**21.8**	420	21.8
434.zeusmp	333	27.3	**333**	**27.3**	333	27.3	333	27.3	**333**	**27.3**	333	27.3
435.gromacs	**358**	**19.9**	358	19.9	358	19.9	355	20.1	**355**	**20.1**	355	20.1
436.cactusADM	308	38.8	307	39.0	**308**	**38.8**	280	42.7	**279**	**42.8**	279	42.9
437.leslie3d	492	19.1	**493**	**19.1**	494	19.0	492	19.1	**493**	**19.1**	494	19.0
444.namd	491	16.3	**490**	**16.4**	490	16.4	**494**	**16.2**	494	16.2	493	16.3
447.dealII	373	30.6	373	30.7	**373**	**30.7**	341	33.6	**341**	**33.6**	341	33.6
450.soplex	438	19.1	**439**	**19.0**	440	19.0	411	20.3	410	20.3	**411**	**20.3**
453.povray	221	24.0	**218**	**24.4**	218	24.4	179	29.7	180	29.6	**179**	**29.6**
454.calculix	336	24.5	**336**	**24.5**	336	24.5	332	24.8	**332**	**24.9**	331	24.9
459.GemsFDTD	553	19.2	**553**	**19.2**	553	19.2	**556**	**19.1**	556	19.1	555	19.1
465.tonto	457	21.5	459	21.4	**459**	**21.4**	414	23.8	416	23.7	**415**	**23.7**
470.lbm	812	16.9	811	16.9	**811**	**16.9**	812	16.9	811	16.9	**811**	**16.9**
481.wrf	**471**	**23.7**	471	23.7	472	23.7	471	23.7	**471**	**23.7**	471	23.7
482.sphinx3	647	30.1	657	29.7	**647**	**30.1**	**648**	**30.1**	648	30.1	651	29.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M

This result was measured on the Gateway GT110 F1.
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc

450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.xml

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For other inquiries, please contact webmaster@spec.org.

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