



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]2006 = 27.6

Acer AT110 F1 (Intel Xeon E3110)

SPECint_base2006 = 24.7

CPU2006 license: 97

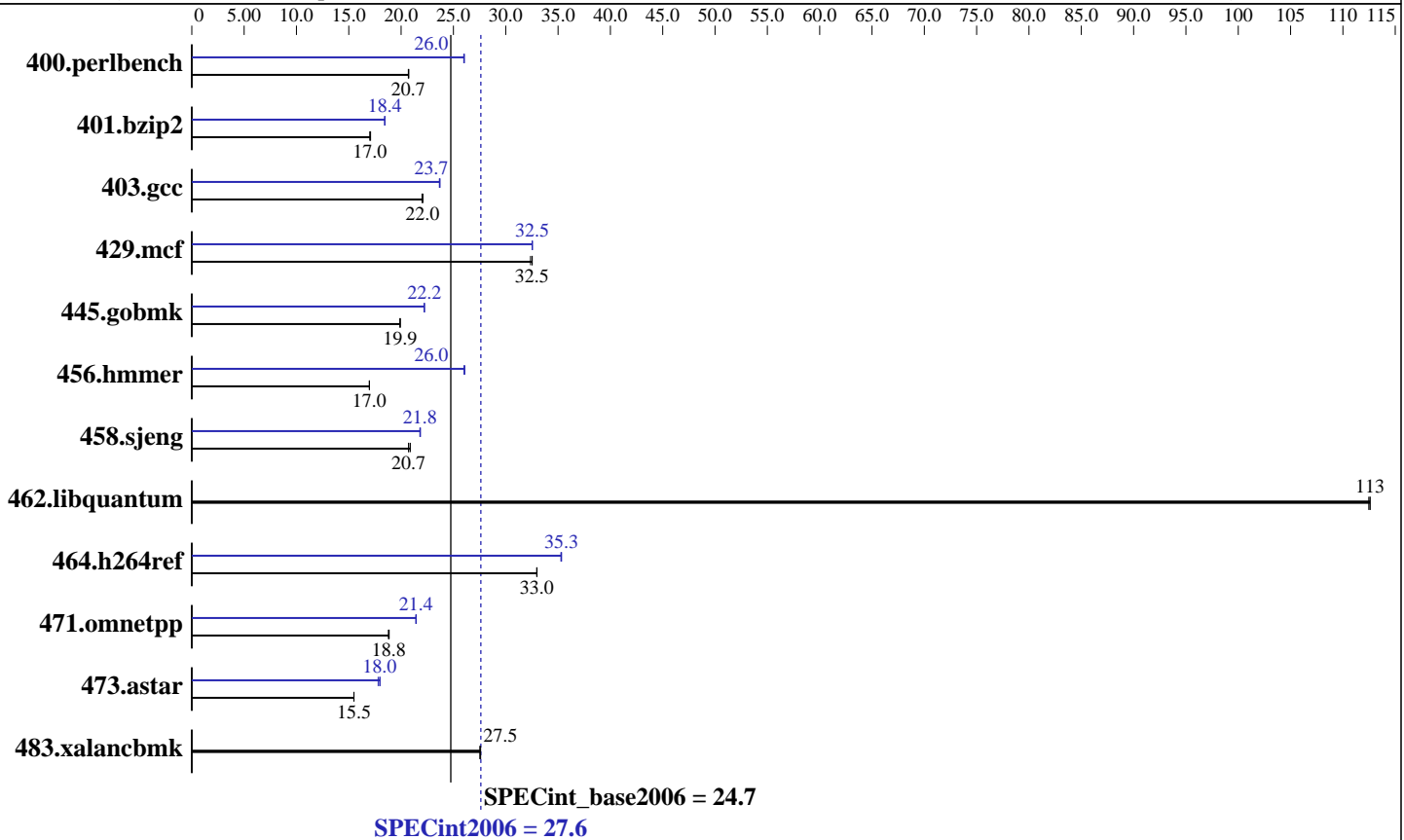
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Dec-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E3110
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
 Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Sserver 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>472</u>	<u>20.7</u>	471	20.7	472	20.7	376	26.0	<u>375</u>	<u>26.0</u>	375	26.0
401.bzip2	565	17.1	<u>567</u>	<u>17.0</u>	567	17.0	523	18.4	<u>523</u>	<u>18.4</u>	523	18.4
403.gcc	365	22.1	<u>365</u>	<u>22.0</u>	366	22.0	340	23.7	340	23.7	<u>340</u>	<u>23.7</u>
429.mcf	282	32.4	280	32.5	<u>281</u>	<u>32.5</u>	280	32.5	280	32.5	<u>280</u>	<u>32.5</u>
445.gobmk	527	19.9	527	19.9	<u>527</u>	<u>19.9</u>	<u>472</u>	<u>22.2</u>	472	22.2	472	22.2
456.hmmer	550	17.0	<u>550</u>	<u>17.0</u>	550	17.0	359	26.0	358	26.1	<u>358</u>	<u>26.0</u>
458.sjeng	579	20.9	584	20.7	<u>584</u>	<u>20.7</u>	554	21.9	<u>555</u>	<u>21.8</u>	555	21.8
462.libquantum	184	112	<u>184</u>	<u>113</u>	184	113	184	112	<u>184</u>	<u>113</u>	184	113
464.h264ref	<u>671</u>	<u>33.0</u>	671	33.0	671	33.0	<u>627</u>	<u>35.3</u>	626	35.3	627	35.3
471.omnetpp	331	18.9	<u>332</u>	<u>18.8</u>	333	18.8	<u>291</u>	<u>21.4</u>	292	21.4	291	21.4
473.astar	<u>453</u>	<u>15.5</u>	453	15.5	453	15.5	390	18.0	<u>391</u>	<u>18.0</u>	394	17.8
483.xalancbmk	250	27.6	<u>250</u>	<u>27.5</u>	251	27.5	250	27.6	<u>250</u>	<u>27.5</u>	251	27.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

This result was measured on the Gateway GT110 F1.
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -auto-ilp32 -opt-prefetch
            -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
          -opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
            -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
              -no-prec-div -static -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
              -no-prec-div -ansi-alias -opt-ra-region-strategy=block
              -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
            -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
            -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

```

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

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