



# SPEC® CFP2006 Result

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## Acer Incorporated

**SPECfp®2006 = 26.3**

## Gateway GT110 F1 (Intel Xeon X3380)

**SPECfp\_base2006 = 25.5**

CPU2006 license: 97

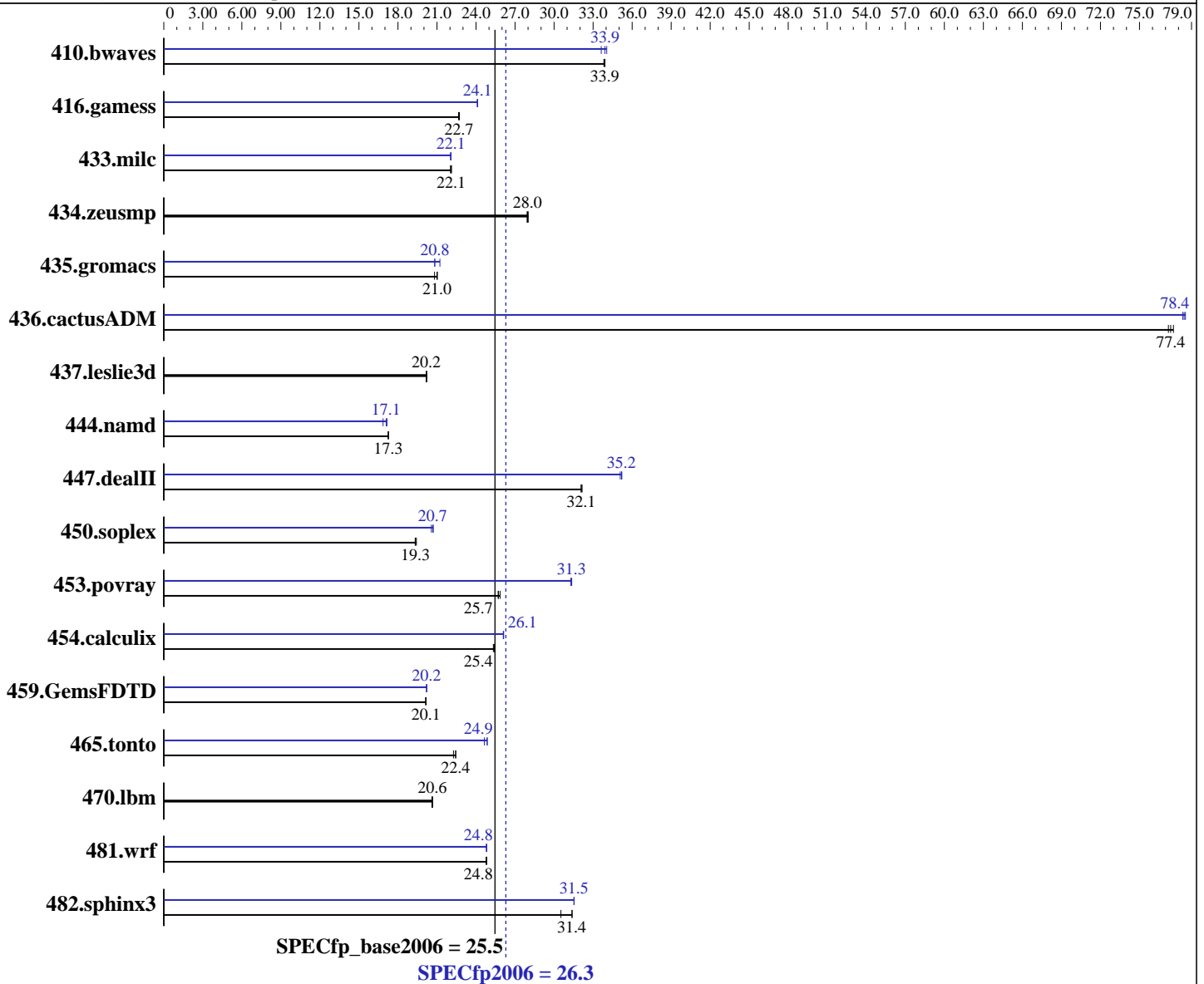
Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Dec-2009

Tested by: Acer Incorporated

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X3380  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3167  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux  
 Build 20080930 Package ID: I\_cprof\_p\_11.0.066,  
 I\_cprof\_p\_11.0.066  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 64-bit

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)  
Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V8.1  
Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	401	33.9	<b>401</b>	<b>33.9</b>	402	33.8	404	33.6	<b>401</b>	<b>33.9</b>	399	34.0
416.gamess	862	22.7	<b>863</b>	<b>22.7</b>	864	22.7	812	24.1	<b>812</b>	<b>24.1</b>	813	24.1
433.milc	417	22.0	415	22.1	<b>415</b>	<b>22.1</b>	416	22.1	417	22.0	<b>416</b>	<b>22.1</b>
434.zeusmp	326	27.9	<b>325</b>	<b>28.0</b>	325	28.0	326	27.9	<b>325</b>	<b>28.0</b>	325	28.0
435.gromacs	<b>340</b>	<b>21.0</b>	343	20.8	340	21.0	336	21.2	<b>343</b>	<b>20.8</b>	343	20.8
436.cactusADM	<b>154</b>	<b>77.4</b>	155	77.2	154	77.6	152	78.5	153	78.3	<b>152</b>	<b>78.4</b>
437.leslie3d	465	20.2	466	20.2	<b>466</b>	<b>20.2</b>	465	20.2	466	20.2	<b>466</b>	<b>20.2</b>
444.namd	465	17.3	465	17.3	<b>465</b>	<b>17.3</b>	476	16.9	467	17.2	<b>469</b>	<b>17.1</b>
447.dealII	357	32.1	356	32.2	<b>356</b>	<b>32.1</b>	326	35.1	325	35.2	<b>325</b>	<b>35.2</b>
450.soplex	429	19.4	432	19.3	<b>431</b>	<b>19.3</b>	<b>403</b>	<b>20.7</b>	406	20.6	403	20.7
453.povray	206	25.8	207	25.7	<b>207</b>	<b>25.7</b>	170	31.4	170	31.3	<b>170</b>	<b>31.3</b>
454.calculix	325	25.4	<b>325</b>	<b>25.4</b>	326	25.3	316	26.1	316	26.1	<b>316</b>	<b>26.1</b>
459.GemsFDTD	<b>527</b>	<b>20.1</b>	527	20.1	526	20.2	525	20.2	<b>525</b>	<b>20.2</b>	525	20.2
465.tonto	438	22.4	442	22.3	<b>439</b>	<b>22.4</b>	399	24.6	396	24.9	<b>396</b>	<b>24.9</b>
470.lbm	667	20.6	<b>666</b>	<b>20.6</b>	665	20.7	667	20.6	<b>666</b>	<b>20.6</b>	665	20.7
481.wrf	450	24.8	<b>450</b>	<b>24.8</b>	451	24.8	450	24.8	450	24.8	<b>450</b>	<b>24.8</b>
482.sphinx3	621	31.4	638	30.5	<b>621</b>	<b>31.4</b>	<b>618</b>	<b>31.5</b>	618	31.5	618	31.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 200M

This result was measured on the Gateway GT110 F1.  
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias -auto-ilp32

447.deallI: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-  
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -ansi-alias  
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch  
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -opt-prefetch -parallel  
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.xml>

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