



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 29.7**

## Gateway GT110 F1 (Intel Xeon X3380)

**SPECint\_base2006 = 26.5**

CPU2006 license: 97

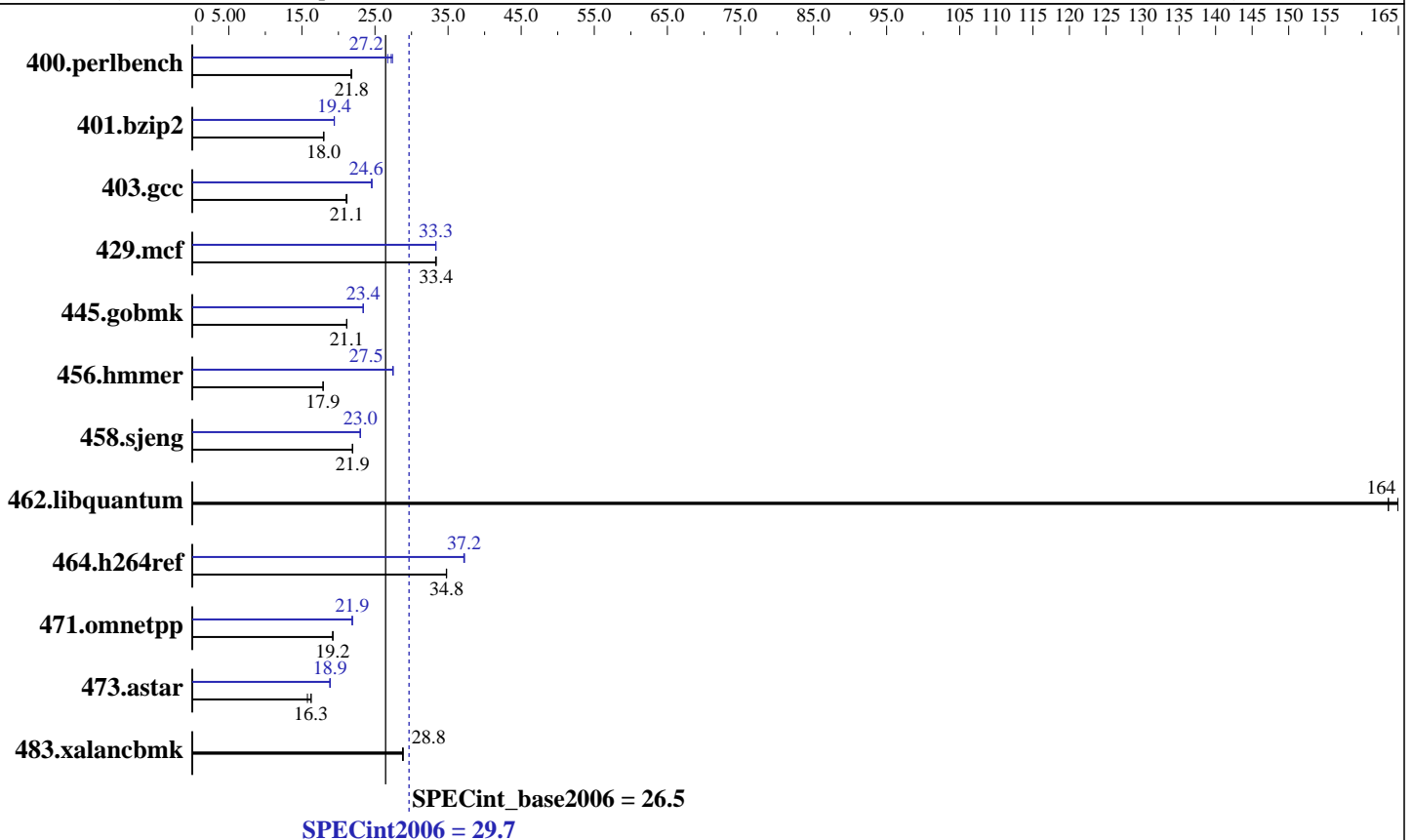
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Dec-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X3380  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3167  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)  
 Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>448</b>	<b>21.8</b>	448	21.8	450	21.7	<b>359</b>	<b>27.2</b>	365	26.7	357	27.4
401.bzip2	538	17.9	535	18.0	<b>537</b>	<b>18.0</b>	<b>497</b>	<b>19.4</b>	496	19.4	497	19.4
403.gcc	380	21.2	<b>381</b>	<b>21.1</b>	382	21.1	328	24.5	<b>328</b>	<b>24.6</b>	328	24.6
429.mcf	274	33.3	<b>273</b>	<b>33.4</b>	273	33.4	<b>274</b>	<b>33.3</b>	274	33.3	273	33.3
445.gobmk	<b>496</b>	<b>21.1</b>	496	21.1	496	21.1	448	23.4	449	23.4	<b>448</b>	<b>23.4</b>
456.hmmr	521	17.9	521	17.9	<b>521</b>	<b>17.9</b>	339	27.5	340	27.4	<b>340</b>	<b>27.5</b>
458.sjeng	<b>552</b>	<b>21.9</b>	552	21.9	551	22.0	<b>526</b>	<b>23.0</b>	527	23.0	526	23.0
462.libquantum	<b>127</b>	<b>164</b>	126	165	127	164	<b>127</b>	<b>164</b>	126	165	127	164
464.h264ref	636	34.8	637	34.8	<b>636</b>	<b>34.8</b>	<b>595</b>	<b>37.2</b>	594	37.2	596	37.2
471.omnetpp	325	19.2	<b>325</b>	<b>19.2</b>	325	19.2	285	21.9	286	21.9	<b>285</b>	<b>21.9</b>
473.astar	431	16.3	<b>432</b>	<b>16.3</b>	445	15.8	<b>372</b>	<b>18.9</b>	373	18.8	372	18.9
483.xalancbmk	239	28.9	<b>239</b>	<b>28.8</b>	240	28.8	239	28.9	<b>239</b>	<b>28.8</b>	240	28.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"

This result was measured on the Gateway GT110 F1.  
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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