SPEC® CINT2006 Result

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Fujitsu

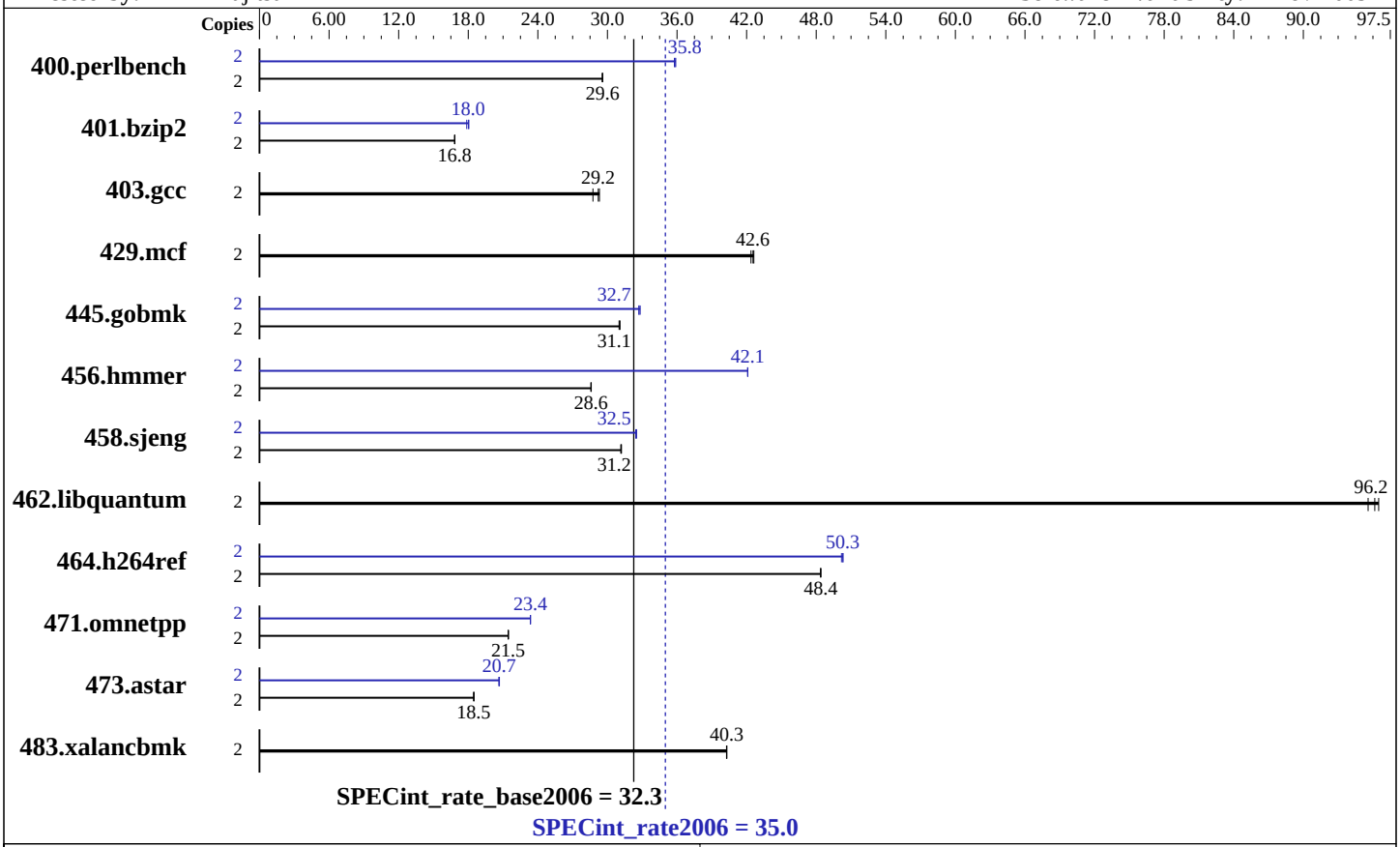
PRIMERGY RX100 S6, Intel Celeron G1101, 2.26 GHz

SPECint®_rate2006 = 35.0

SPECint_rate_base2006 = 32.3

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2010
Hardware Availability: Jan-2010
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**660**	**29.6**	661	29.5	660	29.6	2	**545**	**35.8**	546	35.8	544	35.9
401.bzip2	2	**1146**	**16.8**	1145	16.9	1148	16.8	2	1069	18.1	1080	17.9	**1072**	**18.0**
403.gcc	2	**551**	**29.2**	560	28.8	549	29.3	2	**551**	**29.2**	560	28.8	549	29.3
429.mcf	2	430	42.4	428	42.6	**429**	**42.6**	2	430	42.4	428	42.6	**429**	**42.6**
445.gobmk	2	**676**	**31.1**	675	31.1	677	31.0	2	641	32.7	**641**	**32.7**	639	32.8
456.hmmer	2	652	28.6	653	28.6	**653**	**28.6**	2	**443**	**42.1**	443	42.1	443	42.1
458.sjeng	2	**776**	**31.2**	775	31.2	777	31.1	2	746	32.4	744	32.5	**745**	**32.5**
462.libquantum	2	**431**	**96.2**	429	96.5	434	95.6	2	**431**	**96.2**	429	96.5	434	95.6
464.h264ref	2	**915**	**48.4**	915	48.4	915	48.4	2	880	50.3	882	50.2	**880**	**50.3**
471.omnetpp	2	582	21.5	583	21.4	**583**	**21.5**	2	**535**	**23.4**	535	23.4	535	23.4
473.astar	2	759	18.5	761	18.5	**760**	**18.5**	2	**679**	**20.7**	679	20.7	680	20.6
483.xalancbmk	2	343	40.3	342	40.3	**343**	**40.3**	2	343	40.3	342	40.3	**343**	**40.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
The system automatically configures the memory to run at 1066 MHz.
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml`

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For other inquiries, please contact webmaster@spec.org.

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