



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 19.3**

Acer AW2000h-AW170h F1 (Intel Xeon E5502)

**SPECint\_base2006 = 17.2**

CPU2006 license: 97

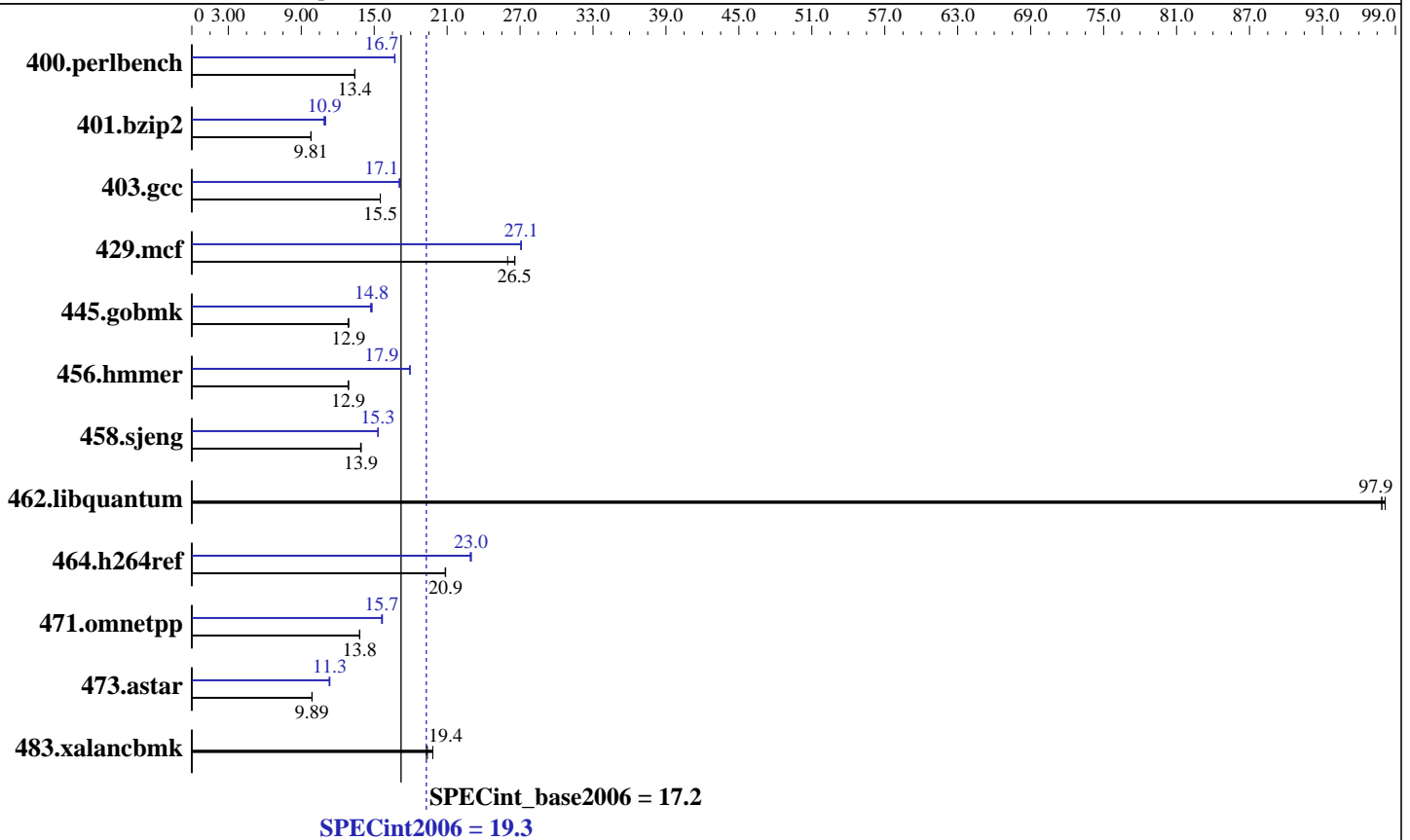
Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2010

Tested by: Acer Incorporated

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (12 x 2 GB DDR3-1066 RDIMM, running at 800 MHz)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20090131 Package ID: I\_cproc\_p\_11.0.080, I\_cprof\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	730	13.4	<b><u>728</u></b>	<b><u>13.4</u></b>	728	13.4	585	16.7	586	16.7	<b><u>585</u></b>	<b><u>16.7</u></b>
401.bzip2	983	9.81	986	9.79	<b><u>984</u></b>	<b><u>9.81</u></b>	877	11.0	888	10.9	<b><u>883</u></b>	<b><u>10.9</u></b>
403.gcc	519	15.5	<b><u>519</u></b>	<b><u>15.5</u></b>	519	15.5	471	17.1	472	17.1	<b><u>472</u></b>	<b><u>17.1</u></b>
429.mcf	351	26.0	<b><u>344</u></b>	<b><u>26.5</u></b>	343	26.6	337	27.1	<b><u>337</u></b>	<b><u>27.1</u></b>	336	27.1
445.gobmk	<b><u>814</u></b>	<b><u>12.9</u></b>	814	12.9	814	12.9	707	14.8	713	14.7	<b><u>710</u></b>	<b><u>14.8</u></b>
456.hammer	725	12.9	724	12.9	<b><u>724</u></b>	<b><u>12.9</u></b>	520	17.9	520	18.0	<b><u>520</u></b>	<b><u>17.9</u></b>
458.sjeng	868	13.9	872	13.9	<b><u>870</u></b>	<b><u>13.9</u></b>	789	15.3	790	15.3	<b><u>790</u></b>	<b><u>15.3</u></b>
462.libquantum	212	97.9	211	98.2	<b><u>212</u></b>	<b><u>97.9</u></b>	212	97.9	211	98.2	<b><u>212</u></b>	<b><u>97.9</u></b>
464.h264ref	<b><u>1061</u></b>	<b><u>20.9</u></b>	1060	20.9	1062	20.8	962	23.0	<b><u>964</u></b>	<b><u>23.0</u></b>	966	22.9
471.omnetpp	<b><u>453</u></b>	<b><u>13.8</u></b>	453	13.8	452	13.8	400	15.6	<b><u>399</u></b>	<b><u>15.7</u></b>	399	15.7
473.astar	709	9.90	<b><u>710</u></b>	<b><u>9.89</u></b>	711	9.88	619	11.3	622	11.3	<b><u>619</u></b>	<b><u>11.3</u></b>
483.xalancbmk	358	19.3	348	19.8	<b><u>356</u></b>	<b><u>19.4</u></b>	358	19.3	348	19.8	<b><u>356</u></b>	<b><u>19.4</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

This result was measured on the Gateway GW2000h-GW170h F1.  
The Acer AW2000h-AW170h F1 and Gateway GW2000h-GW170h F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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