SPEC® CFP2006 Result

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Fujitsu

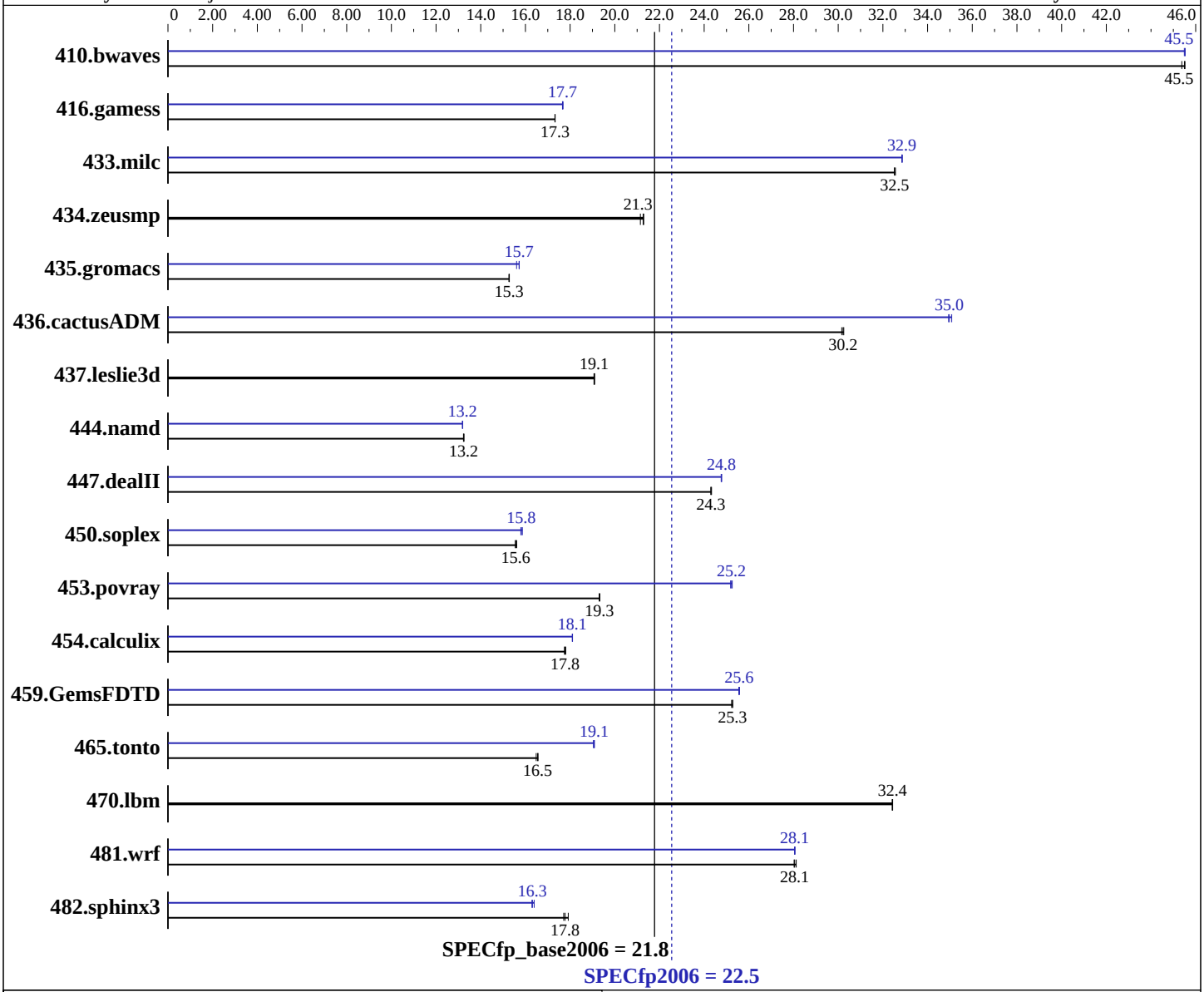
CELSIUS W380, Intel Celeron G1101

SPECfp®2006 = 22.5

SPECfp_base2006 = 21.8

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2x2 GB PC3 10600E, 2 rank, CL9, with ECC, running at 1066 MHz)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	299	45.4	299	45.5	**299**	**45.5**	299	45.5	299	45.5	**299**	**45.5**
416.gamess	**1130**	**17.3**	1131	17.3	1130	17.3	1107	17.7	1109	17.7	**1109**	**17.7**
433.milc	282	32.6	282	32.5	**282**	**32.5**	**279**	**32.9**	279	32.9	279	32.9
434.zeusmp	430	21.1	**428**	**21.3**	427	21.3	430	21.1	**428**	**21.3**	427	21.3
435.gromacs	467	15.3	468	15.3	**467**	**15.3**	458	15.6	**454**	**15.7**	454	15.7
436.cactusADM	396	30.2	395	30.2	**395**	**30.2**	**342**	**35.0**	341	35.1	342	34.9
437.leslie3d	492	19.1	493	19.1	**493**	**19.1**	492	19.1	493	19.1	**493**	**19.1**
444.namd	605	13.2	606	13.2	**606**	**13.2**	608	13.2	608	13.2	**608**	**13.2**
447.dealII	470	24.3	**471**	**24.3**	471	24.3	**462**	**24.8**	462	24.8	462	24.8
450.soplex	536	15.5	**536**	**15.6**	534	15.6	528	15.8	526	15.9	**527**	**15.8**
453.povray	275	19.3	276	19.3	**275**	**19.3**	211	25.2	211	25.3	**211**	**25.2**
454.calculix	463	17.8	465	17.7	**464**	**17.8**	**456**	**18.1**	456	18.1	456	18.1
459.GemsFDTD	421	25.2	420	25.3	**420**	**25.3**	**415**	**25.6**	415	25.6	415	25.6
465.tonto	597	16.5	594	16.6	**595**	**16.5**	515	19.1	**516**	**19.1**	517	19.0
470.lbm	424	32.4	**424**	**32.4**	424	32.4	424	32.4	**424**	**32.4**	424	32.4
481.wrf	399	28.0	**398**	**28.1**	397	28.1	**398**	**28.1**	398	28.1	398	28.1
482.sphinx3	1088	17.9	1100	17.7	**1096**	**17.8**	**1195**	**16.3**	1189	16.4	1196	16.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -auto-ilp32

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

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