SPEC® CINT2006 Result

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Dell Inc.

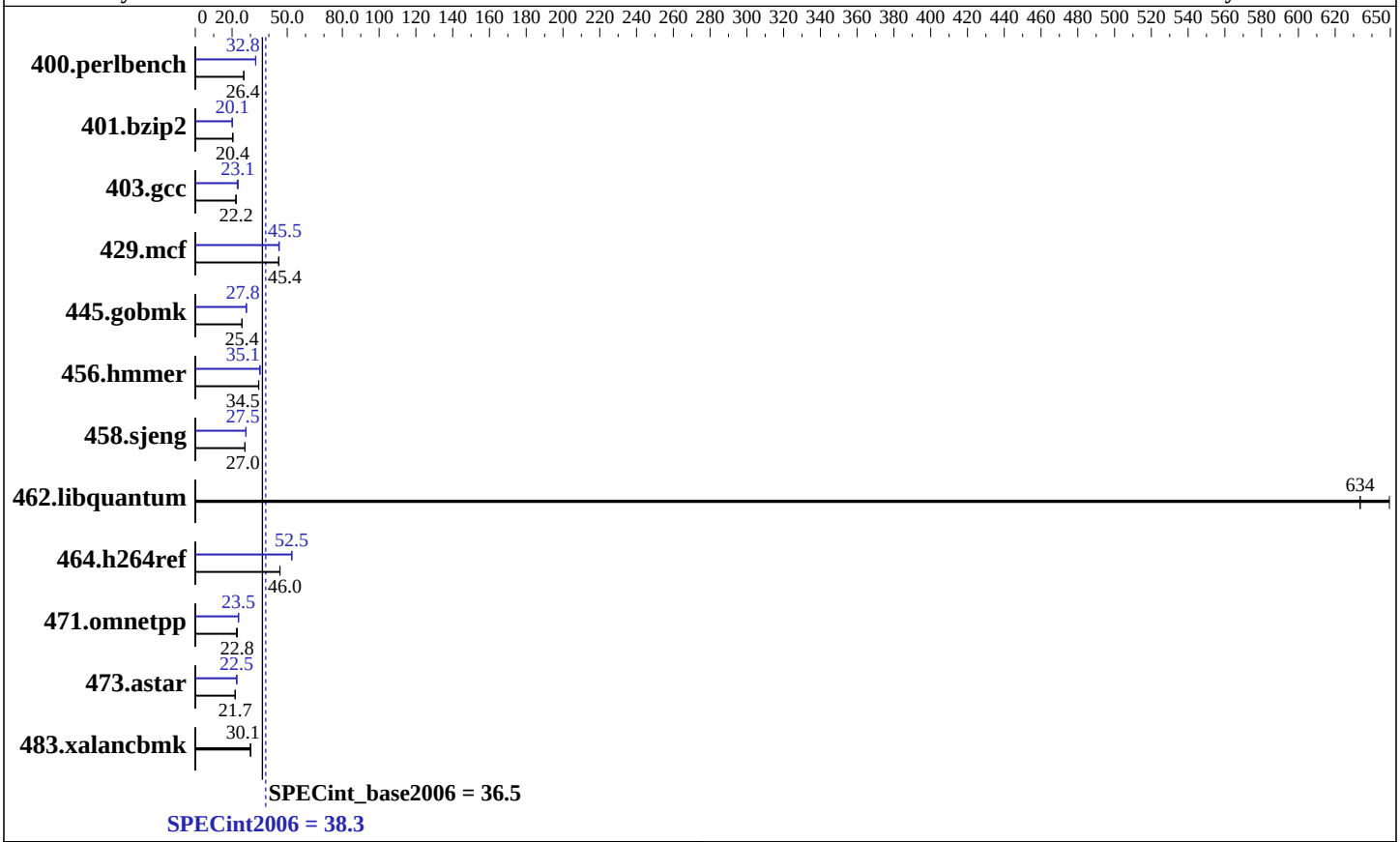
Dell Precision T7500 (Intel Xeon X5680, 3.33 GHz)

SPECint®2006 = 38.3

SPECint_base2006 = 36.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Xeon X5680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R)
Disk Subsystem:	1 x 300 GB SATA 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Professional (64-bit)
Compiler:	Intel C++ Compiler Professional for Intel 64, Version 11.1 Build 20091130 Package ID: w_cproc_p_11.1.054 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 for x64

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**370**	**26.4**	370	26.4	369	26.4	297	32.9	**298**	**32.8**	298	32.8
401.bzip2	474	20.4	**473**	**20.4**	472	20.4	483	20.0	477	20.2	**480**	**20.1**
403.gcc	361	22.3	**363**	**22.2**	365	22.0	345	23.3	**348**	**23.1**	353	22.8
429.mcf	201	45.5	**201**	**45.4**	201	45.3	**201**	**45.5**	200	45.7	201	45.4
445.gobmk	413	25.4	**413**	**25.4**	412	25.4	378	27.7	378	27.8	**378**	**27.8**
456.hmmer	270	34.5	**271**	**34.5**	271	34.4	265	35.2	**266**	**35.1**	266	35.1
458.sjeng	**449**	**27.0**	448	27.0	449	27.0	439	27.6	**440**	**27.5**	441	27.5
462.libquantum	**32.7**	**634**	31.9	650	32.7	634	**32.7**	**634**	31.9	650	32.7	634
464.h264ref	481	46.0	**481**	**46.0**	480	46.1	**422**	**52.5**	422	52.4	422	52.5
471.omnetpp	**274**	**22.8**	274	22.8	279	22.4	**266**	**23.5**	266	23.5	266	23.5
473.astar	323	21.7	324	21.7	**323**	**21.7**	312	22.5	**312**	**22.5**	311	22.6
483.xalancbmk	229	30.1	230	30.0	**229**	**30.1**	229	30.1	230	30.0	**229**	**30.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
OMP_NUM_THREADS=12 (number of cores)
KMP_AFFINITY=granularity=fine,scatter
```

Platform Notes

```
BIOS Settings
     Memory Node Interleaving: NUMA
              Hyper-Threading: ENABLE
```

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY -DSPEC_CPU_WIN64_X64
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel /F512000000

C++ benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features /F512000000 shlw64m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -QxSSE4.2 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-prefetch /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-prefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -QxSSE4.2 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000

429.mcf: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div-
-Qopt-prefetch /F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias
/F512000000

456.hmmer: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2
-Qansi-alias /F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4
/F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2
-Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 05:05:21 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 30 March 2010.