SPEC® CINT2006 Result

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Dell Inc.

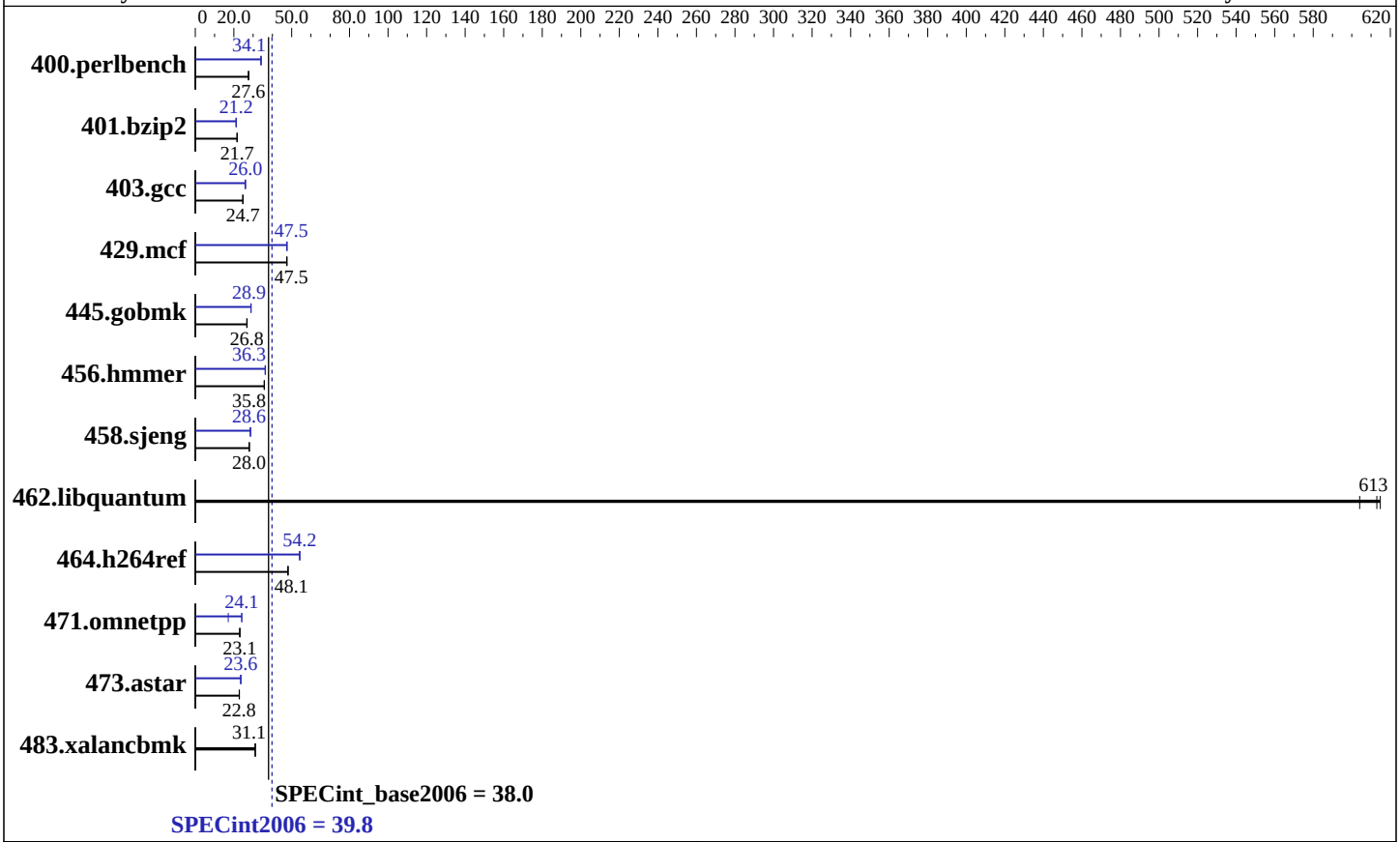
Dell Precision T7500 (Intel Xeon X5677, 3.46 GHz)

SPECint®2006 = 39.8

SPECint_base2006 = 38.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Xeon X5677
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3466
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R)
Disk Subsystem:	1 x 300 GB SATA 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Professional (64-bit)
Compiler:	Intel C++ Compiler Professional for Intel 64, Version 11.1 Build 20091130 Package ID: w_cproc_p_11.1.054 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 for x64

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**354**	**27.6**	353	27.7	354	27.6	286	34.1	**286**	**34.1**	286	34.1
401.bzip2	445	21.7	**445**	**21.7**	446	21.7	455	21.2	**455**	**21.2**	456	21.2
403.gcc	326	24.7	326	24.7	**326**	**24.7**	313	25.8	309	26.1	**309**	**26.0**
429.mcf	192	47.5	192	47.5	**192**	**47.5**	192	47.5	**192**	**47.5**	192	47.6
445.gobmk	390	26.9	**391**	**26.8**	393	26.7	363	28.9	**363**	**28.9**	363	28.9
456.hmmer	261	35.8	261	35.8	**261**	**35.8**	**257**	**36.3**	257	36.3	257	36.3
458.sjeng	433	28.0	433	28.0	**433**	**28.0**	423	28.6	**423**	**28.6**	423	28.6
462.libquantum	33.7	615	34.3	604	**33.8**	**613**	33.7	615	34.3	604	**33.8**	**613**
464.h264ref	**460**	**48.1**	460	48.1	460	48.1	409	54.1	408	54.2	**408**	**54.2**
471.omnetpp	271	23.1	271	23.1	**271**	**23.1**	365	17.1	**260**	**24.1**	259	24.1
473.astar	**307**	**22.8**	307	22.9	308	22.8	**298**	**23.6**	298	23.6	297	23.6
483.xalancbmk	222	31.1	222	31.1	**222**	**31.1**	222	31.1	222	31.1	**222**	**31.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
OMP_NUM_THREADS=8 (number of cores)
KMP_AFFINITY=granularity=fine,scatter
```

Platform Notes

```
BIOS Settings
     Memory Node Interleaving: NUMA
              Hyper-Threading: ENABLE
```

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY -DSPEC_CPU_WIN64_X64
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel /F512000000

C++ benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features /F512000000 shlw64m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -QxSSE4.2 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-prefetch /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-prefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -QxSSE4.2 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000

429.mcf: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div-
-Qopt-prefetch /F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias
/F512000000

456.hmmer: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2
-Qansi-alias /F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4
/F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2
-Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.1.win.xml`

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