**Acer Incorporated**

**Gateway GT350 F1 (Intel Xeon E5502)**

### SPECint®2006 = 21.0

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>16.3</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>14.0</td>
</tr>
<tr>
<td>403.gcc</td>
<td>10.9</td>
</tr>
<tr>
<td>429.mcf</td>
<td>13.5</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>14.6</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>13.8</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>15.2</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>14.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>22.6</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>15.4</td>
</tr>
<tr>
<td>473.astar</td>
<td>12.7</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>19.8</td>
</tr>
</tbody>
</table>

### SPECint_base2006 = 19.4

**CPU2006 license:** 97  
**Test sponsor:** Acer Incorporated  
**Tested by:** Acer Incorporated

- **Test date:** Mar-2010  
- **Hardware Availability:** Jan-2010  
- **Software Availability:** Jan-2010

#### Hardware

- **CPU Name:** Intel Xeon E5502  
- **CPU Characteristics:** Intel Integrated  
- **CPU MHz:** 1867  
- **FPU:** Integrated  
- **CPU(s) enabled:** 4 cores, 2 chips, 2 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 4 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)  
- **Disk Subsystem:** 1000 GB SATAII, 7200 RPM  
- **Other Hardware:** None

#### Software

- **Operating System:** SUSE Linux Enterprise Server 11 (x86_64)  
  Kernel 2.6.27.19-5  
  Build 20091130 Package ID: 1_cproc_p_11.1.064
- **Compiler:** Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
  Build 20091130 Package ID: 1_cproc_p_11.1.064
- **Auto Parallel:** Yes
- **File System:** ReiserFS
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V8.1  
  Binutils 2.18.50.0.7.20080502
### SPEC CINT2006 Result

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#### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Peak</th>
<th>Ratio Peak</th>
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</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>696</td>
<td>14.0</td>
<td>700</td>
<td>14.0</td>
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<td>401.bzip2</td>
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<td><strong>23.6</strong></td>
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<td>456.hmmer</td>
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<td>355</td>
<td><strong>26.3</strong></td>
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<tr>
<td>458.sjeng</td>
<td>834</td>
<td>14.5</td>
<td>833</td>
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<tr>
<td>462.libquantum</td>
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<td><strong>190</strong></td>
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<td>464.h264ref</td>
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<td>12.7</td>
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<tr>
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<td>644</td>
<td>10.9</td>
<td><strong>642</strong></td>
<td><strong>10.9</strong></td>
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<tr>
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<td>361</td>
<td>19.1</td>
<td><strong>357</strong></td>
<td><strong>19.3</strong></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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### Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

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### General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
This result was measured on the Gateway GT350 F1.
The Acer AT350 F1 and Gateway GT350 F1 are electronically equivalent.

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### Base Compiler Invocation

C benchmarks:
- icc  -m64

C++ benchmarks:
- icpc  -m64

---

### Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64  -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64

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# SPEC CINT2006 Result

## Acer Incorporated

**Gateway GT350 F1 (Intel Xeon E5502)**

| SPECint2006 = | 21.0 |
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## Base Portability Flags (Continued)

- 429.mcf: -DSPEC_CPU_LP64
- 445.gobmk: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 464.h264ref: -DSPEC_CPU_LP64
- 471.omnetpp: -DSPEC_CPU_LP64
- 473.astar: -DSPEC_CPU_LP64
- 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

*C benchmarks:*

- -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

*C++ benchmarks:*

- -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs

## Base Other Flags

*C benchmarks:*

- 403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

*C benchmarks (except as noted below):*

- icc -m64
- 400.perlbench: icc -m32
- 429.mcf: icc -m32
- 445.gobmk: icc -m32
- 464.h264ref: icc -m32

*C++ benchmarks (except as noted below):*

- icpc -m32
- 473.astar: icpc -m64
Acer Incorporated
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### Peak Portability Flags

- 400.perlbuch: -DSPEC_CPU_LINUX_IA32
- 401.bzip2: -DSPEC_CPU_LP64
- 403.gcc: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 473.astar: -DSPEC_CPU_LP64
- 483.xalancbmk: -DSPEC_CPU_LINUX

### Peak Optimization Flags

**C benchmarks:**

- 400.perlbuch: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -03(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -ansi-alias -opt-prefetch
- 401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -03(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
  -auto-ilp32 -opt-prefetch -ansi-alias
- 403.gcc: -xSSE4.2 -ipo -03 -no-prec-div -static -inline-calloc
  -opt-malloc-options=3 -auto-ilp32
- 429.mcf: -xSSE4.2 -ipo -03 -no-prec-div -static -opt-prefetch
- 445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
  -ipo -no-prec-div -ansi-alias
- 456.hmmer: -xSSE4.2 -ipo -03 -no-prec-div -static -unroll2
  -ansi-alias -auto-ilp32
- 458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -03(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -unroll1
- 462.libquantum: -xSSE4.2 -ipo -03 -no-prec-div -static -parallel
  -opt-prefetch -par-schedule-static=32768 -ansi-alias
- 464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -03(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -unroll2 -ansi-alias

**C++ benchmarks:**

- 471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

```bash  
473.astar: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)  
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl, -z, muldefs  

483.xalancbmk: -xSSE4.2 -ipo -03 -no-prec-div -opt-prefetch  
-Wl, -z, muldefs  
```

## Peak Other Flags

**C benchmarks:**

- 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at [http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html](http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html)

You can also download the XML flags source by saving the following link:  

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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