# SPEC® CINT2006 Result

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## Acer Incorporated

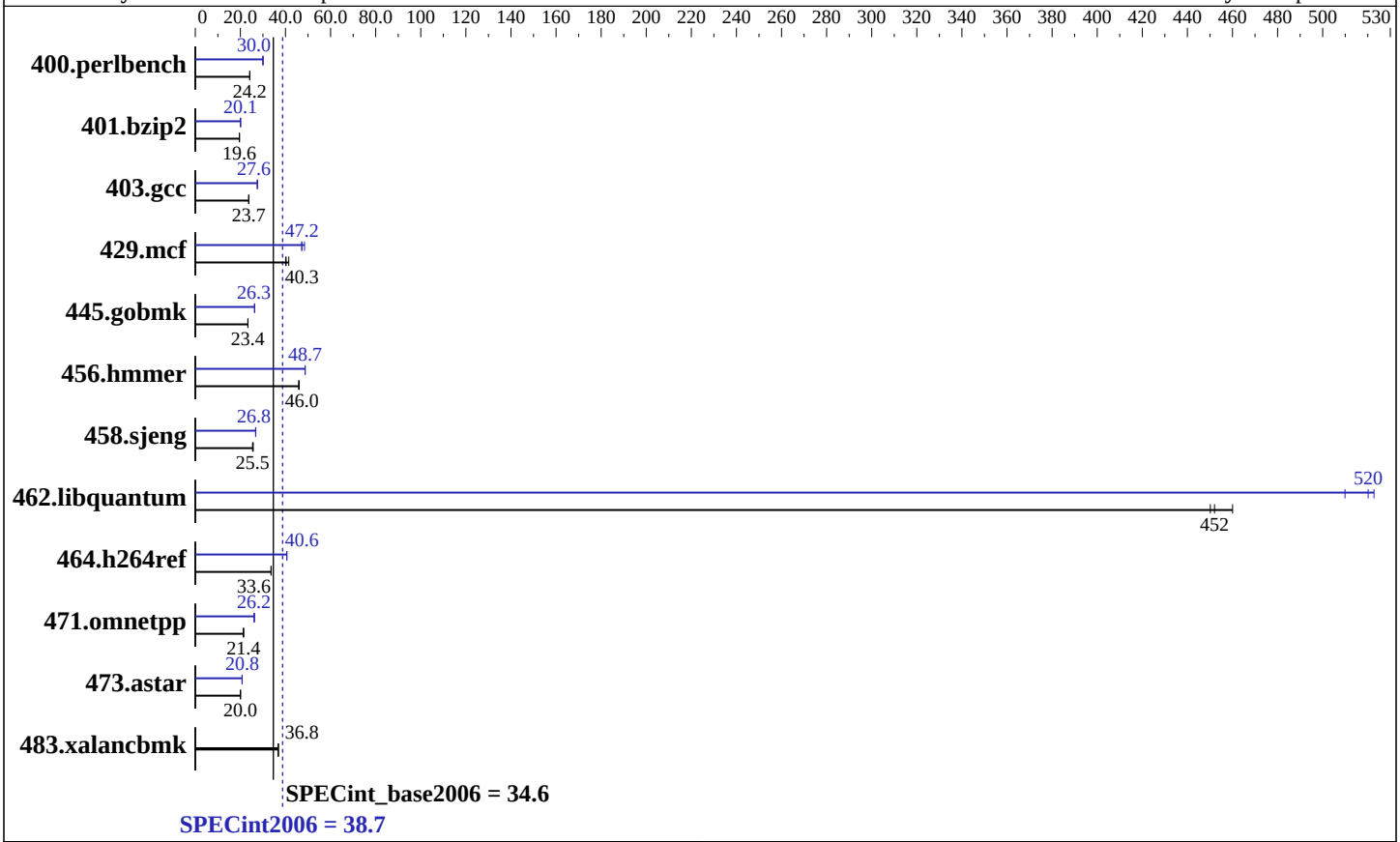
## Gateway GR360 F1 (Intel Xeon X5570)

SPECint®2006 = 38.7

SPECint_base2006 = 34.6

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Mar-2010
**Hardware Availability:** Jan-2010
**Software Availability:** Sep-2009




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5570 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.33 GHz |
| CPU MHz: | 2933 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4GB DDR3-1333 RDIMM) |
| Disk Subsystem: | 1000 GB SATA II, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5 |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **404** | **24.2** | 404 | 24.2 | 405 | 24.1 | 325 | 30.0 | **325** | **30.0** | 326 | 30.0 |
| 401.bzip2 | **493** | **19.6** | 494 | 19.5 | 493 | 19.6 | 481 | 20.1 | **481** | **20.1** | 482 | 20.0 |
| 403.gcc | **340** | **23.7** | 338 | 23.8 | 340 | 23.7 | 292 | 27.6 | **292** | **27.6** | 293 | 27.5 |
| 429.mcf | 220 | 41.5 | **226** | **40.3** | 228 | 40.1 | 193 | 47.2 | **193** | **47.2** | 188 | 48.4 |
| 445.gobmk | **449** | **23.4** | 450 | 23.3 | 449 | 23.4 | **398** | **26.3** | 400 | 26.2 | 398 | 26.3 |
| 456.hmmer | 203 | 45.9 | **203** | **46.0** | 203 | 46.0 | **192** | **48.7** | 191 | 48.7 | 192 | 48.7 |
| 458.sjeng | 476 | 25.4 | **475** | **25.5** | 471 | 25.7 | **452** | **26.8** | 451 | 26.8 | 453 | 26.7 |
| 462.libquantum | 45.0 | 460 | **45.8** | **452** | 46.0 | 450 | 39.6 | 523 | **39.8** | **520** | 40.6 | 510 |
| 464.h264ref | 659 | 33.6 | **658** | **33.6** | 657 | 33.7 | 545 | 40.6 | **545** | **40.6** | 545 | 40.6 |
| 471.omnetpp | 293 | 21.3 | 292 | 21.4 | **292** | **21.4** | 238 | 26.2 | **239** | **26.2** | 240 | 26.1 |
| 473.astar | 350 | 20.1 | 351 | 20.0 | **351** | **20.0** | 337 | 20.8 | **337** | **20.8** | 339 | 20.7 |
| 483.xalancbmk | **188** | **36.8** | 188 | 36.7 | 187 | 36.9 | **188** | **36.8** | 188 | 36.7 | 187 | 36.9 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
This result was measured on the Gateway GR380 F1.
The Gateway GR360 F1 and Gateway GR380 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64

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## Base Portability Flags (Continued)

429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

471.omnetpp: icpc -m32

# Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

# Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
-auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
-par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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# Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml


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For other inquiries, please contact webmaster@spec.org.

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