



SPEC[®] CFP2006 Result

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Supermicro

SPECfp[®]2006 = 42.5

Motherboard X8DAH+-F (Intel Xeon X5650, 2.66 GHz)

SPECfp_base2006 = 40.0

CPU2006 license: 001176

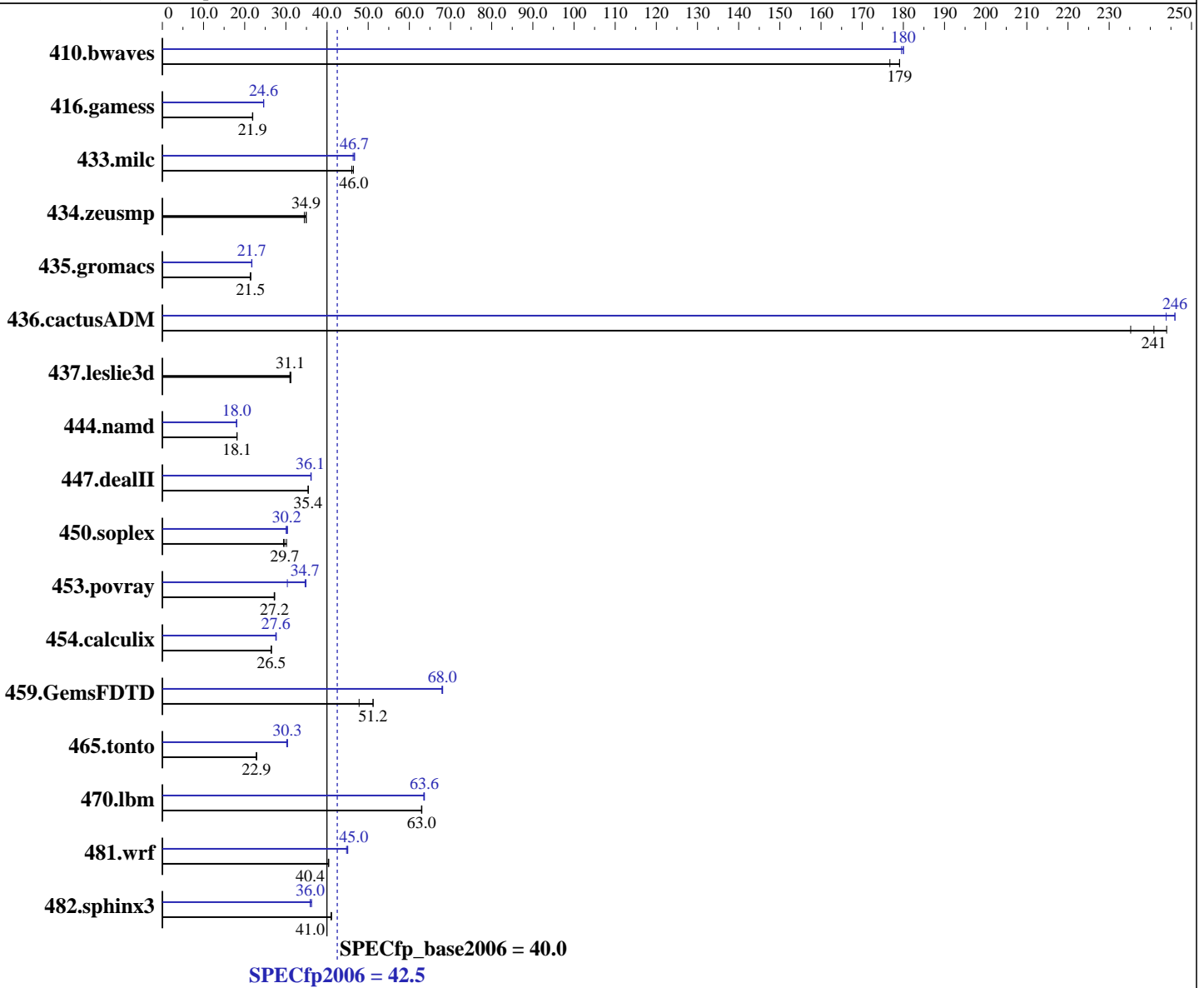
Test date: Apr-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5650
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
 Disk Subsystem: 1 x 320 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	76.9	177	<u>75.9</u>	<u>179</u>	75.9	179	75.7	180	75.5	180	<u>75.5</u>	<u>180</u>
416.gamess	895	21.9	<u>892</u>	<u>21.9</u>	891	22.0	<u>796</u>	<u>24.6</u>	795	24.6	796	24.6
433.milc	199	46.0	198	46.4	<u>199</u>	<u>46.0</u>	<u>197</u>	<u>46.7</u>	198	46.3	197	46.7
434.zeusmp	260	35.0	264	34.5	<u>261</u>	<u>34.9</u>	260	35.0	264	34.5	<u>261</u>	<u>34.9</u>
435.gromacs	335	21.3	<u>333</u>	<u>21.5</u>	333	21.5	328	21.8	<u>329</u>	<u>21.7</u>	329	21.7
436.cactusADM	<u>49.6</u>	<u>241</u>	50.8	235	49.0	244	48.6	246	49.0	244	<u>48.6</u>	<u>246</u>
437.leslie3d	301	31.2	303	31.0	<u>302</u>	<u>31.1</u>	301	31.2	303	31.0	<u>302</u>	<u>31.1</u>
444.namd	<u>442</u>	<u>18.1</u>	442	18.1	442	18.1	444	18.0	<u>444</u>	<u>18.0</u>	445	18.0
447.dealII	323	35.4	<u>323</u>	<u>35.4</u>	323	35.4	317	36.1	317	36.1	<u>317</u>	<u>36.1</u>
450.soplex	276	30.2	283	29.4	<u>281</u>	<u>29.7</u>	277	30.1	274	30.4	<u>276</u>	<u>30.2</u>
453.povray	<u>195</u>	<u>27.2</u>	195	27.3	196	27.2	<u>153</u>	<u>34.7</u>	175	30.4	152	34.9
454.calculix	<u>312</u>	<u>26.5</u>	312	26.4	311	26.5	<u>299</u>	<u>27.6</u>	298	27.7	299	27.6
459.GemsFDTD	222	47.8	<u>207</u>	<u>51.2</u>	207	51.2	156	67.9	156	68.1	<u>156</u>	<u>68.0</u>
465.tonto	<u>431</u>	<u>22.9</u>	430	22.9	431	22.8	<u>325</u>	<u>30.3</u>	324	30.4	326	30.2
470.lbm	218	62.9	<u>218</u>	<u>63.0</u>	218	63.0	216	63.5	216	63.7	<u>216</u>	<u>63.6</u>
481.wrf	<u>276</u>	<u>40.4</u>	277	40.3	276	40.4	<u>248</u>	<u>45.0</u>	250	44.7	248	45.0
482.sphinx3	<u>475</u>	<u>41.0</u>	474	41.1	476	41.0	543	35.9	<u>542</u>	<u>36.0</u>	538	36.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro
 PWS-865-PQ power supply, 2 SNK-P0035AP4 heatsinks,
 and 2 Nidec UltraFlo T92T12MMA7-57 T072,
 1 SANYO DENKI San Ace 80 9G0812G103 cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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