SPEC® CFP2006 Result

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Dell Inc.

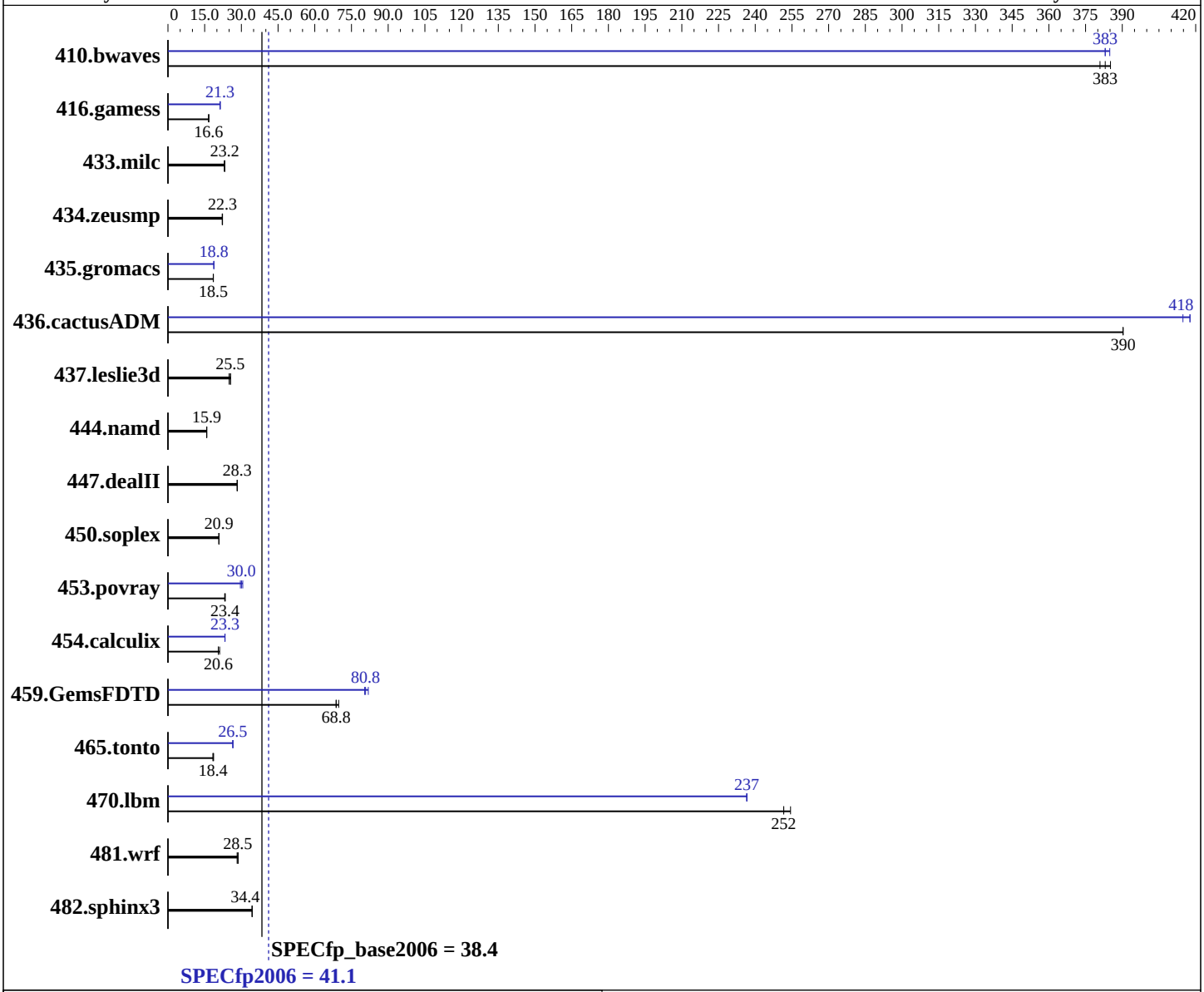
PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp®2006 = 41.1

SPECfp_base2006 = 38.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), kernel-2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (64 x 4 GB DDR3-1066 QR RDIMM, CL7, ECC)
Disk Subsystem: 1 x 300 GB 10000 RPM SAS 6Gb
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	35.7	381	**35.5**	**383**	35.3	385	35.5	383	**35.5**	**383**	35.3	385
416.gamess	1173	16.7	1177	16.6	**1177**	**16.6**	917	21.4	**917**	**21.3**	920	21.3
433.milc	**396**	**23.2**	399	23.0	395	23.3	**396**	**23.2**	399	23.0	395	23.3
434.zeusmp	408	22.3	411	22.2	**408**	**22.3**	408	22.3	411	22.2	**408**	**22.3**
435.gromacs	385	18.6	386	18.5	**385**	**18.5**	381	18.7	380	18.8	**380**	**18.8**
436.cactusADM	30.6	390	30.6	390	**30.6**	**390**	28.6	418	**28.6**	**418**	28.8	415
437.leslie3d	376	25.0	367	25.6	**368**	**25.5**	376	25.0	367	25.6	**368**	**25.5**
444.namd	**506**	**15.9**	507	15.8	506	15.9	**506**	**15.9**	507	15.8	506	15.9
447.dealII	**404**	**28.3**	404	28.3	405	28.3	**404**	**28.3**	404	28.3	405	28.3
450.soplex	**400**	**20.9**	401	20.8	400	20.9	**400**	**20.9**	401	20.8	400	20.9
453.povray	227	23.4	**228**	**23.4**	229	23.2	**177**	**30.0**	174	30.6	180	29.6
454.calculix	**401**	**20.6**	391	21.1	401	20.6	353	23.3	355	23.3	**354**	**23.3**
459.GemsFDTD	152	69.8	**154**	**68.8**	154	68.7	**131**	**80.8**	132	80.4	130	81.9
465.tonto	527	18.7	**534**	**18.4**	534	18.4	371	26.6	**371**	**26.5**	371	26.5
470.lbm	54.0	254	**54.6**	**252**	54.6	252	58.1	237	**58.1**	**237**	58.1	237
481.wrf	**391**	**28.5**	389	28.7	395	28.3	**391**	**28.5**	389	28.7	395	28.3
482.sphinx3	**567**	**34.4**	566	34.5	567	34.4	**567**	**34.4**	566	34.5	567	34.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Memory Settings: Node Interleaving = Enabled (Default = Disabled)

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
The Dell PowerEdge R910 and
the Bull NovaScale R480 F2 models are electronically equivalent.

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General Notes (Continued)

The results have been measured on a Dell PowerEdge R910 model.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias`

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

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