# SPEC® CINT2006 Result

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## Acer Incorporated

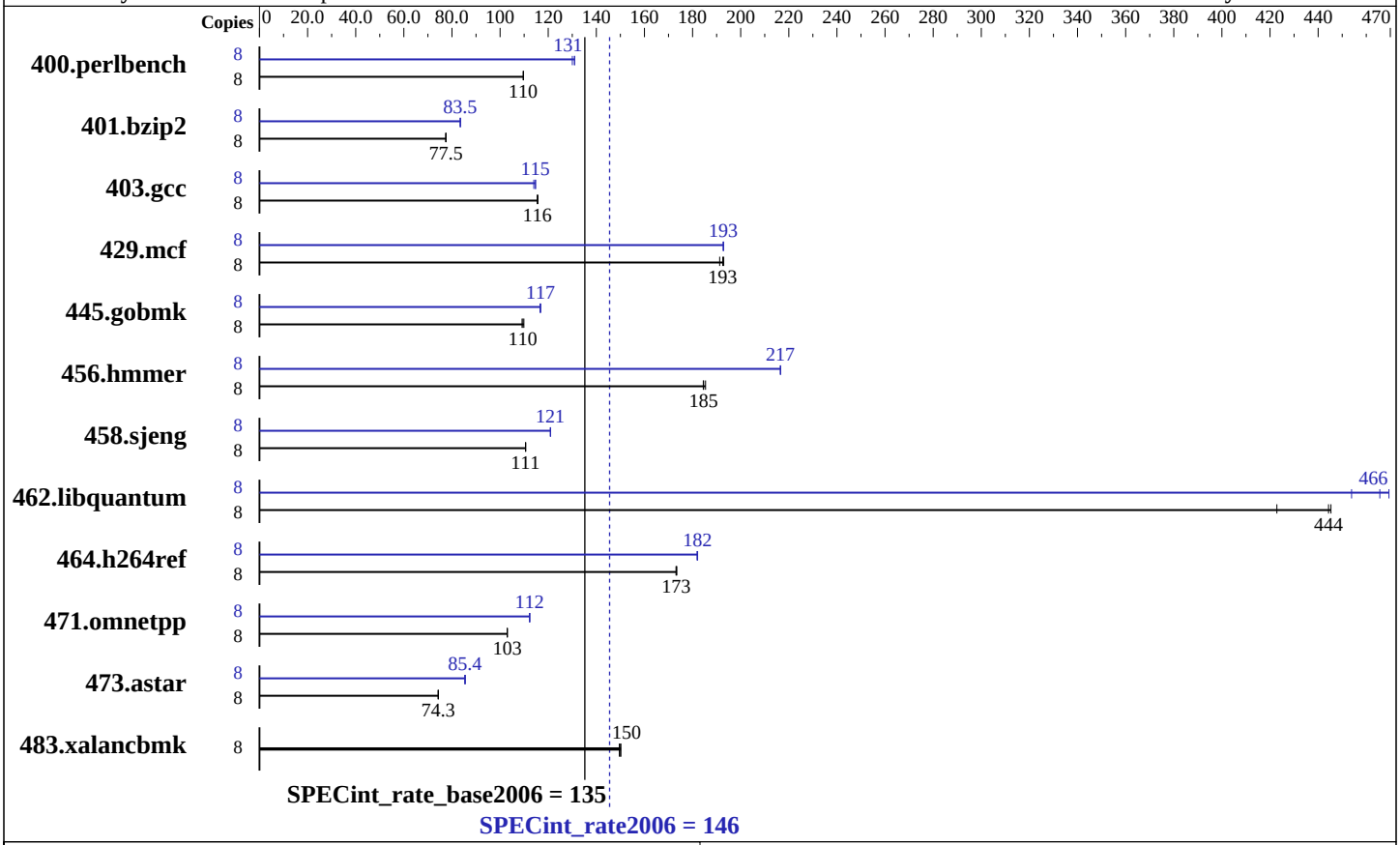
Gateway GB2x280 F1 (Intel Xeon L5609, 1.86 GHz)

SPECint®_rate2006 = 146

SPECint_rate_base2006 = 135

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** May-2010
**Hardware Availability:** Sep-2010
**Software Availability:** Jan-2010




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon L5609 |
| CPU Characteristics: | |
| CPU MHz: | 1867 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9, memory runs at 1066 MHz) |
| Disk Subsystem: | 1 x 150 GB SATA II, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | **713** | **110** | 713 | 110 | 712 | 110 | 8 | 597 | 131 | 601 | 130 | **597** | **131** |
| 401.bzip2 | 8 | **996** | **77.5** | 996 | 77.5 | 997 | 77.4 | 8 | 924 | 83.5 | **925** | **83.5** | 925 | 83.4 |
| 403.gcc | 8 | **558** | **116** | 558 | 115 | 557 | 116 | 8 | **561** | **115** | 561 | 115 | 565 | 114 |
| 429.mcf | 8 | 381 | 191 | 378 | 193 | **379** | **193** | 8 | 378 | 193 | **378** | **193** | 379 | 193 |
| 445.gobmk | 8 | 769 | 109 | 764 | 110 | **766** | **110** | 8 | 720 | 117 | 718 | 117 | **718** | **117** |
| 456.hmmer | 8 | 405 | 184 | **404** | **185** | 403 | 185 | 8 | **345** | **217** | 345 | 217 | 345 | 217 |
| 458.sjeng | 8 | 876 | 111 | 875 | 111 | **875** | **111** | 8 | 800 | 121 | **801** | **121** | 801 | 121 |
| 462.libquantum | 8 | 392 | 423 | **373** | **444** | 372 | 445 | 8 | **356** | **466** | 353 | 469 | 365 | 454 |
| 464.h264ref | 8 | 1023 | 173 | **1022** | **173** | 1021 | 173 | 8 | 972 | 182 | 974 | 182 | **972** | **182** |
| 471.omnetpp | 8 | 486 | 103 | **485** | **103** | 485 | 103 | 8 | 445 | 112 | **445** | **112** | 445 | 112 |
| 473.astar | 8 | 756 | 74.3 | **756** | **74.3** | 756 | 74.3 | 8 | 658 | 85.3 | **658** | **85.4** | 656 | 85.6 |
| 483.xalancbmk | 8 | 369 | 150 | 368 | 150 | **368** | **150** | 8 | 369 | 150 | 368 | 150 | **368** | **150** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

## Platform Notes

```
Fan speed set to full Speed (ie. Enterprise Blade mode) with
Smart Blade Console through CMM (Chassis Management Module)
```

## General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AB2x280 F1, and Gateway GB2x280 F1 are electronically equivalent.
This result was measured on Gateway GB2x280 F1.
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Continued on next page

## Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml`


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For other inquiries, please contact webmaster@spec.org.

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