# SPEC® CINT2006 Result

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## Fujitsu

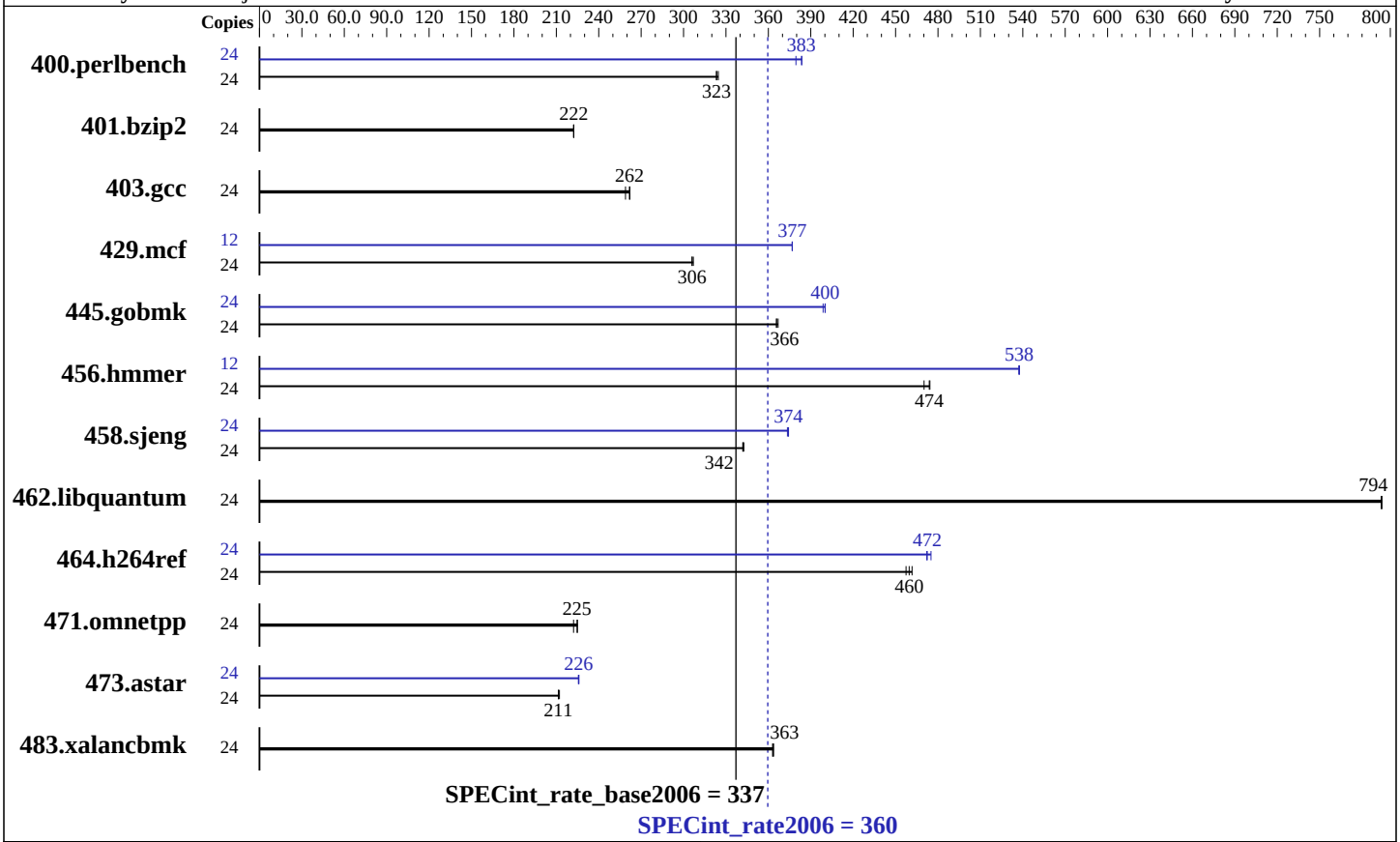
PRIMERGY TX200 S6, Intel Xeon X5670, 2.93 GHz

SPECint®_rate2006 = 360

SPECint_rate_base2006 = 337

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jul-2010
**Hardware Availability:** Jul-2010
**Software Availability:** Jan-2010




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5670 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.33 GHz |
| CPU MHz: | 2933 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SATA, 160 GB, 5400 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 722 | 325 | **725** | **323** | 725 | 323 | 24 | 611 | 384 | 618 | 380 | **612** | **383** |
| 401.bzip2 | 24 | **1042** | **222** | 1041 | 222 | 1042 | 222 | 24 | **1042** | **222** | 1041 | 222 | 1042 | 222 |
| 403.gcc | 24 | 737 | 262 | **738** | **262** | 746 | 259 | 24 | 737 | 262 | **738** | **262** | 746 | 259 |
| 429.mcf | 24 | 715 | 306 | 713 | 307 | **715** | **306** | 12 | **290** | **377** | 290 | 377 | 290 | 377 |
| 445.gobmk | 24 | **688** | **366** | 686 | 367 | 688 | 366 | 24 | **629** | **400** | 629 | 400 | 631 | 399 |
| 456.hmmer | 24 | 476 | 470 | 472 | 474 | **473** | **474** | 12 | 208 | 538 | 208 | 537 | **208** | **538** |
| 458.sjeng | 24 | **849** | **342** | 848 | 343 | 849 | 342 | 24 | 776 | 374 | 778 | 373 | **776** | **374** |
| 462.libquantum | 24 | **626** | **794** | 626 | 794 | 627 | 794 | 24 | **626** | **794** | 626 | 794 | 627 | 794 |
| 464.h264ref | 24 | 1150 | 462 | **1156** | **460** | 1161 | 457 | 24 | **1124** | **472** | 1118 | 475 | 1125 | 472 |
| 471.omnetpp | 24 | 675 | 222 | 667 | 225 | **668** | **225** | 24 | 675 | 222 | 667 | 225 | **668** | **225** |
| 473.astar | 24 | 797 | 211 | **797** | **211** | 794 | 212 | 24 | **746** | **226** | 747 | 226 | 745 | 226 |
| 483.xalancbmk | 24 | 456 | 363 | 455 | 364 | **456** | **363** | 24 | 456 | 363 | 455 | 364 | **456** | **363** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
```

## General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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