SPEC® CINT2006 Result

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Fujitsu

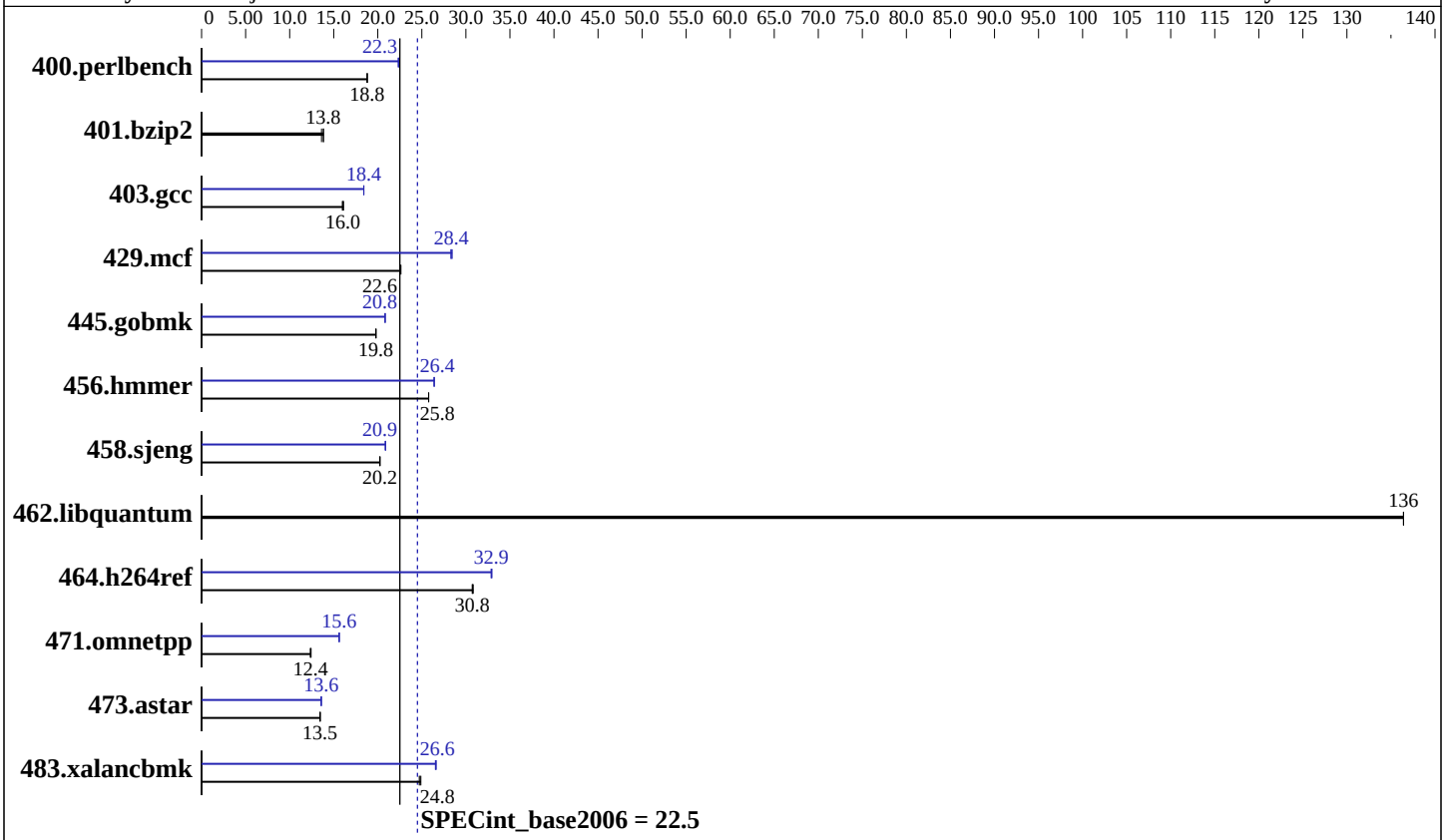
PRIMERGY TX100 S2, Intel Pentium G6950, 2.80 GHz

SPECint®2006 = 24.5

SPECint_base2006 = 22.5

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Pentium G6950
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SATA, 160 GB, 5400 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	519	18.8	**520**	**18.8**	520	18.8	**437**	**22.3**	437	22.4	438	22.3
401.bzip2	697	13.8	**698**	**13.8**	709	13.6	697	13.8	**698**	**13.8**	709	13.6
403.gcc	499	16.1	**502**	**16.0**	504	16.0	437	18.4	**437**	**18.4**	437	18.4
429.mcf	403	22.6	**403**	**22.6**	405	22.5	**322**	**28.4**	321	28.4	322	28.3
445.gobmk	530	19.8	**530**	**19.8**	530	19.8	**503**	**20.8**	504	20.8	503	20.8
456.hmmer	362	25.8	362	25.8	**362**	**25.8**	353	26.4	353	26.4	**353**	**26.4**
458.sjeng	**598**	**20.2**	599	20.2	597	20.3	579	20.9	581	20.8	**579**	**20.9**
462.libquantum	**152**	**136**	152	136	152	136	**152**	**136**	152	136	152	136
464.h264ref	**720**	**30.8**	718	30.8	720	30.7	**673**	**32.9**	673	32.9	671	33.0
471.omnetpp	503	12.4	**505**	**12.4**	506	12.3	**400**	**15.6**	400	15.6	400	15.6
473.astar	**522**	**13.5**	522	13.4	522	13.5	517	13.6	516	13.6	**517**	**13.6**
483.xalancbmk	**278**	**24.8**	277	24.9	279	24.7	260	26.5	**260**	**26.6**	259	26.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1067 MHz.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

429.mcf: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: basepeak = yes

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs

Continued on next page

Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

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For other inquiries, please contact webmaster@spec.org.

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