## Fujitsu PRIMERGY BX924 S2, Intel Xeon X5660, 2.80 GHz

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**Hardware**
- **CPU Name:** Intel Xeon X5660
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz
- **CPU MHz:** 2800
- **FPU:** Integrated
- **CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip, 2 threads/core
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 12 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
- **Disk Subsystem:** 1 x SAS, 300 GB, 10000 RPM
- **Other Hardware:** None

**Software**
- **Operating System:** SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
- **Compiler:** Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
  - Build 20091130 Package ID: l_cproc_p_11.1.064
- **Auto Parallel:** No
- **File System:** ext3
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V8.1

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info@spec.org
http://www.spec.org/
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Base Copies</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used. numactl was used to bind copies to the cores.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page
SPEC CINT2006 Result

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5660, 2.80 GHz

SPECint_rate2006 = 353
SPECint_rate_base2006 = 330

CPU2006 license: 19
Test date: Jul-2010
Test sponsor: Fujitsu
Hardware Availability: Jun-2010
Tested by: Fujitsu
Software Availability: Jan-2010

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xxSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
C++ benchmarks:
-xxSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks (except as noted below):
icpc -m32
473.astar: icpc -m64
Fujitsu
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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -o3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -o3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-o3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
Fujitsu

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Peak Optimization Flags (Continued)

473.astar: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-o3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:

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