SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX100 S2, Intel Core i3-560, 3.33 GHz

$SPECint^®\_rate2006 =$ 66.6

$SPECint\_rate\_base2006 =$ 63.1
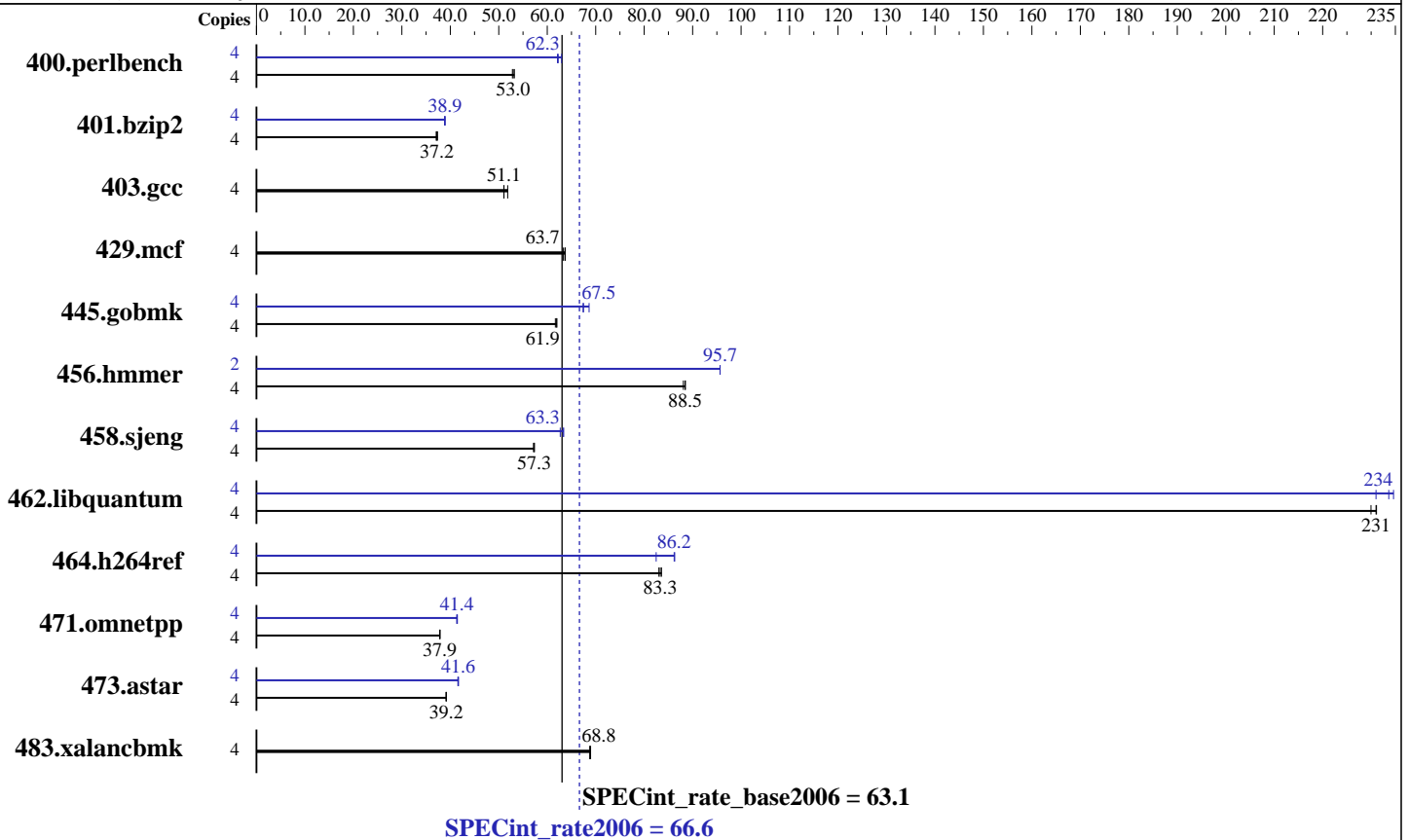
CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010

SPECint_rate_base2006 = 63.1

SPECint_rate2006 = 66.6

Hardware

CPU Name: Intel Core i3-560

CPU Characteristics:

CPU MHz: 3333

FPU: Integrated

CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 4 MB I+D on chip per chip

Other Cache: None

Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)

Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default

Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064

Auto Parallel: No

File System: ext3

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**737**	**53.0**	740	52.8	734	53.2	4	629	62.1	**628**	**62.3**	621	63.0
401.bzip2	4	**1038**	**37.2**	1033	37.4	1041	37.1	4	994	38.8	**993**	**38.9**	991	38.9
403.gcc	4	621	51.8	**630**	**51.1**	631	51.0	4	621	51.8	**630**	**51.1**	631	51.0
429.mcf	4	573	63.7	**573**	**63.7**	576	63.3	4	573	63.7	**573**	**63.7**	576	63.3
445.gobmk	4	**677**	**61.9**	677	62.0	680	61.7	4	623	67.4	611	68.6	**622**	**67.5**
456.hmmer	4	**422**	**88.5**	422	88.5	424	88.1	2	195	95.6	195	95.7	**195**	**95.7**
458.sjeng	4	847	57.2	**844**	**57.3**	844	57.3	4	**765**	**63.3**	763	63.4	772	62.7
462.libquantum	4	360	230	**359**	**231**	359	231	4	359	231	**355**	**234**	353	235
464.h264ref	4	**1062**	**83.3**	1066	83.0	1059	83.6	4	1026	86.3	**1027**	**86.2**	1073	82.5
471.omnetpp	4	**660**	**37.9**	660	37.9	661	37.8	4	605	41.3	**604**	**41.4**	603	41.5
473.astar	4	718	39.1	717	39.2	**717**	**39.2**	4	674	41.7	**674**	**41.6**	676	41.6
483.xalancbmk	4	401	68.8	**401**	**68.8**	400	68.9	4	401	68.8	**401**	**68.8**	400	68.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
     For information about Fujitsu please visit: http://www.fujitsu.com
     Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

C++ benchmarks:
```
     icpc -m32
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: basepeak = yes

         429.mcf: basepeak = yes

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                 -opt-prefetch

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

       473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

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