SPEC® CFP2006 Result

NEC Corporation

Express5800/T120b-E
(Intel Xeon E5503)

SPECfp®2006 = 26.3
SPECfp_base2006 = 25.1

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Aug-2010
Hardware Availability: Jun-2010
Software Availability: Dec-2009

Hardware

CPU Name: Intel Xeon E5503
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)

Continued on next page
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>136</td>
<td>99.9</td>
<td>136</td>
<td>99.9</td>
<td>135</td>
<td>100</td>
<td>136</td>
<td>99.9</td>
<td>137</td>
<td>99.6</td>
<td>136</td>
<td>100</td>
</tr>
<tr>
<td>416.gamess</td>
<td>1307</td>
<td>15.0</td>
<td>1307</td>
<td>15.0</td>
<td>1305</td>
<td>15.0</td>
<td>1229</td>
<td>15.9</td>
<td>1215</td>
<td>16.1</td>
<td>1222</td>
<td>16.0</td>
</tr>
<tr>
<td>433.milc</td>
<td>304</td>
<td>30.2</td>
<td>304</td>
<td>30.2</td>
<td>304</td>
<td>30.2</td>
<td>302</td>
<td>30.4</td>
<td>301</td>
<td>30.5</td>
<td>302</td>
<td>30.4</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>477</td>
<td>19.1</td>
<td>479</td>
<td>19.0</td>
<td>477</td>
<td>19.1</td>
<td>477</td>
<td>19.1</td>
<td>479</td>
<td>19.0</td>
<td>477</td>
<td>19.1</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>185</td>
<td>64.5</td>
<td>185</td>
<td>64.5</td>
<td>187</td>
<td>63.9</td>
<td>157</td>
<td>76.3</td>
<td>157</td>
<td>76.1</td>
<td>158</td>
<td>75.6</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>401</td>
<td>23.5</td>
<td>400</td>
<td>23.5</td>
<td>401</td>
<td>23.5</td>
<td>401</td>
<td>23.5</td>
<td>401</td>
<td>23.5</td>
<td>401</td>
<td>23.5</td>
</tr>
<tr>
<td>444.namd</td>
<td>684</td>
<td>11.7</td>
<td>684</td>
<td>11.7</td>
<td>683</td>
<td>11.7</td>
<td>684</td>
<td>11.7</td>
<td>684</td>
<td>11.7</td>
<td>683</td>
<td>11.7</td>
</tr>
<tr>
<td>447.dealII</td>
<td>513</td>
<td>22.3</td>
<td>505</td>
<td>22.6</td>
<td>513</td>
<td>22.3</td>
<td>487</td>
<td>23.5</td>
<td>480</td>
<td>23.8</td>
<td>487</td>
<td>23.5</td>
</tr>
<tr>
<td>450.soplex</td>
<td>522</td>
<td>16.0</td>
<td>522</td>
<td>16.0</td>
<td>522</td>
<td>16.0</td>
<td>515</td>
<td>16.2</td>
<td>520</td>
<td>16.0</td>
<td>519</td>
<td>16.1</td>
</tr>
<tr>
<td>453.povray</td>
<td>302</td>
<td>17.6</td>
<td>302</td>
<td>17.6</td>
<td>301</td>
<td>17.7</td>
<td>234</td>
<td>22.7</td>
<td>237</td>
<td>22.4</td>
<td>233</td>
<td>22.8</td>
</tr>
<tr>
<td>454.calculix</td>
<td>476</td>
<td>17.3</td>
<td>476</td>
<td>17.3</td>
<td>476</td>
<td>17.3</td>
<td>456</td>
<td>18.1</td>
<td>463</td>
<td>17.8</td>
<td>463</td>
<td>17.8</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>254</td>
<td>41.7</td>
<td>254</td>
<td>41.7</td>
<td>255</td>
<td>41.6</td>
<td>247</td>
<td>43.0</td>
<td>247</td>
<td>43.0</td>
<td>246</td>
<td>43.1</td>
</tr>
<tr>
<td>465.tonto</td>
<td>570</td>
<td>17.3</td>
<td>570</td>
<td>17.3</td>
<td>570</td>
<td>17.3</td>
<td>512</td>
<td>19.2</td>
<td>512</td>
<td>19.2</td>
<td>506</td>
<td>19.4</td>
</tr>
<tr>
<td>470.lbm</td>
<td>189</td>
<td>72.8</td>
<td>187</td>
<td>73.3</td>
<td>187</td>
<td>73.3</td>
<td>189</td>
<td>72.8</td>
<td>187</td>
<td>73.3</td>
<td>187</td>
<td>73.3</td>
</tr>
<tr>
<td>481.wrf</td>
<td>405</td>
<td>27.6</td>
<td>405</td>
<td>27.6</td>
<td>405</td>
<td>27.6</td>
<td>405</td>
<td>27.6</td>
<td>405</td>
<td>27.6</td>
<td>405</td>
<td>27.6</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>1029</td>
<td>18.9</td>
<td>1035</td>
<td>18.8</td>
<td>1049</td>
<td>18.6</td>
<td>1029</td>
<td>18.9</td>
<td>1035</td>
<td>18.8</td>
<td>1049</td>
<td>18.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

Platform Notes

BIOS setting:
NUMA configuration : Disabled

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
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Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
  -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
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Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: basepeak = yes

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

You can also download the XML flags source by saving the following link:
## NEC Corporation

<table>
<thead>
<tr>
<th>Spec Model</th>
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<th>Test date</th>
</tr>
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<tr>
<td>NEC Express5800/T120b-E (Intel Xeon E5503)</td>
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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.


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