SPEC® CINT2006 Result

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Hewlett-Packard Company

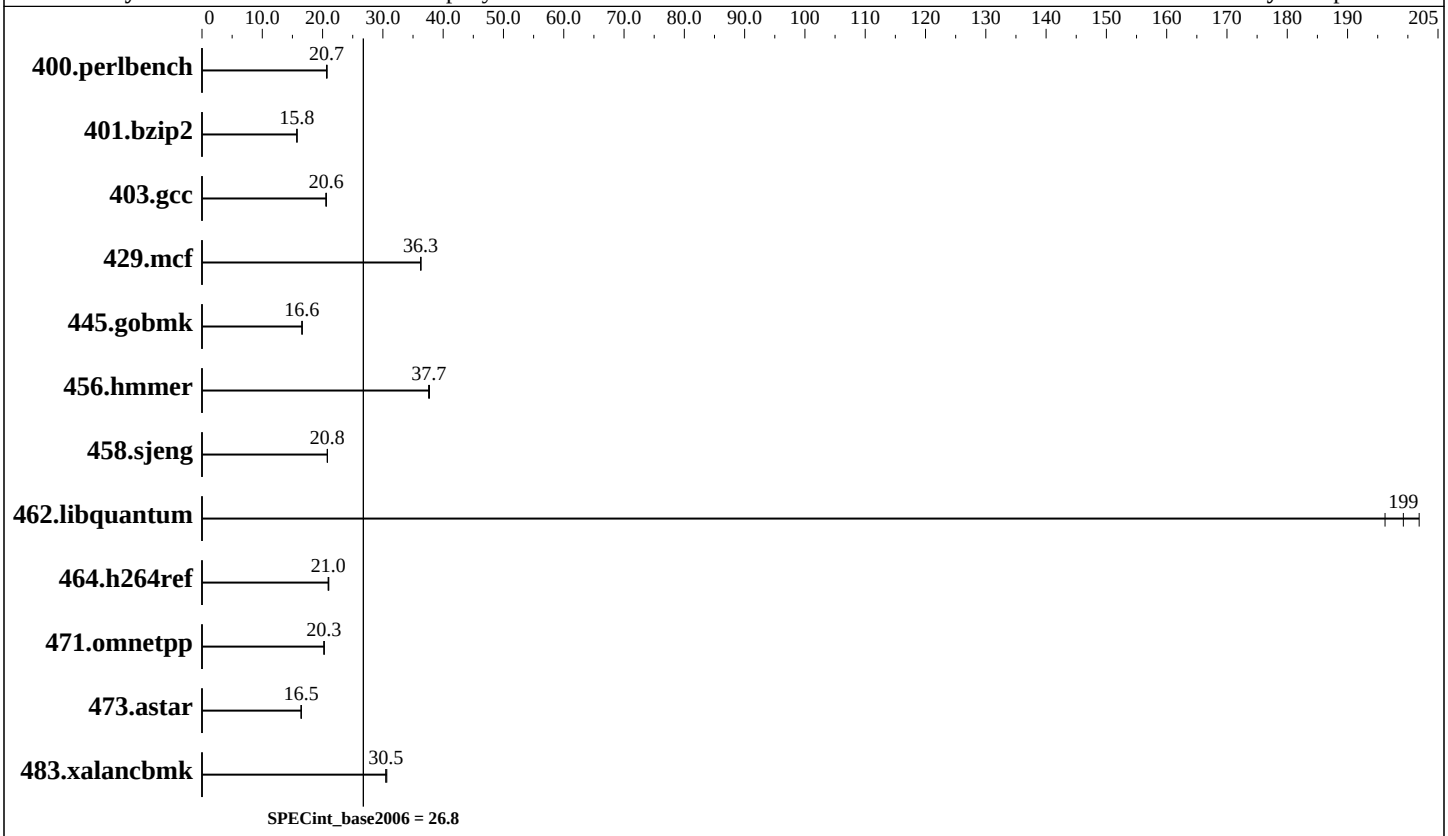
ProLiant DL180 G6 (2.53 GHz, Intel Xeon E5630)

SPECint®2006 = Not Run

SPECint_base2006 = 26.8

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Nov-2010
Hardware Availability: Mar-2010
Software Availability: Apr-2010



Hardware

CPU Name:	Intel Xeon E5630
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2533
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	4 x 146 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5 Kernel 2.6.18-194.el5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**472**	**20.7**	473	20.7	471	20.8						
401.bzip2	612	15.8	614	15.7	**612**	**15.8**						
403.gcc	**391**	**20.6**	392	20.5	390	20.6						
429.mcf	**251**	**36.3**	251	36.3	251	36.3						
445.gobmk	631	16.6	634	16.6	**632**	**16.6**						
456.hmmer	247	37.7	**248**	**37.7**	248	37.6						
458.sjeng	582	20.8	582	20.8	**582**	**20.8**						
462.libquantum	106	196	103	202	**104**	**199**						
464.h264ref	1057	20.9	1052	21.0	**1056**	**21.0**						
471.omnetpp	308	20.3	**309**	**20.3**	309	20.2						
473.astar	427	16.4	**427**	**16.5**	426	16.5						
483.xalancbmk	**226**	**30.5**	225	30.7	226	30.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Efficiency set to Performance

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.html
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101221.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.xml
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101221.xml

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For other inquiries, please contact webmaster@spec.org.

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